

# Current Rectification and Photo-Responsive Current Achieved through Interfacial Facet Control of Cu<sub>2</sub>O–Si Wafer Heterojunctions

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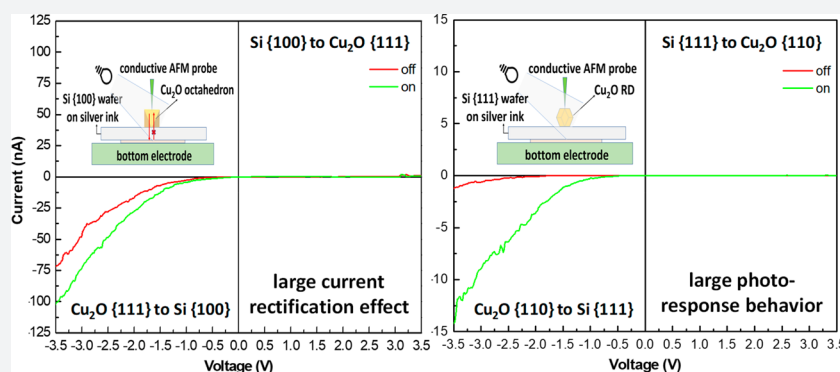
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**ABSTRACT:** Conductive atomic force microscopy (C-AFM) was employed to perform conductivity measurements on a facet-specific Cu<sub>2</sub>O cube, octahedron, and rhombic dodecahedron and intrinsic Si {100}, {111}, and {110} wafers. Similar *I–V* curves to those recorded previously using a nanomanipulator were obtained with the exception of high conductivity for the Si {110} wafer. Next, *I–V* curves of different Cu<sub>2</sub>O–Si heterostructures were evaluated. Among the nine possible arrangements, Cu<sub>2</sub>O octahedron/Si {100} wafer and Cu<sub>2</sub>O octahedron/Si {110} wafer combinations show good current rectification behaviors. Under white light illumination, Cu<sub>2</sub>O cube/Si {110} wafer and Cu<sub>2</sub>O rhombic dodecahedron/Si {111} wafer combinations exhibit the largest degrees of photocurrent, so such interfacial plane-controlled semiconductor heterojunctions with light sensitivity can be applied to make photodetectors. Adjusted band diagrams are presented highlighting different interfacial band bending situations to facilitate or inhibit current flow for different Cu<sub>2</sub>O–Si junctions. More importantly, the observation of clear current-rectifying effects produced at the semiconductor heterojunctions with properly selected contacting faces or planes implies that novel field-effect transistors (FETs) can be fabricated using this design strategy, which should integrate well with current chip manufacturing processes.

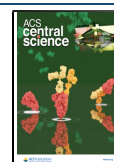
## INTRODUCTION

Various semiconductor materials including Cu<sub>2</sub>O, Ag<sub>2</sub>O, TiO<sub>2</sub>, PbS, and SrTiO<sub>3</sub> crystals have displayed strongly facet-dependent electrical conductivity properties.<sup>1–7</sup> Ag<sub>3</sub>PO<sub>4</sub> crystals, however, do not possess clear electrical facet effects.<sup>8</sup> Moreover, intrinsic, or undoped, Si, Ge, and GaAs wafers also exhibit large facet dependence in their electrical conductivity responses.<sup>9–11</sup> In the case of Cu<sub>2</sub>O crystals, the {111}-bound octahedron is highly conductive, but the {110}-terminated rhombic dodecahedron is actually insulating.<sup>1</sup> The {100} faces of a cube is moderately conductive. For Si {100} and {110} wafers, they exhibit normal semiconductor conductivity properties with very low current at applied voltages below 3 V, while Si {111} wafers are much more conductive with current rising within 1 V.<sup>9</sup> Current-rectifying *I–V* curves resembling those of *p–n* diode junctions can readily be obtained by making electrical connections to a conductive face and a poorly conductive face of the same crystal or wafer. This field-effect transistor (FET) design can be considered as a first-generation approach. To explain the emergence of the

observed electrical facet effects, density functional theory (DFT) calculations have revealed the presence of an ultrathin surface layer (~1 nm or less in thickness) with dissimilar band structures for various surface planes, which should lead to different degrees of surface band bending and facilitate or prevent charge transport across a particular crystal face.<sup>1,3,6,12–15</sup> Furthermore, bond length and bond directions, as well as frontier orbital electron energy distribution, within the thin surface layer can show deviations or variations for the highly conductive crystal faces such as the Si (111) and Cu<sub>2</sub>O (111) planes, which should yield changes in the surface band structure.<sup>12,13</sup> These DFT results provide a more physical

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picture of the thin surface layer. Interestingly, the predicted structural deviations in the surface lattice planes may be observable, as seen in the high-resolution transmission electron microscopy (HR-TEM) images of SrTiO<sub>3</sub> crystals with slight atomic position deviations and noticeable peak shifts in the X-ray diffraction (XRD) patterns of polyhedral Cu<sub>2</sub>O crystals.<sup>7,16</sup> To gain further insights, the most conductive Si and Ge {111} wafers were found to have the lowest surface trap state population and the shortest carrier lifetime, matching with their best electrical conductivity behavior.<sup>17,18</sup> GaAs wafers, however, do not show such surface trap state and carrier lifetime correlation to their conductivity properties.<sup>19</sup>

In addition to surface-related electrical conductivity properties, Cu<sub>2</sub>O, Ag<sub>2</sub>O, Ag<sub>3</sub>PO<sub>4</sub>, SrTiO<sub>3</sub>, and other polyhedral crystals also present strong photocatalytic facet effects.<sup>8,20–24</sup> For example, Cu<sub>2</sub>O rhombic dodecahedra are most active toward methyl orange photodegradation, but Cu<sub>2</sub>O cubes are simply photocatalytically inert. The idea of a thin surface layer with tunable degrees of band bending was adopted to understand the strong facet dependence in photocatalytic activity. Remarkably, when other semiconductor particles including ZnO, ZnS, CdS, and Ag<sub>3</sub>PO<sub>4</sub> are deposited on Cu<sub>2</sub>O polyhedra, in many cases, photocatalytic activity suppression to the degree of complete deactivation can happen instead of the expected activity enhancement on the basis of band energy alignment analysis for the semiconductor heterojunctions.<sup>25–27</sup> We can understand these observations because both favorable and mismatched interfacial band bending can happen depending on the contacting lattice planes between the two semiconductor materials. Thus, one can extend the surface layer idea to semiconductor interfaces. Multiple interfacial combinations are possible. In all these heterostructure cases, Cu<sub>2</sub>O cubes remain photocatalytically inert, but Ag<sub>2</sub>S-decorated Cu<sub>2</sub>O cubes show surprisingly good photocatalytic activity, illustrating the unpredictable nature of semiconductor heterojunctions.<sup>28</sup>

Since one knows diverse photocatalytic outcomes are possible for semiconductor heterojunctions, it is natural to think electrical properties of semiconductor heterojunctions with facet control can produce the same current-rectifying effect. The placement of a Cu<sub>2</sub>O cube, octahedron, and rhombic dodecahedron on Si {100}, {111}, and {110} wafers gives 9 combinations to investigate. Moreover, if the conductivity measurements can be carried out using a conductive atomic force microscopy (C-AFM) probe, instead of a conventional 4-probe nanomanipulator installed in a scanning electron microscopy (SEM) chamber, the photoconductivity effect can be performed with additional light illumination. Here, we have first performed electrical conductivity measurements on Cu<sub>2</sub>O particles and intrinsic silicon wafers exposing different crystal surfaces, followed by conductivity measurements on the nine combinations of Cu<sub>2</sub>O–Si wafer heterostructures. Remarkably, some combinations yield useful current-rectifying properties, while others give notable photocurrent enhancement. The results demonstrate that semiconductor heterojunctions with appropriate interfacial plane control can be explored in the fabrication of transistors and photodetectors.

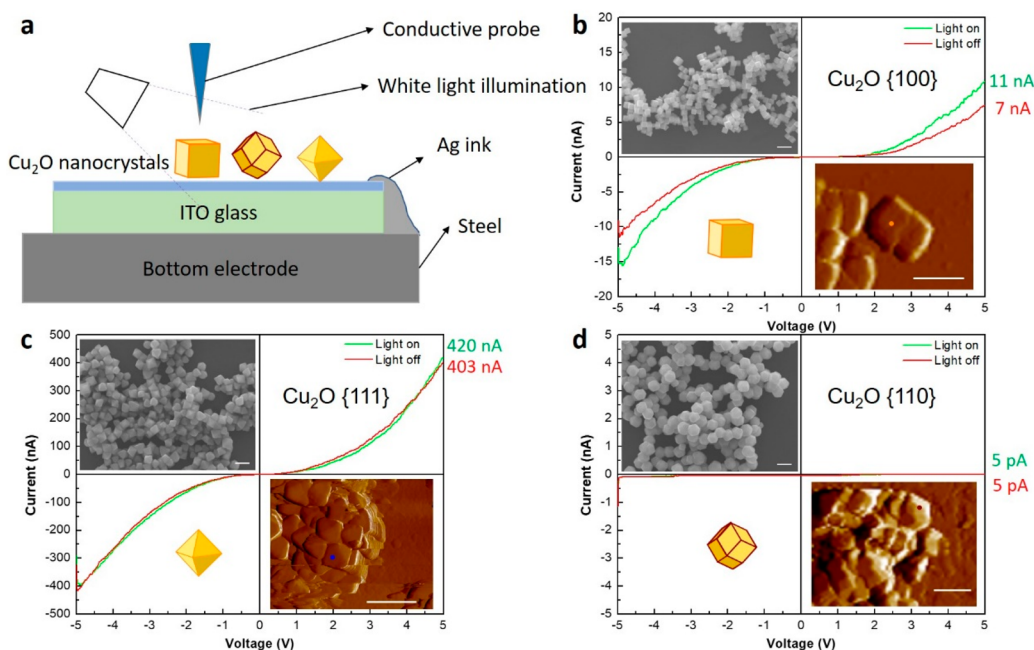
## RESULTS AND DISCUSSION

The conductive AFM is a powerful tool for probing electrical properties of nanoscale structures including quantum dots,<sup>29,30</sup> nanorods,<sup>31</sup> and halide perovskites.<sup>32</sup> By using a commercial

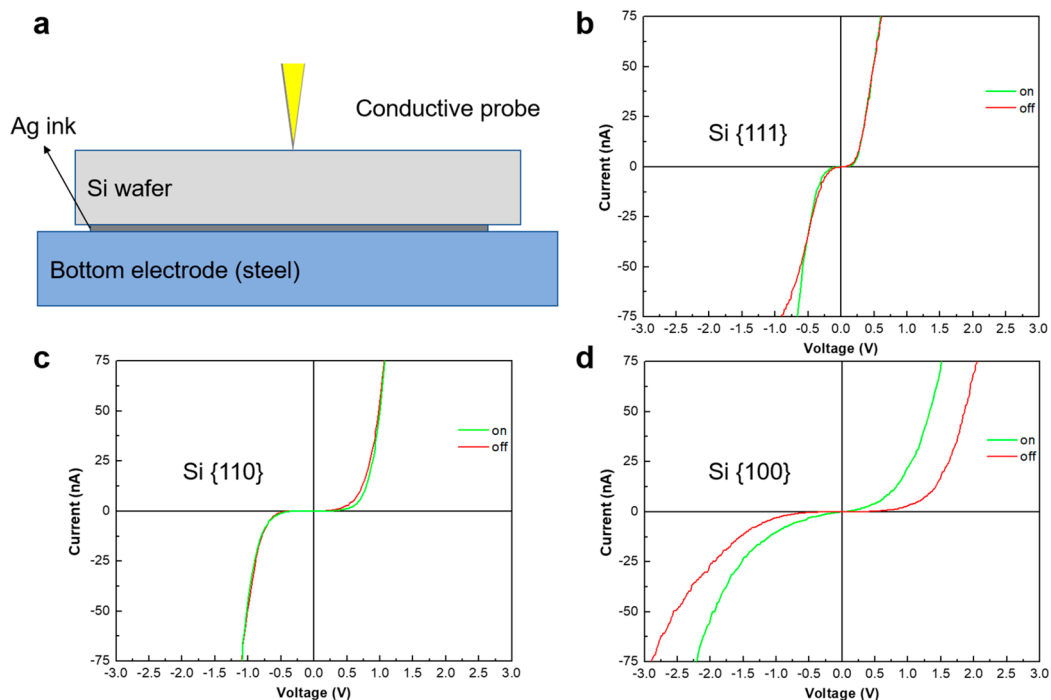
conductive probe with a typical tip radius of 20 nm as a movable electrode, conductive AFM can provide nanoscale surface structure and electrical behavior. However, the poor positional stability when the probe makes contact with the particle surface can be a problem. With adjustable tip loading force, the peak force (PF) mode can improve contact stability. Equipped with the tunneling module, the tunneling AFM (TUNA) is an advanced C-AFM technique with tunable current sensitivity in the range from fA to  $\mu$ A for *I*–*V* measurements. The combination of the PF mode and TUNA, PF-TUNA, is an ideal method for measuring the conductivity of individual nanostructures with high electrical resistance. Here, conductivity measurements were accomplished by using PF-TUNA.

Initially, Cu<sub>2</sub>O cubes, octahedra, and rhombic dodecahedra were synthesized following our reported procedures (see the Supporting Information for the experimental detail and Table S1 for a summary of the reagent amounts used).<sup>20,33</sup> SEM images of the synthesized Cu<sub>2</sub>O crystals are provided in Figure S1. The particles all possess high shape and size uniformity with sharp faces. The average sizes of the Cu<sub>2</sub>O cubes, octahedra, and rhombic dodecahedra are approximately 330, 270, and 265 nm, respectively. For octahedra, the particle size refers to the opposite corner length. For rhombic dodecahedra, opposite face lengths were measured. Figure S1 also shows their XRD patterns, matching exactly to the standard pattern of Cu<sub>2</sub>O. Again slight peak shifts can be identified. With respect to the peak positions for rhombic dodecahedra, diffraction peaks for octahedra and cubes are shifted progressively to higher  $2\theta$  angles. This phenomenon may arise from the presence of the thin surface layer with bond length deviations. Figure S2 gives XRD patterns of the front and back sides of Si wafers exposing {100}, {110}, and {111} surfaces, confirming their correct crystal orientations.

Since band diagrams of the Cu<sub>2</sub>O and Si heterojunctions will be presented to explain the recorded electrical responses, it is useful to first construct band diagrams of different Cu<sub>2</sub>O crystals. Figure S3 offers diffuse reflectance spectra of the prepared Cu<sub>2</sub>O crystals and the corresponding Tauc plot for band gap determination. The synthesized Cu<sub>2</sub>O cubes, octahedra, and rhombic dodecahedra have band gaps of 1.61, 2.17, and 1.84 eV, respectively, showing again that band gap values are still tunable for very large Cu<sub>2</sub>O crystals, and light absorption by semiconductors includes both bulk and surface components.<sup>34</sup> Using the band gap values and ultraviolet photoelectron spectroscopy (UPS) results previously obtained for Cu<sub>2</sub>O crystals synthesized by the same method for energies of valence band maximum (VBM), Fermi level, and work function, the band diagrams of the prepared Cu<sub>2</sub>O cubes, octahedra, and rhombic dodecahedra are presented in Figure S4.<sup>20</sup> When the vacuum level is equalized, there are just small valence band energy differences among the three particle shapes and a small energy difference in the conduction band between octahedra and rhombic dodecahedra. Hence, the conventional band diagrams without regard for the surface effects to band energies cannot explain why Cu<sub>2</sub>O octahedra and rhombic dodecahedra show very different electrical properties and why Cu<sub>2</sub>O cubes should be photocatalytically inert, while rhombic dodecahedra are far more photocatalytically active than octahedra. Clearly, significant surface band bending should be introduced to address experimental observations.



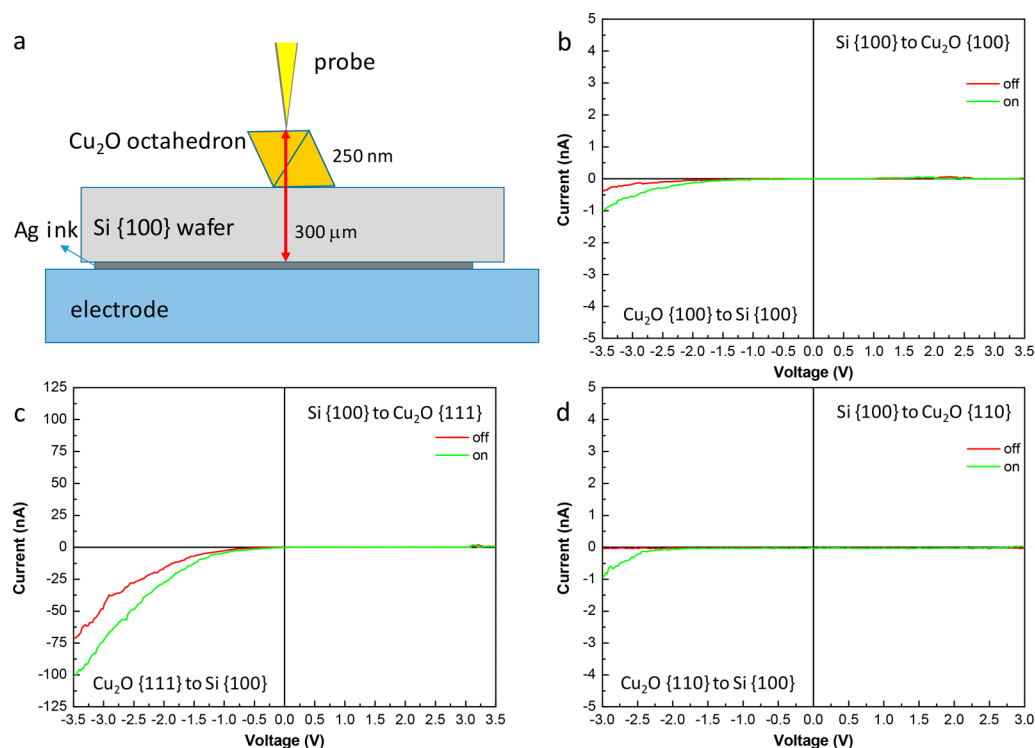
**Figure 1.** (a) Illustration of the photoresponsive conductivity measurements. The light blue layer represents the deposited Cu<sub>2</sub>O crystals. (b–d) Particle SEM and AFM images and the recorded *I*–*V* curves for Cu<sub>2</sub>O (b) cubes, (c) octahedra, and (d) rhombic dodecahedra with and without light illumination. The dots indicate the measured spots. All scale bars are equal to 500 nm.



**Figure 2.** (a) Illustration of the conductivity measurements for Si wafers. (b–d) The recorded *I*–*V* curves for Si (b) {111}, (c) {110}, and (d) {100} wafers with and without light illumination.

To ensure that proper electrical connections have been made and the obtained *I*–*V* curves are correct, it is necessary to perform conductivity measurements on individual Cu<sub>2</sub>O crystals first and see if the recorded *I*–*V* curves agree with previous measurements using a nanomanipulator with tungsten probes installed in a SEM chamber. Figure 1 provides a schematic drawing of the sample configuration for electrical conductivity measurements, SEM and AFM images of different Cu<sub>2</sub>O crystals, and the recorded *I*–*V* curves with and without

LED light illumination. The drawing illustrates that Cu<sub>2</sub>O crystals are sandwiched between a conductive probe and an ITO glass, which is electrically connected to the bottom steel electrode using silver ink. The dots in the AFM images indicate the AFM probe contacts for the measured *I*–*V* curves. The AFM images reveal that the Cu<sub>2</sub>O crystals can be aggregated, so we tried to select a recognizable individual particle with upright orientation as the probe contact points. Each *I*–*V* curve represents an average of three continuous measurements.



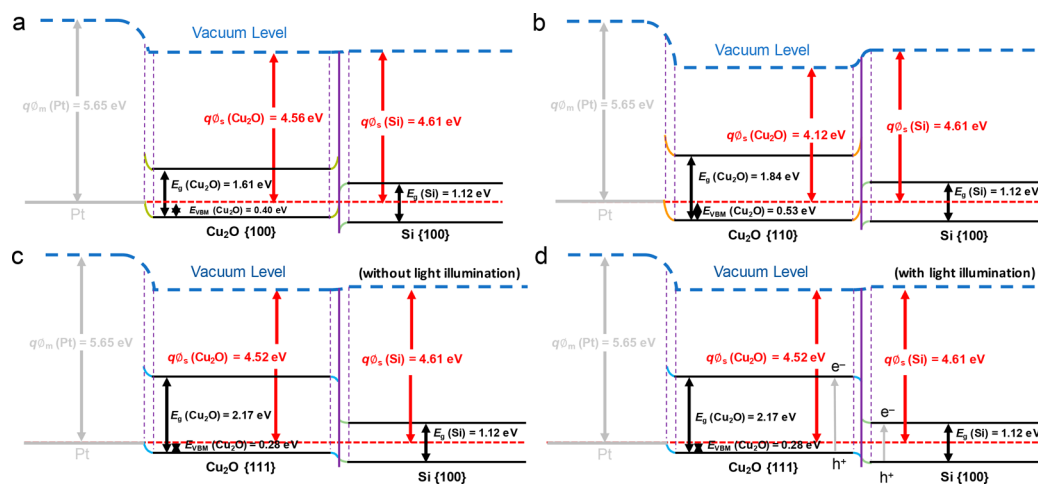
**Figure 3.** (a) Illustration of using a conductive AFM probe for conductivity measurements of the  $\text{Cu}_2\text{O}$  octahedron/Si {100} wafer heterojunction. (b–d) Measured  $I$ – $V$  curves for the (b)  $\text{Cu}_2\text{O}$  cube/Si {100} wafer, (c)  $\text{Cu}_2\text{O}$  octahedron/Si {100} wafer, and (d)  $\text{Cu}_2\text{O}$  rhombic dodecahedron/Si {100} wafer combinations. Current directions are indicated.

In agreement with previous observations, a  $\text{Cu}_2\text{O}$  octahedron is highly conductive with the current reaching 403 nA at 5 V, while a  $\text{Cu}_2\text{O}$  cube only gives a current of 7 nA at 5 V.<sup>1</sup> This is a 57-fold current difference, demonstrating the power of crystal facets to charge transport. The slight asymmetry in the  $I$ – $V$  curves should be related to the considerable difference in the two electrode contact area and the electrode composition. A  $\text{Cu}_2\text{O}$  rhombic dodecahedron remains insulating. Thus, the AFM conductivity measurements give similar results to those obtained using tungsten probes. Under light irradiation, the current increases slightly to 420 nA for the octahedron and 11 nA for the cube, but there is no change for the rhombic dodecahedron. The intense light of sufficient energies promotes more electrons to the conduction band and creates holes in the valence band, so photocurrent should generally be improved. The results show that photocurrent enhancement is more pronounced for the less conductive  $\text{Cu}_2\text{O}$  crystals.

Figure 2 is a drawing of the sample configuration for silicon wafer conductivity measurements and the recorded  $I$ – $V$  curves with and without light illumination. Each curve represents an average of three continuous measurements. Consistent with the previous observation, the Si {111} wafer is considerably more conductive than the Si {100} wafer.<sup>9</sup> However, the Si {100} wafer actually has fairly good conductivity, and current rises at a lower applied voltage for the Si {110} wafer than for the Si {100} wafer, meaning the Si {110} wafer is more conductive than the Si {100} wafer. Previously, the Si {110} wafer was found to be the least conductive.<sup>9</sup> It is unclear why such a discrepancy exists. A possibility is that the electrode connections are different with tungsten probes on the same side of the wafer. Nevertheless, the new conductivity results should be considered when evaluating the conductivity behaviors of the  $\text{Cu}_2\text{O}$ –Si heterostructures. Upon light

illumination, again, the comparatively less conductive Si {100} wafer displays the largest degree of photocurrent enhancement. The adjusted band diagram with silicon wafers connecting to a Pt electrode is shown in Figure S5. Although intrinsic Si wafers were used, the majority carrier should still be the electrons. Because of the large work function of Pt, a Schottky contact is formed with upward band bending at the interface.<sup>35</sup> The Si {111} wafer and the Pt electrode have the smallest energy difference to indicate its best conductivity. Upon light illumination, a smaller degree of upward band bending can be drawn for the Si {100} wafer to suggest improved conductivity.

Figure 3 depicts the sample configuration of a  $\text{Cu}_2\text{O}$  octahedron on top of a Si {100} wafer for conductivity measurements and the recorded  $I$ – $V$  curves of different  $\text{Cu}_2\text{O}$  crystal/Si {100} wafer combinations. Figure S6 provides selected  $I$ – $V$  curves for five or more continuous measurements of the  $\text{Cu}_2\text{O}$  octahedron/Si {100} wafer combination to see the current reproducibility. In the positive voltage side of the  $I$ – $V$  curve, current flows from the Si wafer to the  $\text{Cu}_2\text{O}$  crystal, while in the negative voltage region, the current direction is from the  $\text{Cu}_2\text{O}$  crystal to the Si wafer through the electrical connection. In the case of a  $\text{Cu}_2\text{O}$  cube on a Si {100} wafer (Figure 3b), slight current-rectifying  $I$ – $V$  curves are obtained, and light illumination gives a photocurrent; however, this combination is not useful because of the very small current achieved. Remarkably, the combination of {111}-bound  $\text{Cu}_2\text{O}$  octahedron and Si {100} wafer gives excellent and clean current-rectifying responses with current reaching 75 nA at 3.5 V in the negative voltage region (or in the direction from Pt probe to the bottom electrode) and zero current in the positive voltage region or the opposite direction. Under light irradiation, photocurrent reaching 100 nA was achieved.



**Figure 4.** Band diagrams for a Si {100} wafer in contact with (a) a  $\text{Cu}_2\text{O}$  cube, (b) a rhombic dodecahedron, and an octahedron (c) without and (d) under white LED illumination.

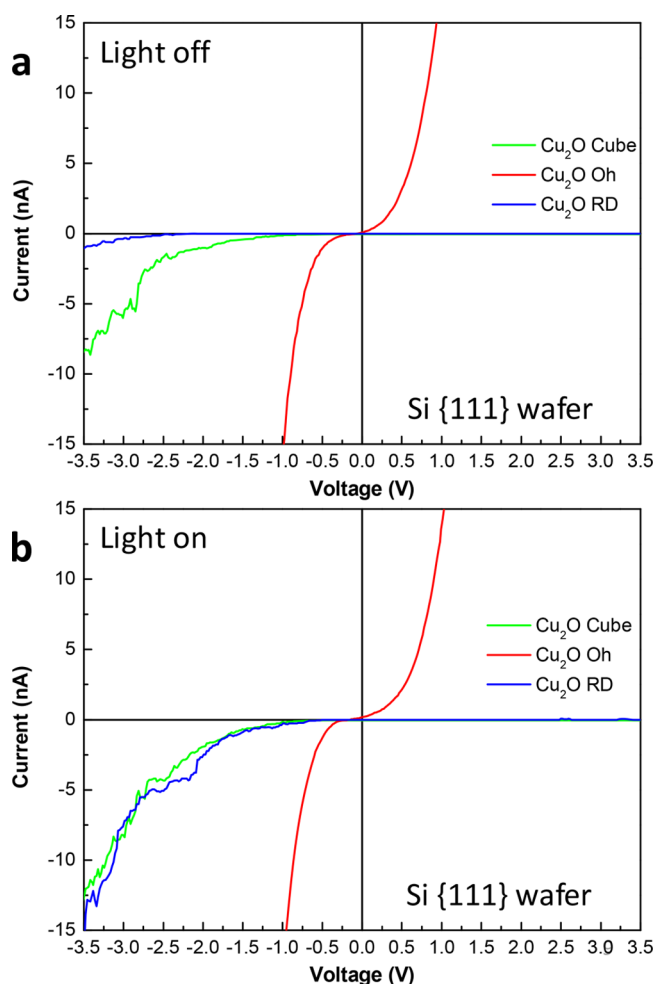
Considering current has risen sharply beyond 1 V for the Si {100} wafer as seen in Figure 2, this current-rectifying  $\text{Cu}_2\text{O}$  {111}/Si {100} combination cannot be predicted from the electrical response of each component. It also suggests that the interfacial band bending situation can change significantly, even when there is no chemical bond formed at the junction. Previously, current-rectifying  $I$ – $V$  curves have been observed for  $\text{Cu}_2\text{O}$  cubes and octahedra deposited on 1,8-nonylamine-treated amorphous silicon substrate, so the silicon substrate does not have a well-defined crystal plane.<sup>36–40</sup> Next, the combination of a  $\text{Cu}_2\text{O}$  rhombic dodecahedron on a Si {100} wafer did not produce any current (Figure 3d). A small current of 1 nA was observed at 3 V under light illumination. This result seems reasonable, since a  $\text{Cu}_2\text{O}$  rhombic dodecahedron is electrically insulating. However, as we will see later, one cannot safely assume that any  $\text{Cu}_2\text{O}$  rhombic dodecahedron/Si wafer combination gives no current.

Figure 4 shows the adjusted band diagrams of the three  $\text{Cu}_2\text{O}$ /Si {100} wafer combinations to understand the observed electrical behaviors. The Fermi levels for the metal and semiconductors are equalized, but under applied voltages, the Fermi levels can change. Although  $\text{Cu}_2\text{O}$  is a p-type semiconductor, the Si wafer is undoped. Because intrinsic Si wafer was used, Fermi level is drawn midway between the valence band and the conduction band. Both electrons and holes can be considered as charge carriers for the band diagrams. In the case of a  $\text{Cu}_2\text{O}$  cube with the Si {100} wafer, there is nearly no current. At the Pt– $\text{Cu}_2\text{O}$  junction, upward band bending is drawn to reflect the larger work function of Pt relative to that of  $\text{Cu}_2\text{O}$  (Figure 4a). At the heterojunction, the  $\text{Cu}_2\text{O}$  {100} face bends upward, and the Si {100} surface bends downward to represent a significant energy barrier to charge migration across this interface. For the  $\text{Cu}_2\text{O}$  rhombic dodecahedron case (Figure 4b), a mismatched band bending situation at the semiconductor junction (upward for  $\text{Cu}_2\text{O}$  and downward for Si) is drawn to indicate the overall electrically insulating behavior of this heterostructure combination. Under light illumination, current increases negligibly for both the cube and rhombic dodecahedron cases, so the interfacial band bending should not change. In contrast, current flows in the direction from a  $\text{Cu}_2\text{O}$  octahedron to the Si {100} wafer (Figure 4c). At the interface, the  $\text{Cu}_2\text{O}$  side should bend downward and the Si {100} side bends upward to represent

facile electron migration in the direction from  $\text{Cu}_2\text{O}$  to Si. In the reverse current direction, or under the application of a positive voltage, the conduction band energy of  $\text{Cu}_2\text{O}$  is at a much higher level than that of Si, so even favorable interfacial band bending would not enable current flow in the direction from the Si {100} wafer to the  $\text{Cu}_2\text{O}$  octahedron. The current-rectifying effect then results. Under light illumination, more photoexcited electrons and holes are generated to produce a current increase (Figure 4d).

Figure 5a displays the recorded  $I$ – $V$  curves for the Si {111} wafer in contact with a  $\text{Cu}_2\text{O}$  cube, octahedron, and rhombic dodecahedron. Current-rectifying effects are observed for the cube case with 8 nA at 3.5 V in the direction from  $\text{Cu}_2\text{O}$  to the Si wafer. Current increases moderately to 12.5 V under LED light illumination. Selected  $I$ – $V$  curves for five or more continuous measurements of this combination are available in Figure S7. For  $\text{Cu}_2\text{O}$  rhombic dodecahedron, a very small current of just 2 nA was observed for the current direction from the Pt probe to the bottom electrode. Remarkably, current rises to 13–14 nA under light illumination. Figure S8 offers  $I$ – $V$  curves for three measurements of this combination with and without shining light. Thus, the  $\text{Cu}_2\text{O}$  {110}/Si {111} combination can potentially be applied for photon detection with its high light sensitivity. This design employing semiconductor heterojunctions for photodetector application should be a new approach. The considerable photocurrent generation is unexpected, as  $\text{Cu}_2\text{O}$  rhombic dodecahedra are not electrically conductive. This example further demonstrates that semiconductor heterojunctions with interfacial plane control can bring unusual and exciting phenomena, as have often been observed in  $\text{Cu}_2\text{O}$ -based semiconductor heterostructures for photocatalytic reactions.<sup>27</sup> For the  $\text{Cu}_2\text{O}$  octahedron/Si {111} wafer combination, a very high current has been recorded for both directions, considering both semiconductors are highly conductive. Figure S9 shows the current can reach 200 nA below 2.5 V and below 2 V with the LED light on.

Figure 6 presents band diagrams for a Si {111} wafer in contact with different  $\text{Cu}_2\text{O}$  crystals to understand their conductivity behaviors. A favorable interfacial band bending is drawn for the  $\text{Cu}_2\text{O}$  cube/Si {111} wafer combination to correspond to its current-rectifying property (Figure 6a). Upon light illumination, photocurrent increases just slightly, so the



**Figure 5.** *I*-*V* curves recorded for the combinations of a Si {111} wafer with Cu<sub>2</sub>O cube, octahedron, and rhombic dodecahedron (a) without and (b) with LED light illumination.

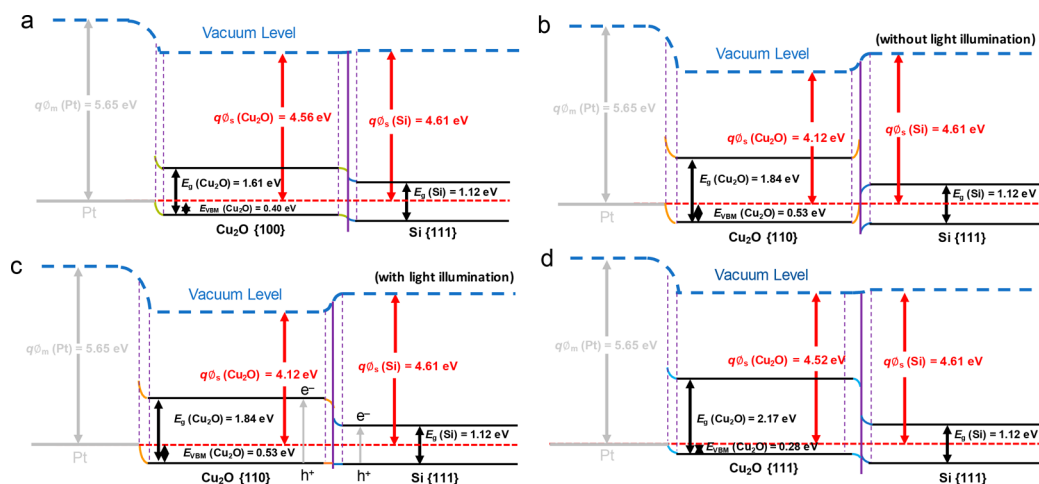
interfacial band bending should look the same. An unmatched interfacial band bending depicts the nearly nonconducting property for the Cu<sub>2</sub>O rhombic dodecahedron/Si {111} wafer combination (Figure 6b). Under LED light illumination, however, a large current increase was observed, so the

interfacial band bending becomes favorable (Figure 6c). For the very high conductivity of the Cu<sub>2</sub>O octahedron/Si {111} wafer combination, the interfacial band bending should be favorable to charge transport with and without light illumination.

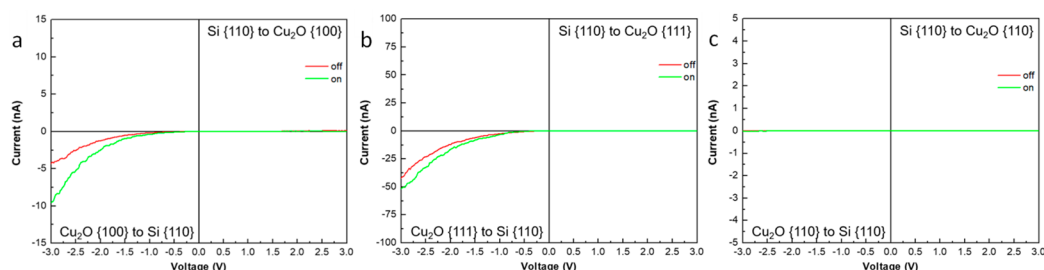
Figure 7 summarizes the *I*-*V* curves collected by electrically connecting a Cu<sub>2</sub>O cube, octahedron, or rhombic dodecahedron to a Si {110} wafer with and without LED light illumination. Current-rectifying *I*-*V* curves were recorded for the Cu<sub>2</sub>O cube/Si {110} wafer combination. Additional *I*-*V* curves for this combination are provided in Figure S10. Current is about 4 nA at -3 V in the direction from Cu<sub>2</sub>O to Si, while the other direction is not conductive. Under light irradiation, current increases to 9 nA, so this combination is also photoresponsive. The combination of a Cu<sub>2</sub>O octahedron/Si {110} wafer gives similar current-rectifying *I*-*V* curves, but a much larger current of 42 nA was achieved at -3 V, attributed to the higher conductivity of a Cu<sub>2</sub>O octahedron. Photoenhanced current goes to 50 nA at -3 V. For the final Cu<sub>2</sub>O rhombic dodecahedron/Si {110} wafer combination, there is no current even under light illumination, attributed partly to the insulating nature of a Cu<sub>2</sub>O rhombic dodecahedron.

The adjusted band diagrams for the different Cu<sub>2</sub>O crystal/Si {110} wafer combinations are shown in Figure S11. A favorable interfacial band bending situation is drawn for the Cu<sub>2</sub>O cube/Si {110} wafer combination for its current rectification effect. Under light illumination, more mobile charge carriers are available to produce a larger current. For the complete lack of current, a mismatched interfacial band bending is presented for the Cu<sub>2</sub>O rhombic dodecahedron/Si {110} wafer combination. Finally, the Cu<sub>2</sub>O octahedron/Si {110} wafer connection gives a matched interfacial band bending to indicate its current-rectifying behavior.

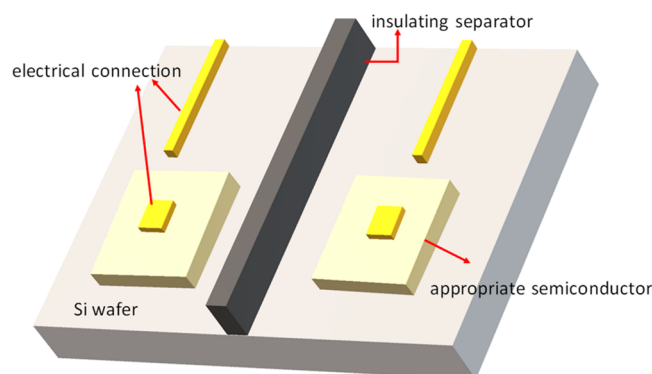
Utilizing the interfacial plane control to achieve current rectification is a new and exciting FET fabrication concept. Figure 8 provides a simple drawing of the FET design. By testing various interfacial combinations between the deposited top semiconductor and the bottom Si or other wafers, it may be possible to find a good semiconductor that meets various electrical response and fabrication requirements. It is envisioned that the top semiconductor can be produced



**Figure 6.** Band diagrams for a Si {111} wafer in contact with (a) a Cu<sub>2</sub>O cube, a Cu<sub>2</sub>O rhombic dodecahedron (b) without and (c) with LED light illumination, and (d) a Cu<sub>2</sub>O octahedron.



**Figure 7.** Measured  $I$ – $V$  curves for the combinations of a Si {110} wafer and a  $\text{Cu}_2\text{O}$  (a) cube, (b) octahedron, and (c) rhombic dodecahedron. The current directions are indicated.



**Figure 8.** Conceptual design for FETs utilizing the proper combination of a top semiconductor and a Si wafer (or other wafers) with suitable contacting faces or planes to control current flow. The top semiconductor can form through patterned liquid or vapor deposition and can be very thin and small in size. Electrical connections are made to the top semiconductor and the wafer. Current is generated in only one direction under an applied voltage. Some insulating separator may be needed to avoid current crossover.

through a solution or vapor deposition process similar to the patterned photolithography procedure that multiple FETs are made simultaneously on the wafer. The FETs can be made very small and thin with positional control, so they are easier to make than using 2-dimensional materials such as graphene. For example, the reagent-mixed solution can be sprayed onto a patterned substrate to form  $\text{Cu}_2\text{O}$  crystals. Electrical connections are then made to the top semiconductor and bottom Si substrate. Insulating separators may be needed to avoid current crossover. The electrical connection to the top semiconductor at a different height to the substrate needs to be addressed. Previously, it has been proposed that electrical connections to the top and side faces of a Si or Ge wafer exposing different surfaces can be a new FET design.<sup>9,10</sup> We can consider semiconductor heterojunctions as the second-generation FET design, utilizing the current control effects generated at the semiconductor interfaces, which is easier to fabricate. Here, p and n regions are not required. Impurity doping to produce the best electrical performance can be incorporated, since a conventional wafer fabrication process is involved.

## CONCLUSION

A conductive AFM approach was used to investigate electrical conductivity behaviors of different  $\text{Cu}_2\text{O}$  crystal/Si wafer heterojunctions to seek proper combinations with current rectification and enhanced photocurrent properties. Initially, conductivity measurements on a  $\text{Cu}_2\text{O}$  cube, octahedron, and

rhombic dodecahedron, as well as Si {100}, {111}, and {110} wafers, were carried out, showing good agreement with the results obtained using a nanomanipulator. However, the Si {110} wafer is much more conductive than observed previously. Among the various  $\text{Cu}_2\text{O}$  crystal/Si wafer heterojunctions, the  $\text{Cu}_2\text{O}$  octahedron/Si {100} wafer and  $\text{Cu}_2\text{O}$  octahedron/Si {110} wafer combinations give good current-rectifying properties. The  $\text{Cu}_2\text{O}$  cube/Si {110} wafer and  $\text{Cu}_2\text{O}$  rhombic dodecahedron/Si {111} wafer combinations exhibit a pronounced photocurrent increase under white LED light illumination for the photodetector application. Some enhanced photocurrent responses were also measured for the  $\text{Cu}_2\text{O}$  octahedron/Si {100} wafer and  $\text{Cu}_2\text{O}$  cube/Si {111} wafer combinations. Adjusted band diagrams showing different interfacial band bending situations to facilitate or inhibit current flow are shown. These results suggest a novel FET design utilizing the current rectification ability of properly selected semiconductor heterojunctions, and the formation of such transistors should fit well with current semiconductor fabrication processes.

## EXPERIMENTAL SECTION

**Chemicals and Materials.** Sodium dodecyl sulfate ( $\text{C}_{12}\text{H}_{25}\text{SO}_4\text{Na}$ , >99%, J. T. Baker), anhydrous copper(II) chloride ( $\text{CuCl}_2$ , 98%, Alfa Aesar), sodium hydroxide ( $\text{NaOH}$ , 98%, Sigma–Aldrich), hydroxylamine hydrochloride ( $\text{NH}_2\text{OH}\cdot\text{HCl}$ , 99%, Sigma–Aldrich), and ethanol ( $\text{C}_2\text{H}_5\text{OH}$ , 99.8%, Honeywell) were used without further purification. Ultrapure distilled and deionized water (18.2  $\text{M}\Omega$ ) was used for all preparations. Commercial indium tin oxide (ITO) glass (6  $\mu\text{m}$  ITO on 1 mm glass, <10  $\Omega$ ), colloidal silver (16031, Ted Pella Inc.), a Si (111) wafer (4 in.  $\times$  500  $\mu\text{m}$ ,  $R = 16$ –20  $\text{k}\Omega$ , double sides polished, Elight), a Si (100) wafer (4 in.  $\times$  300  $\mu\text{m}$ ,  $R = 15$ –20  $\text{k}\Omega$ , double sides polished, Elight), and a Si (110) wafer (4 in.  $\times$  500  $\mu\text{m}$ ,  $R = 20$   $\text{k}\Omega$ , double sides polished, EL-CAT Inc.) were cut and cleaned before the conductivity measurements.

**Sample Preparation for Electrical Conductivity Measurements.** The synthesized  $\text{Cu}_2\text{O}$  nanocrystal solutions were first diluted to 24  $\mu\text{g}/\text{mL}$  with 99.8% ethanol. Next, 5  $\mu\text{L}$  of the diluted solution was dropped onto a commercial ITO glass, and the glass was kept in a desiccator before taking the conductivity measurements. The intrinsic silicon wafers were cut to an area of 1  $\times$  0.8  $\text{cm}^2$ , cleaned with acetone for 10 min using an ultrasonicator, and kept in a desiccator. For the combinations of  $\text{Cu}_2\text{O}$  crystals and Si wafers, a drop of the diluted  $\text{Cu}_2\text{O}$  solution was added onto a Si piece, and the wafer was kept in a desiccator before taking the conductivity measurements. No unexpected or unusually high safety hazards were encountered.

For the conductivity measurements of the Cu<sub>2</sub>O crystals, an electrical connection was made by smearing conductive silver ink to the side wall of an ITO substrate and to the bottom steel electrode. For the Si wafers, 1  $\mu$ L of conductive silver ink was added between the Si wafer and the bottom steel electrode without overflow to avoid an unintended side facet contact. For the combinations of Cu<sub>2</sub>O crystals on Si wafers, the same method as described for Si wafers was used.

**Instrumentation.** SEM images of synthesized Cu<sub>2</sub>O nanocrystals were obtained using a JEOL JSM-7000F scanning electron microscope. XRD patterns were collected by a Shimadzu XRD-6000 diffractometer using Cu K $\alpha$  radiation ( $\lambda = 1.5418 \text{ \AA}$ ). UV-vis absorption spectra were recorded using a JASCO V-670 spectrophotometer. The conductivity measurements were carried out using a Dimension Icon AFM (Bruker, USA) with a Nanoscope V controller and PF-TUNA module under ambient conditions. A conductive probe was used, which has a Pt/Ir coating with a spring constant of 2 N/m and resonance frequency at 70 kHz (OMCL-AC240TM-R3, Olympus). A white light-emitting diode (LED, XM-L2 U3, CREE) was used as the light source with a power density of  $\sim 30 \text{ W/cm}^2$ . The lamp was placed at a distance of 10 cm from the sample.

## ■ ASSOCIATED CONTENT

### SI Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/10.1021/acscentsci.1c01067>.

Procedures for the synthesis of Cu<sub>2</sub>O crystals, SEM images, XRD patterns, diffuse reflectance spectra and Tauc plot, band diagrams of different Cu<sub>2</sub>O crystals and Si wafers, illustrations and multiple *I*-*V* curves of different Cu<sub>2</sub>O crystal/Si wafer combinations, and adjusted band diagrams (PDF)

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### Notes

The authors declare no competing financial interest.

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