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Novel Self-shrinking Mask for OPENSub-3 nm Pattern Fabrication

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Itis very difficultto realize sub-3 nm patterns using conventional lithography for next-generation high-performance nanosensing, photonic, and computing devices. Here we propose a completely original and novel concept,termed self-shrinking dielectric mask (SDM),to fabricate sub-3 nm patterns. Instead of focusing the electron and ion beams or lightto an extreme scale,the SDM method relies on a hard dielectric mask which shrinks the critical dimension of nanopatterns during the ion irradiation. Based on the SDM method, a linewidth as low as 2.1 nm was achieved along with a high aspect ratio in the sub-10 nm scale. In addition, numerous patterns with assorted shapes can be fabricated simultaneously using the SDM technique, exhibiting a much higherthroughputthan conventional ion beam lithography.Therefore,the SDM method can be widely applied in the fields which need extreme nanoscale fabrication.

Because of the rapid evolution of nanosensors, nanophotonics, and semiconductor technology down to the sub-10 nm technology node, the research of sub-10 nm patterning has attracted much attention in recent years¹⁻⁶. For instance, sub-10nm electromagnetic hot spots with extreme near-field confinement are crucial to plasmonic applications^{6–8}. Nanoscaled graphene, quantum point contacts, and quantum dot devices for high level computing require precise sub-10nm patterning[9](#page-6-2) . Thus a variety of approaches including the helium and neon ion beam lithography^{[10](#page-6-3),[11](#page-6-4)}, electron beam lithography^{[12–16](#page-6-5)}, advanced scanning probe lithography^{[17](#page-6-6)}, nanoimprint lithography^{18,19}, and extreme ultraviolet lithography (EUV)²⁰⁻²², have been proposed in order to resolve the difficulties of the sub-10nm patterning. Nonetheless, these methods all have their individual problems which are difficult to be overcome. For example, ion beam, electron beam, and scanning probe lithography are capable of high-resolution patterning, but their throughput is typically low[23–27](#page-6-10). Although the high-voltage electron-beam lithography based on an aberration-corrected scanning transmission electron microscope (TEM) is a very promising approach to achieve the patterns down to 2 nm^{15,16}, large-area patterning is not feasible currently using this method because a large sample cannot be put into the TEM holder. Nanoimprint lithography suffers from the surface sticking issue and poor mold condition¹⁹. As for the EUV lithography, although a short wavelength of 13.5 nm provides a high potential for nanopatterning, EUV lithography has not yet to be proven effective and needs further development due to the challenges from photoresist, mask, optical reflectivity, and source power[21,](#page-6-13)[28](#page-7-0),[29](#page-7-1). Consequently, quadruple patterning is under high consideration for the 7 nm technology node in semiconductor industry due to the delay of the EUV lithography. However, how far the quadruple patterning can go depends on the cost, overlay control, and the cost-effectiveness of the extreme scaling³⁰. Another technique such as ALD (atomic layer deposition) spacer technique has also been proposed to reduce pitch size 31 . However, the spacer is prone to collapse if the spacer thickness is less than 5 nm³¹. The shadow (angle) evaporation is also capable of obtaining sub-10 nm gaps based on the poor conformality of evaporation^{[32](#page-7-4)}. Nevertheless, it is not easy to precisely control the patterning by this approach.

It is difficult to achieve the patterns with a linewidth down to sub-3nm scale using the conventional lithography based on focusing optical, electron, or ion beams. Hence, we conceive that instead of using focused beam approaches, shrinking the pattern itself will be a more practical and facile way to achieve the sub-3nm fabrication. In this study, we demonstrate a new concept and method, entitled self-shrinking dielectric mask (SDM), to realize sub-3 nm patterns. The SDM method starts from an initial pattern on a hard dielectric mask, which could be defined by conventional optical, ion beam, or electron beam lithography. Afterwards the sample is exposed to the ion irradiation for a couple of minutes, and then the linewidth of the initial patterns on the mask will shrink during the ion irradiation. Together with a high aspect ratio, a critical dimension down to 2.1 nm has been realized with the SDM method in this study. Besides, a large area of nanopatterns can be fabricated

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Figure 1. The SDM method. (a) The substrate is deposited with a hard dielectric mask such as Al₂O₃. (b) An initial pattern (e.g. a line array) is defined by the conventional lithography such as optical, ion and electron beam lithography, etc. (**c**) Scheme illustrates the self-shrinking process on the dielectric mask during the ion irradiation. (**d**) A hard mask with the target linewidth is obtained when the ion irradiation is stopped. (**e**) Then the patterns are transferred to the substrate by subsequent dry etching such as reactive ion etching or further ion exposure. (**f**) The dielectric mask is etched away and then the nanopatterns on the substrate are obtained.

simultaneously by the ion irradiation on SDM, instead of using sequential line-by-line writing such as those in the focused ion beam and electron beam lithography. Thus, a considerable amount of process time can be saved and the throughput can be greatly improved. Actually, one of the practical applications of SDM is shown in the supporting information. We have implemented the SDM method to define a sub-10nm gate in the junctionless Si transistors on silicon-on-insulator substrate. Therefore, the SDM technique is an effective, high-throughput, and practically applicable nanofabrication technique to tailor the structures with the feature sizes down to a few nanometers, which can be applied not only in the high-performance computing devices, but also in the advanced nanosensors and nanoplasmonics. Accordingly, SDM shows a strong and promising potential to have a role in the next-generation high-throughput sub-3nm fabrication.

Results

[Figure 1](#page-1-0) depicts the SDM method schematically. First, a hard dielectric layer such as Al_2O_3 is deposited on the substrate [\(Fig. 1a\)](#page-1-0), and then an initial pattern (e.g. a line array) is defined by conventional lithography [\(Fig. 1b](#page-1-0)). Afterwards, the sample is exposed to the ion irradiation for a couple of minutes, and the gap will shrink during the ion irradiation ([Fig. 1c](#page-1-0)). The ion irradiation will be stopped once the target linewidth of nanogap is reached, and then a hard mask with the target linewidth is obtained ([Fig. 1d](#page-1-0)). With this hard dielectric mask, we can transfer the patterns to the substrate by subsequent reactive ion etching or by further ion irradiation [\(Fig. 1e](#page-1-0)). Finally, the hard mask is removed away by etching after the patterns are transferred to the substrate ([Fig. 1f\)](#page-1-0).

The scanning electron microscope (SEM) pictures of nanopatterns fabricated by SDM method are shown in [Fig. 2.](#page-2-0) In [Fig. 2a](#page-2-0), an Al₂O₃ line array with ~145 nm gap width was fabricated by focused Ga ion beam to act as the initial pattern for SDM. [Figure 2b](#page-2-0) shows the cross section of the line array with a V**-**shaped geometry (a top width of \sim 145 nm and a bottom width of \sim 83 nm) which is mostly due to the redeposition effect^{[33](#page-7-5)}. This is hard to avoid when using focused Ga ion beam to fabricate deep nanopatterns. Afterwards, the $A₂O₃$ line array was exposed to the Ga ions, and the gap size started to shrink during the ion irradiation. After a couple of minutes, the gap shrunk to the sub-10nm scale. [Figure 2c](#page-2-0) shows an example of the cross section of the nanogap after the ion irradiation. The gap was estimated to be ~280nm in depth with an opening of only 9.2nm in width. This aspect ratio is ~30 which is quite extraordinary for sub-10 nm patterns^{[34](#page-7-6)}. [Figure 2d](#page-2-0) illustrates a tiny gap size on the Al₂O₃ mask of a critical dimension as low as 2.9nm after the subsequent ion exposure. Further ion irradiation leads to a minimum gap width down to 2.1nm as shown in [Fig. 2e](#page-2-0). This narrow linewidth is much smaller than the spot size of the focused Ga ion beam even with the lowest beam current (The smallest spot size is \sim 7 nm at the lowest beam current of ~1.1 pA for FEI Helios Nanolab 600i focused ion beam system). In fact, there is no need to focus the ion beam to get nanopatterrns using the SDM technique. The beam current we used in the experiment is as large as 2.5 nA, and the focused spot size at this beam current is about 133 nm³⁵. Accordingly, with a 2.5 nA beam current, the optimal resolution for Ga ion beam lithography is not smaller than 133nm. Nevertheless, a pattern down to 2.1 nm has been achieved by the SDM method, which clearly indicates that focusing the beam is not necessary to get a sub-3nm pattern using SDM. Additionally, it is known that the ion milling rate is proportional

Figure 2. SEM images of the nanopatterns fabricated by the SDM method. (a) An initial pattern (an Al_2O_3 line array) fabricated by the focused Ga ion beam. (**b**) Cross section of the initial pattern with a V-shaped structure before the ion irradiation. (**c**) Cross section of the shrunk line array with a high aspect ratio of ~30. The nanogap on the left side of the figure was filled by the debris of $\rm Al_2O_3$ during the fabrication of the cross section using the focused Ga ion beam. A protection layer was not deposited upon the dielectric mask for the fabrication of the cross section because it would fill the gap so that we could not observe the gap width clearly. In fact, the absence of the protection layer does not result in the change of the lateral width of the nanogaps. It may only influence the surface morphology due to the sputtering away of the surface. (**d**) The line array shrunk after the ion irradiation, and a linewidth as low as 2.9nm was shown in the inset. (**e**) A top view of the nanogap in the line array after the further ion exposure, exhibiting a minimum linewidth as low as 2.1nm. (**f**) Line patterns were transferred to the substrate by subsequent ion irradiation, revealing a critical dimension less than 10nm (minimum linewidth is ~5.4nm). It should be noticed that the patterns were transferred from the hard mask shown in [Fig. 3f](#page-3-0). It is seen that the linewidth of patterns transferred to the substrate (f) is comparable to that of the hard mask [\(Fig. 3f](#page-3-0)).

to the beam current^{[36](#page-7-8)}. As a result, by using a more than two thousand times larger beam current (2.5 nA beam current rather than 1.1 pA), we have saved over 99.9% of time to fabricate sub-10 nm patterns. Finally, after the patterns were transferred to the substrate underneath the AI_2O_3 layer, the AI_2O_3 mask was removed by wet

Figure 3. SEM images of the shrinking line array during the ion irradiation. The initial pattern with a gap width of 145nm before the ion irradiation is shown in (**a**). After merely ~4minutes of ion exposure, the gap linewidth shrunk to 75nm (**b**). The images from (**c**–**f**) were taken chronologically after 6, 8.5, 9, and 10minutes of the ion irradiation, respectively. The result clearly demonstrates the linewidth of the gap shrunk dramatically from 145nm to 5nm using the SDML method.

etching. An example of the sub-10nm linewidth on the substrate is shown in [Fig. 2f](#page-2-0), demonstrating a linewidth as low as \sim 5.4 nm.

To observe the self-shrinking process more clearly, a series of SEM images were taken during the ion irradiation and are shown in [Fig. 3.](#page-3-0) It is clearly observed that the gap size reduces dramatically from 145nm to 5nm with the increase of exposure time. The self-shrinking process shown in [Fig. 3](#page-3-0) also reveals that the pattern linewidth can be manipulated arbitrarily over a wide range from hundreds to a few nanometers, depending on the ion exposure time. In the supplementary information, Fig. S10 shows the Al_2O_3 gap width as a function of the exposure ion dose, which were obtained from the SEM images taken after every dose increment of 55.86 pC/ μ m² chronologically (shown in Figs $S11~S13$). It can be clearly seen from these SEM images that the line array is very uniform and nearly free of any defects across a wide horizontal field width (HFW). Figure S10 reveals a linear relation between the gap width reduction and the ion irradiation dose, with a wide range of the gap width from ~120nm to ~5nm. The result demonstrates that SDM is a highly controllable method for the precise fabrication of uniform nanopatterns.

Nanopatterns with a variety of different shapes are also feasible to be fabricated by SDM. In [Fig. 4a,](#page-4-0) a circle ring array with a ~100 nm gap width as the initial pattern was fabricated by focused Ga ion beam. A gap width down to 4.3nm was achieved by SDM as depicted in [Fig. 4b](#page-4-0). The inset in [Fig. 4b](#page-4-0) is the cross section of the nanogap of the circle ring, revealing that the bottom width of the nanogap is only 2.7nm. Apart from the circle ring

Figure 4. Multiple shapes fabricated by SDM. (**a**) A circle ring array with ~100nm gap width as the initial pattern prepared by the focused Ga ion beam. (**b**) The gap width of the circle ring was reduced to 4.3nm after the ion irradiation. The inset shows the cross section of the circle ring, revealing a bottom linewidth down to 2.7nm. (**c**) A nanohole array with a diameter of ~120nm as the initial pattern prepared by the focused Ga ion beam. (**d**) The diameter of the nanohole array shrunk to 7nm after the ion irradiation. (**e**) A square ring array with ~100nm gap width as the initial pattern prepared by the focused Ga ion beam. (**f**) The gap width of the square ring was reduced to 4.1nm after the ion irradiation.

array, initial patterns including a nanohole array with a diameter of \sim 120 nm and a square ring array with a gap width of ~100 nm were also prepared as shown in Fig. 4c, e, respectively. [Figure 4d,f](#page-4-0) show the diameter of the nanoholes and the gap width of the square ring dramatically shrunk to 7nm and 4.1nm, respectively, after the ion irradiation. The result clearly demonstrates that the SDM method is capable of creating nanopatterns with different shapes of a critical dimension down to sub-10 nm scale. It should be noted that the surface of Al₂O₃ inside the square become spherical (in [Fig. 4e\)](#page-4-0) after the ion irradiation of the square ring patterns.

The most attractive feature of SDM is the self-shrinking process of the hard dielectric mask. There might be a number of mechanisms behind this phenomenon. One of the possible explanation is the re-deposition effect during the ion beam irradiation. That is, the hard dielectric materials are sputtered from the base of the feature onto the sidewalls during the ion irradiation. It has been reported that the holes of the anodic aluminum oxide film were closed after the Ga ion beam irradiation, which was mainly ascribed to the re-deposition of the sidewall alumina to the top capping layer³⁷. However, the re-deposition effect cannot explain the whole phenomenon we have observed. For example, [Fig. 2b,c](#page-2-0) clearly shows that the Al_2O_3 mask re-shapes from the wide V-shaped geometry to the straight nanogap with a high aspect ratio after the ion irradiation. It is not reasonable that re-deposition would form such a straight gap rather than a V-shaped geometry^{33,[38](#page-7-10),[39](#page-7-11)}. Furthermore, the spherical A_2O_3 surface as shown in [Fig. 4e](#page-4-0) is unlikely to be solely caused by the re-deposition effect. As a result, re-deposition is not the only mechanism involved in the self-shrinking mask, and some other mechanisms should take place simultaneously. Another likelihood is the ion bombardment induced viscous flow and surface diffusion of the hard dielectric mask, which has been widely discussed in the previous studies^{[37,](#page-7-9)40-43}. Please be noticed that the nanopattern as shown in [Figs 2](#page-2-0) and [3](#page-3-0) is quite uniform and straight, which cannot be solely explained by either the re-deposition

Figure 5. SEM images of a large area of uniform ~5nm patterns fabricated by the SDM method. (**a**) A large exposure area of \sim 3570 μ m² with the ion irradiation. (**b**) A magnified image from (**a**), revealing a uniform line array over a large area. (**c**) A magnified image from (**b**), revealing a narrow gap of only 5.1nm in width.

or the ion-induced viscous flow and surface diffusion. The charging effect might be the possible reason for this high quality nanopattern. When the sample is exposed to the ions, the non-conductive dielectric mask will be charged by positive ions⁴⁴. The charging effect might keep the sputtered or flowing dielectric mask from being severely distorted due to the repulsive force between the positively charged mask. As a result, the line array could stay straight and uniform as shown in in [Figs 2](#page-2-0) and [3](#page-3-0). The TEM image and energy dispersive spectroscopy (EDS) mapping of the nanogap cross section have been provided in the supplementary information. The EDS mapping indicates that the edge of the Al2O3 mask was highly doped by Ga, and so it can be deduced that the Al2O3 mask was charged during the ion irradiation. The TEM image shows that the Ga-doped Al2O3 was filled into the gaps, which might be attributed to the re-deposition or ion-induced viscous flow/surface diffusion. To sum up, the self-shrinking process may be a comprehensive manifestation of the above-mentioned mechanisms. It is noteworthy that the dielectric mask should be hard enough in order to prevent it from being milled out completely. If this happens, self-shrinking process will not occur. Therefore, Al2O3 was chosen as the mask in this study because of its extremely hard-wearing nature to ion milling.

Another attractive point of SDM is its capability to fabricate a myriad of nanopatterns simultaneously as long as the initial patterns are within the exposure area of the ion source. In [Fig. 5](#page-5-0), its ability to fabricate a large area of a uniform sub-10 nm line array is shown. A large exposure area of $3570 \mu m^2$ irradiated by the Ga ions is illustrated in [Fig. 5a.](#page-5-0) In fact, even a larger area can be achieved by SDM as long as the initial pattern is within the exposure region of the ion source. [Figure 5b](#page-5-0) depicts the uniformly fabricated sub-10nm line array after the ion irradiation, and a small linewidth of only 5.1nm is clearly manifested in [Fig. 5c](#page-5-0).

Some particular methods had been proposed to define sub-10nm patterns by focused ion beam, such as delineating patterns on a sample with a thick sacrificial metal layer using the lowest Ga ion beam current⁴⁵, or utilizing helium or neon as the ion beam source^{10,11}. However, these techniques are extremely time-consuming with a very low throughput to fabricate nanostructures within a large area, because of the low beam current (1.1pA) and the inefficient milling rate of helium and neon. For example, to fabricate a line array with a large area of $3570 \mu m^2$ (the same as that shown in [Fig. 5a](#page-5-0)) with sequential line-by-line writing by focused Ga ion beam, it will take more than 610 hours using the lowest 1.1 pA beam current. Nevertheless, the minimum gap width is \sim 20 nm even using this lowest beam current. However, it takes only ~30 minutes (less than 20 minutes for the initial patterns and about 10 minutes for the ion irradiation) to fabricate a sub-10 nm (\sim 5.1 nm) line array with a large area of $3570 \,\upmu m^2$ using the SDM technique [\(Fig. 5](#page-5-0)). This result indicates that SDM is over 1000 times more rapid than the focused ion beam lithography and the feature size down to a few nanometer can be achieved at the same time.

In conclusion, we have proposed a novel concept to fabricate sub-3nm patterns. Based on the self-shrinking mask instead of the focused beam approach, the SDM method can fabricate nanopatterns with the feature size of only a few nanometer, which is difficult to be fulfilled with the conventional lithography. We believe that the SDM technique opens a new door and impinges a great impact on the nanofabrication of sub-5nm patterns in a variety of applications.

Methods

Preparation of the Al₂O₃ dielectric mask. The Al₂O₃ layer of a thickness of ~360 nm was deposited by atomic layer deposition at 200 **°C** (Savannah S100, Cambridge nanotech), using trimethylaluminum [TMA, $\text{Al}(\text{CH}_3)_3$, and H_2O vapor as the precursors.

Initial patterns on the AI_2O_3 **dielectric mask.** A line array or other initial patterns on the AI_2O_3 dielectric mask can be prepared by conventional lithography techniques. In this study, we delineated the initial patterns on the Al_2O_3 mask by the focused Ga ion beam (FEI Helios Nanolab 600i dual beam system) with an accelerating voltage of 30 kV, a beam current of 2.5 nA, 1 μ s dwell time, and a dose of 2.4 nC/ μ m² for each gap.

Ion irradiation. The samples with initial patterns on the AI_2O_3 mask was irradiated by Ga ions at 30 keV and a beam current of 2.5nA (FEI Helios Nanolab 600i dual beam system). The samples were exposed to Ga ions with a field of view of 3570 μ m² for about 10 minutes to get sub-3 nm nanopatterns on the Al_2O_3 mask. *In situ* observation was carried out by the SEM equipped in the dual beam system.

Wet etching of the $\mathbf{A} \cdot \mathbf{A}_2 \mathbf{O}_3$ **mask.** $A_{12} O_3$ **was etched away in dilute HF solution. Then the sample was** observed by the SEM equipped in FEI Helios 600i dual beam system.

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AuthorContributions

M.-J.C. conceived the project. P.-H.Y. designed the experiments, prepared the materials and collected the data. P.-H.Y. and M.-J.C. wrote the manuscript. P.-H.Y., P.-H.C., C.R.K. and M.-J.C. discussed the results and commented on the manuscript.

Additional Information

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