A nonvolatile magnon field effect transistor at room temperature

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Jun Chen[g](http://orcid.org/0000-0001-5925-1881) ^{® [1](http://orcid.org/0000-0003-0336-374X),6}, Rui Yu^{1,2,6}, Liang Sun ® ^{1,6}, Kang He¹, Tongzhou Ji¹, Man Yang ® ¹, Zeyuan Zhang1 , Xueli Hu1 , Heng Ni[u](http://orcid.org/0000-0001-5321-1387) ¹ , Xi Yang1 , Peng Chen1 , Gong Che[n](http://orcid.org/0000-0002-5102-7487) [1](http://orcid.org/0000-0002-5102-7487) , Jiang Xia[o](http://orcid.org/0000-0002-3245-3579) ®³, Fen[g](http://orcid.org/0000-0002-2487-1930)zhen H[u](http://orcid.org/0000-0002-8794-1000)ang ®^{[1](http://orcid.org/0000-0002-2855-3016)}, X[i](http://orcid.org/0000-0002-2855-3016)aomei Lu ®¹, Hongling Cai ®¹, Huaiyan[g](http://orcid.org/0000-0001-7524-0779) Yuan $\mathbf{D}^4\boxtimes$ $\mathbf{D}^4\boxtimes$ $\mathbf{D}^4\boxtimes$, Bingfeng Miao $\mathbf{D}^1\boxtimes$ & Haifeng Ding $\mathbf{D}^{1,5}\boxtimes$

Information industry is one of the major drivers of the world economy. Its rapid growth, however, leads to severe heat problem which strongly hinders further development. This calls for a non-charge-based technology. Magnon, capable of transmitting spin information without electron movement, holds tremendous potential in post-Moore era. Given the cornerstone role of the field effect transistor in modern electronics, creating its magnonic equivalent is highly desired but remains a challenge. Here, we demonstrate a nonvolatile three-terminal lateral magnon field effect transistor operating at room temperature. The device consists of a ferrimagnetic insulator $(Y_3Fe_5O_{12})$ deposited on a ferroelectric material $[Pb(Mg_{1/3}Nb_{2/3})_{0.7}Ti_{0.3}O_3$ or $Pb(Zr_{0.52}Ti_{0.48})O_3]$, with three Pt stripes patterned on $Y_3Fe_5O_{12}$ as the injector, gate, and detector, respectively. The magnon transport in $Y_3Fe_5O_{12}$ can be regulated by the gate voltage pulses in a nonvolatile manner with a high on/off ratio. Our findings provide a solid foundation for designing energy-efficient magnon-based devices.

Since the 1960s, the density of transistors in integrated circuits has followed Moore's law, doubling approximately every two years. However, with the increasing density and speed of the devices, the density of the power consumption also increases, resulting in a growing serious heat problem. This obstacle strongly limits the further development of integrated circuits¹. In order to overcome this problem, it becomes imperative to explore alternative possibilities instead of solely relying on the charge property of the electrons. Magnon, a collective excitation of magnetic moments, can carry spin information without electron movement, i.e., in the absence of the Joule heating 2^{-10} . Besides, magnon can also enable the switching of magnetization $¹¹$.</sup> These remarkable features make magnon one of the most promising information carriers in post-Moore $era^{2,9,12-19}$ $era^{2,9,12-19}$ $era^{2,9,12-19}$. Given the prominent role of the field effect transistor (FET) in modern electronics, it is highly

desirable to create its corresponding magnonic version. However, unlike the charge currents in semiconductors which can be readily regulated with an electric field, finding an equivalent method for controlling magnon current is very challenging due to the weak coupling between the electric field and magnetic moment because of their different symmetry requirements²⁰. Though there are several theoretical and experimental investigations on the electric field induced frequency shift of the spin waves 21,22 21,22 21,22 , the control of the magnon flow with an electric field has yet to be reported.

In the hot pursuit of manipulation of magnon transport, different versions of magnon transistors have been demonstrated 2^{3-33} . These devices either transmit magnon current vertically or rely on gating with the electric current or magnetic field $31-33$. The magnonic circuits, however, require the magnon transistor to be in lateral structure and

¹National Laboratory of Solid State Microstructures and Department of Physics, Nanjing University, and Collaborative Innovation Center of Advanced Microstructures, Nanjing 210093, PR China. ²Shanghai Advanced Research Institute, Chinese Academy of Sciences, Shanghai 201204, PR China. ³Department of Physics, Fudan University, Shanghai 200433, PR China. ⁴Institute for Advanced Study in Physics, Zhejiang University, 310027 Hangzhou, PR China. ⁵Shishan Laboratory, Suzhou Campus of Nanjing University, Suzhou 215000, PR China. ⁶These authors contributed equally: Jun Cheng, Rui Yu, Liang Sun. e-mail: [hyyuan@zju.edu.cn;](mailto:hyyuan@zju.edu.cn) [bfmiao@nju.edu.cn;](mailto:bfmiao@nju.edu.cn) hfding@nju.edu.cn

have three terminals for device cascading, ideally having high energy efficiency such as being controlled by electric field and nonvolatile. Despite successfully controlling magnon current through the magnonmagnon scattering 23 and switching the magnetization beneath the detector³⁰, developing a three-terminal electric field controlled lateral magnon FET still remains a grand challenge.

Here, we report a unique design and the experimental demonstration of a nonvolatile three-terminal lateral magnon FET. By applying electric field pulses at the gate, a strong modulation of the thermally excited magnon current laterally propagating from the source to the detector is achieved at room temperature. A high on/off ratio up to ~400% is observed. Importantly, our device operates in a nonvolatile manner at room temperature and with the electric field gating. This notable progress demonstrates the capabilities of the three-terminal magnon FET, enabling the development of innovative and energy-efficient solutions. Our results not only open doors to electric field controlled, low-power-consumption magnon-based conventional logic computing, but also reveal potential applications in neuromorphic computing.

Results

The operational principle
Figure 1 presents a brief sketch of our design of the magnon FET. The device consists of three parallel heavy metal (HM) nano-stripes on top of a ferromagnetic or ferrimagnetic insulator (FI) film deposited on a ferroelectric (FE) material. The nano-stripes serve as the magnon source (S), gate (G), and detector (D), respectively. When a charge current passes through the S-stripe, magnons are excited by the spin current via the spin Hall effect (SHE) in HM and the thermal activation. The imbalanced distribution of the magnon results in a lateral magnon current propagating from the S-stripe toward the D-stripe. When voltage pulses with sufficient intensities are applied, the electric polarization of the ferroelectric domain beneath the G-stripe can be modulated. The coupling between the FI film and the FE layer subsequently has a substantial impact on the propagation of the magnon current from the S- to D-stripe, resulting in a "high" or "low" non-local voltage V_{nl} across the D-stripe, which can be detected via the inverse spin Hall effect (ISHE) in HM.

In our devices, we deposited a ~60-nm thick $Y_3Fe_5O_{12}$ (YIG) film onto a (011)-oriented Pb($Mg_{1/3}Nb_{2/3}$)_{0.7}Ti_{0.3}O₃ (PMN-PT) single crystal or (001)-oriented Pb(Zr_0 ₅₂Ti_{0.48})O₃ (PZT) thin film. The structure and magnetic properties are presented in Supplementary Note 1. YIG is chosen because it has low damping, which is suitable for magnon transport, and is an insulator where the joule heating is absent. Besides, PMN-PT has been demonstrated to exhibit nonvolatile modulation on various transport properties $34-37$ $34-37$. This direct contact enables a strong coupling between the FI film and the FE material. Three parallel Pt-stripes (~550-nm wide, 15-μm long, and 3.5-nm thick) were patterned on top of the FI film using electron beam lithography to serve as the S-, G-, and D-stripes, respectively. The in-plane and out-ofplane I−V curves indicate that the YIG film maintains an insulating behavior upon applying the gate voltage (Supplementary Note 2). For details on sample preparation, see Methods. All measurements were conducted at room temperature.

We first established a non-local measurement for the magnon transport. Figure [2a](#page-2-0) shows a scanning electron microscopy (SEM) image of a typical device, which consists of only the S- and D-stripes. The centerto-center distance between the two stripes is d , and the in-plane external magnetic field is applied with an angle α with respect to the perpendicular direction of the stripe, as marked in the inset. We applied a low-frequency $(f = 15.7 \text{ Hz})$ AC current with an amplitude of I_{ω} = 0.71 mA in the S-stripe and measured the first ($V_{\text{nl}}^{1\omega}$) and second $(V_{\text{nl}}^{2\omega})$ harmonic non-local voltages at the D-stripe as a function of a with a lock-in amplifier. Figure [2](#page-2-0)b, c presents the angular dependence of $V_{\text{nl}}^{\text{1}\omega}$ and $V_{\text{nl}}^{\text{2}\omega}$ for $d=2\,\mu\text{m}$, respectively. Together with the I_{ω} -dependent studies (Supplementary Note 3), the corresponding fittings show that these non-local voltages can be well described by $V_{\text{nl}}^{\text{1}\omega} \propto I_{\omega} \cos^2 \alpha$ and $V_{\text{nl}}^{\text{2}\omega} \propto I_{\omega}^2 \cos \alpha$, respectively. These results are consistent with previous findings^{[3](#page-6-0),[31](#page-6-0)}, and show that $V_{\text{nl}}^{\text{1}\omega}$ originates from the electronically injected magnon current via the SHE and $V_{nl}^{2\omega}$ is due to the magnon current thermally excited via the spin Seebeck effect (SSE). We note our obtained phase difference between 1ω- and 2ω-curves is the same as ref. [3](#page-6-0) but has a shift of π with ref. [31](#page-6-0), which may be due to the opposite definition of the injected current direction.

Fig. 1 | Sketch of a three-terminal magnon field effect transistor (Magnon FET). Three heavy-metallic (HM) stripes are placed onto a ferromagnetic or ferrimagnetic insulator (FI) /ferroelectric (FE) bilayer. The left stripe acts as the magnon source (S), while the transmission of magnons to the detector (D) stripe placed on the right side can be modulated by the voltage pulses V_g applied at the gate (G) stripe, which is located in the middle. Meanwhile, spin accumulation is generated at the HM/FI

interface of the injector stripe via the spin Hall effect or thermal excitation, and the spin accumulation is converted into a charge current at the detector stripe through the inverse spin Hall effect. Additionally, a bottom electrode is positioned beneath the FE layer and is aligned with the G-stripe. The magnetic field μ_0H is applied in the film plane. In our experiments, the materials of HM, FI, and FE are selected as Pt, $Y_3Fe_5O_{12}$, and Pb($Mg_{1/3}Nb_{2/3}O_{0.7}Ti_{0.3}O_3$ or Pb($Zr_{0.52}Ti_{0.48}O_3$, respectively.

Fig. 2 | Non-local measurements of the lateral magnon transport. a Scanning electron microscope image of a typical device for non-local detection of the magnon transport with a two-terminal configuration. The left Pt-stripe serves as the magnon source, while the right one is the detector. The center-to-center distance between the two stripes is d, α denotes the angle between the direction of the applied magnetic field μ_0 H and the direction perpendicular to the stripes. b, c Angular dependence of the simultaneously measured first- and secondharmonic non-local voltage signals, respectively. The lines are the fittings with the

 $\cos^2 \alpha$ and $\cos \alpha$ functions, respectively. **d** Angular dependence of the DC non-local voltage signal. The line is the fitting obtained with the sum of both cos α and cos² α functions. The best fitting yields their relative ratio in amplitude to be ~13:1. e Dependence of the DC non-local voltage amplitude with the square of DC current. The line is a linear fitting.f DC non-local voltage amplitude (in the logarithmic scale) as a function of d. Error bars are obtained from repeated measurements for each d . The line is a linear fitting. In b -f, the symbols are the experimental data.

To circumvent the obstacle induced by capacitive and inductive couplings and explicitly demonstrate the electric field gating effect on the magnon transport, we also performed the non-local measurements with the DC injected current I_{DC} . In principle, the detected non-local voltage V_{nl}^{DC} (measured with a voltmeter) corresponds to the combined effect induced by both the electrically and thermally generated magnons. Due to the different time scales between the electron and thermal relaxation, the contribution from the thermally generated magnons is expected to increase with decreasing frequency $38,39$. Indeed, the obtained V_{nl}^{DC} is dominated by the angular dependence of cos α for the DC measurements ($f \approx 0$ Hz) (Fig. 2d). This is further supported by the power-dependent measurement (Fig. 2e), which shows that V_{nl}^{DC} amplitude is proportional to the square of the applied DC current, I_{DC}^2 . We also fabricate a series of devices with different distance d between the S- and D-stripes as marked in Fig. 2a. The measured V_{nl}^{DC} amplitude shows an exponential decay with d (Fig. 2f). The extracted magnon diffusion length $\lambda = 1.42 \pm 0.20 \,\mu m$ is slightly shorter than that obtained in the epitaxial YIG films $3,40$, as expected for the sputtered polycrystalline films.

To study the electric field controlled magnon transport in the YIG channel, we prepared the samples with all the S-, G-, and D-stripes. Figure [3a](#page-3-0) shows an SEM image of a typical device. A voltage pulse V_g is applied at the G-stripe, and the modulation of the magnon propagation is measured via the voltage change at the D-stripe. Before the electric field dependent measurements, several cycles of sweeping V_{σ} between −200 V and +200 V were performed to reduce the memory effect. We first measured the non-local voltage $V_{\text{nl}}^{\text{DC}}$ as a function of the in-plane magnetic field μ_0H (α = 0) under different applied voltage

pulses (Fig. [3](#page-3-0)b, c). We note that all curves were obtained after the electric field had been removed, namely in a nonvolatile manner. When V_g = +200 V, V_{nl}^{DC} exhibits a hysteresis loop with an amplitude (defined as half the difference between $V_{\text{nl}}^{\text{DC}}$ obtained under positive and negative saturation magnetic field) of ∼130 nV. This behavior is consistent with the thermally generated magnon current, as the electrically generated magnon current exhibits a $\cos^2 \alpha$ -angle-dependence and remains unaffected by changing the magnetic field direction. We find that the magnetic-field-dependent V_{nl}^{DC} exhibits an enhancement near the coercivity (H_C -2.4 mT). A similar phenomenon was observed previously, though its origin was not mentioned 41 . We proposed a possible explanation for this phenomenon (see Supplementary Note 4). When V_g decreases from +200 V to −10 V, the amplitude of $V_{\text{nl}}^{\text{DC}}$ remains almost unchanged. In contrast, with further decreasing $V_{\rm g}$ the amplitude of $V_{\rm nl}^{\rm DC}$ first decreases gradually and goes more sharply near $V_g = -100 V$, and eventually becomes negligibly small when V_g is below −110 V. In this process, we also observed a change in the shape of the hysteresis loop, indicating a change in magnetic anisotropy. Increasing V_g from −200 V back to +200 V gives the reverse behavior. The amplitude of V_{nl}^{DC} does not change significantly when V_{g} is increased to +10 V, and a sharp increase occurs around V_g = +150 V. The curve almost recovers the initial state when V_g returns to +200 V. We also calculated the charge loop (blue curve in Fig. [3](#page-3-0)b) with the V_g -dependent leakage current^{[42](#page-6-0)} and found that they exhibited close similarity, indicating a strong correlation between the observed magnon current modulation and the change of the electric polarization of the FE layer.

Figure [3b](#page-3-0) summarizes the electric control of the amplitude of the non-local voltage V_{nl}^{DC} in the magnon FET. The V_{nl}^{DC} amplitude shows an almost square-shaped electric hysteresis loop, with a larger (smaller)

Fig. 3 | Performance of a magnon FET device. a SEM image of a typical threeterminal magnon FET device. V_g is applied across the G-stripe and the bottom electrode (BE). **b** Non-local voltage $V_{\text{nl}}^{\text{DC}}$ amplitude (red) as a function of the gate voltage pulse V_g for device 1 with a width of -585 nm for the gate stripe, exhibiting a typical hysteresis in close similarity with the charge loop (blue) obtained on the same sample. Error bars are obtained from repeated measurements for each V_g . **c** Magnetic field-dependent V_{nl}^{DC} after applying different voltage pulses V_g . For

clarity, the loops are shifted vertically. All non-local measurements are conducted after the electric voltage is removed, namely, in a nonvolatile manner. d Electric field induced nonvolatile switching of $V_{\text{nl}}^{\text{DC}}$ amplitude at the remnant states after applying the pulses of $V_g = \pm 200$ V. e The averaged magnetic field-dependent V_{nl}^{DC} for $V_g = \pm 200$ V, showing a high on/off ratio of ~400%. In **b–e**, the symbols are the experimental data, and the lines are guides to the eye.

signal observed when the PMN-PT substrate is positively (negatively) polarized. Furthermore, the remnant states at $V_g = \pm 0$ V remain the same as the previous states at ±200 V (where +0 V and −0 V correspond to the 0 V state withdrawn from +200 V and −200 V, respectively), demonstrating clear non-volatility. The turning points for V_{nl}^{DC} amplitude around −100 V and +150 V coincide with the polarization switching voltage of the PMN-PT substrate, as evidenced by the charge loop (blue curve) calculated from the polarization current loop (Supplementary Note 2). We also demonstrated a continuous toggling between two states in the magnon FET with the voltage gating. When V_g = +200, V is applied, the remnant state "1" results in a high V_{nl}^{DC} amplitude (above 100 nV). After switching V_g to −200 V, the remnant state "0" exhibits a low value (below 50 nV). By further cycling V_{α} between +200 V and −200 V, the two nonvolatile states of measured $V_{\text{nl}}^{\text{DC}}$ amplitude exhibit no apparent attenuation. Figure 3e presents the averaged magnetic field-dependent $V_{\text{nl}}^{\text{DC}}$ for the remnant states after $\pm 200 \text{ V} - V_{\text{g}}$ are applied. The average on/off ratio of 6 cycles shows a high value of ~400% $[(V_{nl}^{DC, +200 \,V} - V_{nl}^{DC, -200 \,V})/V_{nl}^{DC, -200 \,V} \times 100\%$, with $V_{nl}^{DC, +200 \,V} (V_{nl}^{DC, -200 \,V})$ denotes the V_{nl}^{DC} amplitude for $V_{\rm g}$ = +200 V(-200 V)]. Though the minimum value of $V_{\rm nl}^{\rm DC}$ is not 0, it does not impede their applications as long as the high/low states can be well-defined. This is similar to the high resistance/low resistance states in giant magnetoresistance or tunneling magnetoresistance devices, where the low resistance values are also not zero. The stability of an additional device with 21 cycles is provided in Supplementary Note 5. And the magnetic field-dependent V_{nl}^{DC} curves under higher gate voltage for a new sample are presented in Supplementary Note 6. In addition, the comparison of non-local voltage with V_g holding and removal is presented in Supplementary Note 7. Therefore, an electric

field controlled, nonvolatile three-terminal magnon FET with high on/ off ratio at room temperature is demonstrated.

Discussion

We next discuss the possible mechanisms for the electric field induced modulation of the magnon current transport with device 2 (Fig. [4a](#page-4-0), devices 2 and device 1 have similar dimensions but were prepared on different PMN-PT substrates), which exhibits a similar V_{nl}^{DC} amplitude of ~150 nV for the remnant state at +0 V and a larger $V_{\text{nl}}^{\text{DC}}$ amplitude of ~70 nV for the remnant state at −0 V as compared with device 1. The different on/off ratio between device 1 (400%) and device 2 (115%) may be caused by various reasons, such as different substrate-induced variations and those caused by complicated fabrication processes. The ferroelectric substrate we used is $[Pb(Mg_{1/3}Nb_{2/3})O_3]_{(1-x)}$ -[PbTiO₃]_x (PMN–PT), whose property depends sensitively on the composition. The substrate from the different positions of a wafer may have different properties, resulting in different device performance. And the YIG films may exhibit non-uniform properties after polarizing the PMN-PT substrates. In addition, the fabrication processes may also introduce discrepancies in quantity between different samples. To further reveal the role of the gap width and substrate, we performed the gate width dependent on/off ratio measurements with two series of samples fabricated on two different pieces of PMN-PT substrates (Supplementary Note 8). Furthermore, we present a summary of the on/off ratio of 16 PMN-PT/YIG/Pt samples with different YIG thicknesses, where most of the samples can achieve 20%~100% on/off ratio (Supplementary Note 9).

We first crosschecked the temperature change at both S- and D-stripes since the SSE is directly proportional to the temperature

Fig. 4 | Possible mechanisms of the magnon FET device. a Non-local V_{nl}^{DC} amplitude as a function of the gate voltage V_g for device 2 (gate stripe width: -526 nm), with an on/off ratio of $-115%$ for ± 200 V. Error bars are obtained from repeated measurements for each V_g . **b** Temperature variation ΔT of the S-stripe (green curve) and D-stripe (blue curve) relative to the ambient temperature for

different V_g , with a 0.5-mA DC current applied to the S-stripe. The red curve represents the temperature difference between S- and D-stripes. c Voltagedependent strain curve along the [100] direction for the PMN-PT(011) substrate, exhibits a mainly symmetric butterfly-shaped feature. d–f Local SSE signals for three stripes of $V_g = \pm 200$ V, respectively.

difference ΔT between them. We measured the local temperature variation of these two stripes via the corresponding resistance change. With a DC electric current applied to the S-stripe, both stripes exhibit temperature increases (Fig. 4b). And the resistance remains almost constant under a certain DC current (Supplementary Note 10). Although the gate voltage pulse-dependent ΔT for both S-stripe and D-stripe exhibits a minute hysteresis loop feature, the temperature difference between them remains almost unchanged with different V_{σ} values. Thus, we can exclude the change in temperature gradient as the dominant source for the observed gate voltage controlled magnon transport modulation. Secondly, we examined the electric field induced lattice strain. The electric field dependent strain measurement in PMN-PT(011) exhibits a typical butterfly-like feature (Fig. 4c) with a tiny remanence, which cannot explain the large nonvolatile modulation of V_{nl}^{DC} , either. Thirdly, we checked the electric field pulse dependent magnon current generation and detection efficiency at the S- and D-stripes, as well as the absorption at the G-stripe. We performed local SSE measurements for all S-, D-, and G-stripes with out-ofplane temperature gradients⁴³. After applying $V_g = \pm 200$ V at the Gstripe, the local SSE signals were almost unchanged at both S- and D-stripes (Fig. 4d, f), which indicates that the electric field at the G-stripe has little influence on both generation and detection of magnon current in the non-local geometry. Although there is a discernible difference in the local SSE signals of the G-stripe itself, the relative change ratio (~41%) is much smaller compared to the ~115% ratio of $V_{\rm nl}^{\rm DC}$ amplitude presented in Fig. 4a. This change actually can be attributed to the simultaneously measured resistance change of the G-stripe (5.07 kΩ for -200 V and 4.36 kΩ for $+200$ V) and the subsequent heating power change. Therefore, we conclude that the absorption by the G-stripe plays a minor contribution to the modulation of the lateral magnon current with the electric field gating. Furthermore, we measured the spin Hall magnetoresistance (SMR) of Pt G-stripe with gate voltage $V_g = \pm 200$ V, and the SMR ratio of Pt/YIG is almost independent of the gate voltage (Supplementary Note 11). Since the SMR measurement indicates the reflection property of the spin current (generated in Pt) at the YIG/Pt interface, it suggests that the property of the YIG/Pt interface has no apparent change upon applying different gate voltages. In addition, we performed piezoelectric force microscopy (PFM) measurements on our PMN-PT substrate, and the results evidenced that the ferroelectric domain can be switched in a nonvolatile manner by the electric field (Supplementary Note 12), consistent with the reports in literature⁴⁴. This suggests a possible link between the modulation of the magnon transport and the change of the local electric polarization.

Since the efficiencies of the magnon current generation, detection, and absorption at both the S- and D-stripes are basically immune to the gate voltage, we attribute the modulation of V_{nl}^{DC} in our magnon-FET to the changes in magnon relaxation within the YIG channel. Although magnetoelectric coupling between YIG and PMN-PT can result in a change of magnetic anisotropy in YIG, the anisotropy change itself cannot account for the modulation of the magnon current. Firstly, in the uniform magnetic state with an external magnetic field, the anisotropy field increases the energy of the magnons with small wavevectors near the easy axis but has little influence on the energy of magnons with large wavevectors 45 . As a result, the group velocity should be slower for the easy axis. One would expect that the magnon diffusion length is shorter in the direction of the magnetic easy axis rather than the reverse. Secondly, the anisotropy mainly influences the group velocity with the small wavevectors, while the energy of magnons is dominated by the exchange and dipole terms for the large wavevector. Since SSE is a broadband excitation technique with magnon frequency up to a few THz range⁴⁶, the magnons throughout the whole Brillouin zone will be thermally excited. Thus, the anisotropy change can hardly have discernible modulation on the

Table 1 | Comparison of different three-terminal lateral magnon transistors

	Materials	Working temperature	Control manner	On- off ratio	Nonvolatile
Chumak et al. 23	YIG	300 K	Microwave	$~10^{5}$ %	No
Cornelissen et al. 31	YIG	250 K	Current	1.6% m _A	No
Das et al. 32	YIG	300 K	Magnetic field	18%	No
Chen et al. 27	MnPS ₃	2K	Current	∞	No
This work	YIG	300K	Voltage	400%	Yes

magnon group velocity for the uniform magnetic state. Therefore, we speculate that the modulation of exchange stiffness and/or magnon relaxation in YIG may influence the magnon transport behavior in our magnon FET. Interestingly, anisotropic exchange stiffness has been proposed to explain the anisotropic magnon transport in ferromag-netic insulator and antiferromagnet insulator very recently^{45,[47](#page-6-0)}. We propose that the modulation is due to polarization-dependent ions $(Fe³⁺$ or $O²⁻)$ accumulation in the YIG layer adjacent to the interface, resulting in different coupling since Fe^{3+} is magnetic while O^{2−} is not. Dynamically, the magnon in YIG could couple with the collective excitation, such as the ferron or phonon in PMN-PT (Supplementary Note 13). A theoretical model about the influence of these couplings on the magnon transport is presented in Supplementary Note 14. Besides, noncollinear spin textures excited by magnon can induce electric dipoles $21,22,48$ $21,22,48$, which could couple with the ferroelectric polarization of the PMN-PT substrate and influence the magnon propagation. The found large modulation of magnon flow with the electric field gating could also originate from the joint effect of multi-mechanisms. Due to differences between devices, however, it would be difficult to obtain the magnon diffusion length with different gate voltages at the present stage. Future studies like Brillouin light scattering^{49–[51](#page-6-0)} or inelastic neutron scattering $52,53$ might be helpful to reveal the effect.

It is important to note that the FE substrate used in the devices discussed here has a thickness of 0.5 mm, necessitating a high voltage of 200 V for gating. It has been shown that the gate voltage can go down to the volt range when the thickness of the PMN-PT decreases down to μm range⁵⁴. It is interesting to note that PMN-PT single crystal films can be grown on silicon substrates with the thickness up to μm range⁵⁵, which is compatible with current semiconductor technology. As the strain releases with increasing thickness, it can be expected that the epitaxial PMN-PT film with μm thickness grown on a matched substrate will have similar lattice constants as the bulk PMN-PT substrate. Thus, the YIG films grown on both PMN-PT films and substrate are expected to have similar structure properties and magnon transport properties. The growth temperature of high-quality PMN-PT film, however, is beyond the maximum temperature that we can reach in our sputtering system. Instead, we deposited a 400-nm PZT (PbZr_{0.52}Ti_{0.48}O₃) film on an Nb-doped SrTiO₃(001) substrate and further prepared a magnon field effect transistor. This device exhibits ~28% on/off ratio under ±10 V gate voltages (Supplementary Note 15). Furthermore, the current devices have been demonstrated at a lateral size of a few μm. Given that the magnon current exhibits exponential decay with propagation distance and the gating only requires switching of an FE domain, it can be anticipated that the device can be readily scaled down in lateral size.

Lastly, we briefly compare the reported three-terminal lateral magnon transistors so far. Ideally, a good magnon transistor candidate should have a large on/off ratio for sensitivity, can be triggered by electric field and be nonvolatile for energy efficiency, and work in a

wide temperature range including room temperature. As summarized in Table 1, some reported devices either have large on/off ratio 23,27 23,27 23,27 or have room temperature functionality $23,32$. But none of them possesses all the above-mentioned characters of an ideal magnon transistor as presented in this work. Thus, our demonstrated device has clear advantages in comparison with previously reported ones.

In conclusion, we have successfully shown that the magnon transport in a structure consisting of a ferrimagnetic/ferroelectric insulator bilayer can be efficiently controlled by an electric field at room temperature, achieving a high on/off ratio of ~400%. This achievement represents the realization of the long-awaited threeterminal magnon field effect transistor. In combination with the nonvolatility nature of the device, the ability to manipulate magnon transport using an electric field provides significant advantages in terms of energy efficiency. These findings open up new possibilities for the development of energy-efficient magnon based logic devices and hold promising potential in unconventional computing schemes like neuromorphic computing.

Methods

The ~60-nm YIG films were deposited on PMN-PT(011) substrates with RF magnetron sputtering with a post-annealing under 800 °C for 4 h in air atmosphere. Three Pt stripes with a thickness of 3.5 nm (lateral dimensions of -550 nm \times 15 µm) were fabricated using e-beam lithography, DC magnetron sputtering, and lift-off techniques. Finally, Ti(5 nm)/Au(100 nm) and Ti(5 nm)/Au(200 nm) stripes were successively prepared with DC magnetron sputtering and lift-off techniques as the top and bottom electrodes, respectively. All depositions were performed at room temperature. The 400-nm PZT films were deposited on Nb-doped SrTiO₃ (001) substrate by RF magnetron sputtering with the following conditions: 550 \degree C, Ar/O₂ ratio of 15/1, and a total pressure of 10 mTorr.

All electric measurements were performed at room temperature using our home-built transport measurements system with a rotatable magnetic field. Harmonic non-local measurement. An AC current with the amplitude I_{ω} = 0.71 mA and frequency f = 15.7 Hz was injected into the S-stripe with a Keithley 6221 current source, and the firstharmonic and second-harmonic signals at D-stripe were measured by an SR860 lock-in amplifier from Stanford Research Systems. DC nonlocal measurement. A DC current was supplied to the S-stripe with a Keithley 6221 current source, and the voltage across the D-stripe was measured by a Keithley 2182A voltmeter. The electric field between the G-stripe and bottom electrode was applied by a Keithley 2400 source meter. All measurements were performed after the gate voltage is removed. Local spin Seebeck effect (SSE) measurement. The local SSE signals were obtained by applying AC current $I_{\omega} = 0.50$ mA with frequency $f = 1.37$ kHz to each stripe and detecting the secondharmonic signal by an SR860 lock-in amplifier. Piezoelectric force microscopy (PFM) measurement.The ferroelectric domain switching measurements of our PMN-PT single crystal substrate were performed by PFM (Bruker, Dimension Icon).

Data availability

All data supporting the findings of this study are available within the main text and the Supplementary Information file. The data that support the findings of this study are available from the corresponding authors upon reasonable request. Source data are provided with this paper.

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Author contributions

J.C., R.Y., L.S. contributed equally to this work. H.F.D. supervised the project. J.C., R.Y., L.S., K.H., X.Y. grew the films. J.C., R.Y. prepared the pattern with the lithography. J.C., R.Y., B.F.M., M.Y., Z.Y.Z. performed the measurements. X.L.H., F.Z.H., X.M.L. made the contribution of the PFM measurements and analysis. P.C., H.L.C. performed the ferroelectric measurements and analysis. J.C., R.Y., B.F.M., L.S., H.F.D. analyzed the data. H.Y.Y., J.C., H.F.D. proposed a possible mechanism for the coupling of ferrimagnetic insulator and ferroelectric material. J.C., R.Y., L.S., K.H., T.Z.J., M.Y., Z.Y.Z., X.L.H., H.N., X.Y., P.C., G.C., J.X., F.Z.H, X.M.L., H.L.C., H.Y.Y., B.F.M., and H.F.D. participated the discussions. J.C., R.Y., B.F.M., L.S., H.F.D. wrote the original manuscript. All authors contributed to the interpretation and editing of the manuscript.

Competing interests

The authors declare no competing interests.

Additional information

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Correspondence and requests for materials should be addressed to Huaiyang Yuan, Bingfeng Miao or Haifeng Ding.

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