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# Addressing Reliability and Degradation of Chemitransistor Sensors by Electrical Tuning of the Sensitivity

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Here we show that electrical tuning of the sensitivity of chemitransistor sensors, namely field-effect-transistors (FETs) exploiting nano/mesostructured sensing materials, can be used to effectively address two chief problems of state-of-the-art gas sensors, specifically fabrication reliability and degradation by aging. Both experimental evidences and theoretical calculations are provided to support such a result, using as a case-of-study junction field-effect-transistors (JFETs) exploiting mesostructured porous silicon (PS) as sensing material (PSJFETs) for the detection of nitrogen dioxide  $(NO<sub>2</sub>)$  at hundreds ppb. Proof of concept is given by fully compensating the effect of fabrication errors on the sensitivity of two PSJFETs integrated on the same chip, which, though identical in principle, feature sensitivities to  $NO<sub>2</sub>$  differing from about 30% before compensation. Although here-demonstrated for the specific case of PSJFETs, the concept of sensor reliability/aging problem compensation by sensitivity electrical-tuning can be applied to other chemitransistor sensors that exploit sensing materials different than PS.

 $\bigotimes$  is one of the most dangerous air-pollutants affecting human health and safety already at concentra-<br>tions of a few part-per-million (ppm)<sup>1</sup>. The need for miniaturized, sensitive, and reliable sensors for NO<sub>2</sub><br>det tions of a few part-per-million (ppm)<sup>1</sup>. The need for miniaturized, sensitive, and reliable sensors for  $\rm NO_2$ detection, in particular, and for air-quality monitoring, in general, has significantly pushed the research in the field in last years. Besides the well-known "chemiresistor" concept<sup>2-4</sup>, the "chemitransistor" concept, based on changes of the current of a FET structure that exploits a meso/nanostructured material as extra gate electrode, is an interesting alternative for gas sensors, in general, and NO<sub>2</sub> detection, in particular<sup>5-7</sup>. A number of meso/ nanostructured materials, such as organic polymers<sup>8,9</sup>, porous silicon<sup>10,11</sup>, silicon nanowires<sup>12,13</sup>, carbon nanotubes<sup>14,15</sup>, and metal-oxide<sup>16</sup> nanowires<sup>17</sup>, have been successfully used. The key advantage of chemitransistors over chemiresistors is the amplified sensor response due to current modulation by the extra gate electrode<sup>18</sup>, which allows sensitivity and limit of detection to be improved up to the part-per-billion (ppb) range for  $NO<sub>2</sub>$  detection. Moreover, modulation of the extra gate voltage of chemitransistors has been demonstrated to allow tuning of the sensitivity to be effectively performed<sup>19-24</sup>. Nonetheless, aging effects and fabrication reliability, which lead to sensor sensitivity degradation over time and to different sensitivity values among identical (in principle, at least) sensors, respectively, still remain challenging for chemitransistor sensors, as well as for chemiresistor sensors, and represent a major bottleneck towards on-field applications.

In recent years there has been an intense research into self-healing and self-repairing materials (mostly polymer-based systems) capable of autonomously restoring either electrical or mechanical (or both of them in some cases) properties<sup>25–27</sup>. These smart materials are tremendously useful for enhancing structural safety, increasing lifetime, and improving environmental sustainability, with significant impact over a broad range of applications, from biology to electronics. As to electronics, self-healing digital circuits that rely on hardware redundancy or self-tuning digital circuits able to adapt circuit parameters in real-time have been proposed so far for addressing reliability problems and aging effects, respectively<sup>28,29</sup>.

Here we show, for the first time to our best knowledge, that real-time tuning of the sensitivity of chemitransistor sensors by controlling the electrical bias of the transistor gate-terminal can be used to effectively address two chief problems of state-of-the-art gas sensors, namely fabrication reliability and degradation by aging. Both experimental evidences and theoretical calculations are provided to support such a result, using as a case of study junction fieldeffect transistors (JFETs) with a mesostructured porous silicon (PS) extra gate (PSJFETs) for the room-temperature detection of nitrogen dioxide (NO2) in the range of hundreds ppb. Proof of concept is given by fully compensating the effect of fabrication errors on the sensitivity of two PSJFETs integrated on the same chip, which, though identical in principle, result to have sensitivities to NO<sub>2</sub> that differ by about 30% before compensation.

This paper clearly demonstrates that concepts of self-repairing (self-tuning) materials and electronics can be successfully applied to sensor design, thus envisaging a novel class of self-tuning gas sensors with increased lifetime, enhanced reliability, and improved environmental sustainability.

### **Results**

The PSJFET sensor is a  $p$ -channel JFET<sup>30</sup> modified with a mesostructured porous silicon (PS) layer that is integrated on top of the pchannel between the source  $(S)$  and drain  $(D)$  terminals (Fig. 1a)<sup>31</sup>. PS is a nano/mesostructured form of silicon, obtained by electrochemical erosion of crystalline silicon in acidic solutions under anodic polarization, that features peculiar characteristics very appealing for gas sensing applications, among which huge surface-to-volume ratio (over 500  $\,\mathrm{m}^2/\mathrm{cm}^3$ ), effective room temperature interaction with gas molecules, and compatibility with commercial silicon-based integrated processes $^{11}$ . The PSJFET sensor is characterized by two independent gates: a solid electrical gate (G), which allows the current of the JFET device to be electrically tuned, and a mesostructured sensing gate, which allows the JFET device to be provided with sensing capability. The two gates, electrical and sensing, independently operate on the depletion regions (also known as space charge regions because they are free of mobile charge carriers and only contain fixed charges)  $x_1$  at the p-channel/n-silicon interface and  $x_2$  at the PS/pchannel interface, respectively, thus affecting the JFET current (Fig. 1a). On the one hand, the electrical gate allows the depletion region width  $x_1$  at the p-channel/n-silicon interface to be electrically tuned, thus effectively modulating the p-channel thickness and, in turn, the current flowing in the p-channel independently of the analyte concentration (Fig. 1a and 2a). On the other hand, the sensing gate allows the depletion region width  $x_2$  at the PS/p-channel interface to be tuned upon adsorption of analytes in the PS layer itself, thus effectively changing the p-channel thickness and, in turn, the current flowing in the p-channel dependently on the analyte concentration, for a given gate voltage (Fig. 1a and 2b).

The PSJFET sensor is fabricated by defining aluminum source and drain comb-finger contacts on a silicon substrate, this latter consisting of a 2.4- $\mu$ m-thick p-type silicon layer on top of a *n*-type silicon substrate, then performing the selective electrochemical erosion of part of the p-type silicon between drain and source contacts to produce the mesostructured PS layer on top of the  $p$ -channel<sup>31</sup>. The electrochemical erosion step is controlled to obtain mesostructured



Figure 1  $|$  (a) Schematic cross-section of the PSJFET sensor and effect of the gate voltage and analytes adsorbed into the PS on the depletion regions  $x_i$  and  $x_2$ , respectively; (b) Optical microscope top-image of two PSJFET sensors highlighting source and drain interdigitated contacts, and (inset) magnification of one of the sensors in which the PS layer between contacts is clearly visible; (c) typical SEM cross-section image of the PS layer integrated between drain and source terminals of the PSJFET; (d) high-magnification SEM image of the PS layer in (c) highlighting the mesostructured morphology of the sensing gate.



Figure 2 | (a) Typical  $I_{PSJFET}$ - $V_{DS}$  output curves of the PSJFET in synthetic air for several gate voltages and (b) typical  $I_{PSJFET}$ - $V_{DS}$  output curves of the PSJFET at  $V_{GS} = 0$  V upon exposure to different NO<sub>2</sub> concentrations.

PS layers with uniform thickness  $t_{PS}$ , which can be tuned between 800 nm and 1200 nm. Consequently, the thickness of the crystalline pchannel underneath the PS layer ranges between 1600 nm and 1200 nm. Fig. 1b shows an optical microscope image of two PSJFETs, together with details (in inset) of the PS layer between source and drain terminals. Fig. 1c and 1d show typical Scanning Electron Microscope (SEM) cross-section images of the mesostructured PS layer integrated between drain and source terminals of the PSJFET. In Fig. 1d, which was acquired at high magnifications (500 kx), the mesostructured morphology of the PS layer (sensing gate) is clearly visible.

The PSJFET sensors are tested with  $NO<sub>2</sub>$  at concentrations in the range 0–500 ppb, using the flow-through technique. For all the experiments, flow-rate is maintained at 200 standard cubic centimeter per minute (sccm), synthetic air is used as carrier gas, relative humidity is set to zero, and temperature is set to  $30^{\circ}$ C. Both timeresolved (current versus time) and static (current versus voltage) measurements are performed for different  $V_{GS}$  values and  $NO<sub>2</sub>$  concentrations on a large set of silicon chips, each one integrating several PSJFETs with different channel width W and length L, so as to carry out a quantitative investigation of the PSJFET behavior both from electrical and sensing points of view.

Fig. 2a and 2b show typical output curves of the PSJFET, that is  $I_{PSIFET}$  versus  $V_{DS}$  for different  $V_{GS}$  values and  $NO_2$  concentrations, respectively. In the specific case of Fig. 2a and 2b, the output curves refer to a PSJFET sensor with channel length  $L = 10 \mu m$ , effective width  $W = 3.9$  mm, and PS thickness  $t_{PS} = 800$  nm. The output curves of the PSJFET sensor exhibit the typical behavior of a JFET device. For a given  $V_{GS}$  value and  $NO<sub>2</sub>$  concentration, the increase of the absolute value of the  $V_{DS}$  voltage initially produces a linear increase of the absolute value of the sensor current  $I_{PSFET}$  (linear region); by increasing the absolute value of  $V_{DS}$  beyond the linear region a saturation of the sensor current  $I_{PSFET}$  is observed (saturation region), especially evident in synthetic air and for the lower  $NO<sub>2</sub>$  concentrations. The effect of increasing  $V_{GS}$  is that of reducing the sensor current  $I_{PSIFFT}$  for any  $V_{DS}$  value, while only slightly increasing the output resistance (slope of the curves in the saturation region slightly decreasing) of the PSJFET sensor (Fig. 2a). The effect of increasing the  $NO<sub>2</sub>$ concentration is that of significantly increasing the sensor current  $I_{PSIFET}$  for any  $V_{DS}$  value, while reducing the output resistance (slope of the curves in the saturation region increasing) of the PSJFET sensor, especially at the higher  $NO<sub>2</sub>$  concentrations (Fig. 2b). In fact, the PS layer, which acts as a resistor in parallel to the  $p$ -channel of the JFET, has a conductivity whose value increases with the  $NO<sub>2</sub>$  concentration<sup>32</sup>.

According to the literature,  $NO<sub>2</sub>$  molecules act as strong acceptors at PS surface, thus producing an increase of the hole concentration (conductivity) of PS after adsorption of  $NO<sub>2</sub>$  molecules. For a given  $V<sub>GS</sub>$ , especially at the higher  $NO<sub>2</sub>$  concentrations, the increase of the conductivity of the PS layer significantly reduces PS resistance value and, in turn, the output resistance of the PSJFET sensor, thus accounting for non-saturation of the sensor current  $I_{PSIFET}$ .

Fig. 3a shows the typical relative current variation  $\Delta I_R$  of a PSJFET as a function of the  $NO<sub>2</sub>$  concentration for several  $V<sub>GS</sub>$  values, both in the linear ( $V_{DS}$  = -0.1 V) and saturation ( $V_{DS}$  = -3 V) regions (by definition  $\Delta I_R = (I_{PSIFET} - I_{PSIFET0})/I_{PSIFET0}$ , with  $I_{PSIFET0}$  the PSJFET sensor current in synthetic air). For a given  $V_{GS}$  value, the value of  $\Delta I_R$  linearly increases as a function of the NO<sub>2</sub> concentration  $([NO<sub>2</sub>])$  in the range 0–500 ppb, at least in a first-order approximation, in both the working regions. Moreover, for a given  $NO<sub>2</sub>$ concentration, the higher the  $V_{GS}$  value the larger the value of  $\Delta I_R$ . Fig. 3b shows the PSJFET sensitivity, which is defined as  $S = \partial \Delta I_R$ /  $\partial[NO_2]$ , for different  $V_{GS}$  values, both in the linear and saturation regions. The value of S, for a given  $V_{GS}$  value, is evaluated from the slope of the linear function obtained by best-fitting the corresponding experimental curve in Fig. 3a. Remarkably, Fig. 3b clearly highlights that the PSJFET sensitivity can be effectively varied over a large dynamic range (up to about one order of magnitude) by properly modulating the electrical gate voltage  $V_{GS}$ . The higher the  $V_{GS}$  value the higher the sensitivity S, in both the working regions. Although the dynamic range of the PSJFET sensitivity does not significantly change between the two working regions, sensitivity values are larger in the saturation region than in the linear region, for any  $V_{GS}$  value. For instance, by changing the  $V_{GS}$  value between 0 V and 1 V the sensitivity S of the PSJFET in Fig. 3a can be varied of a factor 7.6, from  $0.1$  ppb<sup>-1</sup> to  $0.76$  ppb<sup>-1</sup>, in the saturation region, compared to a variation of a factor 7, from 0.04  $ppb^{-1}$  to 0.28  $ppb^{-1}$  in the linear region. Fig. 3b also shows best-fitting of experimental sensitivity data with a quadratic function, which allows to speculate the dependence of the PSJFET sensitivity on the square value of gate voltage  $V_{GS}$  for both the working regions, at least in the range of  $NO<sub>2</sub>$  concentrations under investigation. It is worth noting that the sensitivity tuning process does not produce any significant increase of the sensor power dissipation  $P_D (P_D = V_{DS} I_{PSI FET} + V_{GS} I_{GS})$  when the gate voltage is increased. In fact, thanks to the high-impedance of the gate terminal of JFET devices and, in turn, to the very low current  $I_{GS}$  (always much lower than  $I_{PSIFET}$ ) flowing through the gate terminal, the term  $V_{GS}I_{GS}$  is always much smaller than  $V_{DS}I_{PSFET}$ , which mainly accounts for the sensor power dissipation.



**Figure 3** | (a) PSJFET experimental relative current variation,  $\Delta I_R$ , for several NO<sub>2</sub> concentrations (0–500 ppb), at different V<sub>GS</sub> voltages (0–1 V), measured in the saturation ( $V_{DS}$  = -3 V) and (inset) linear ( $V_{DS}$  = -0.1 V) regions; (b) Sensitivity S of the PSJFET to NO<sub>2</sub> versus gate voltage  $V_{GS}$  in the linear and saturation regions; (c) Experimental  $\Delta I_R$  for several NO<sub>2</sub> concentrations (0–500 ppb), at different V<sub>GS</sub> voltages (0–1 V), measured in the saturation and (inset) linear regions for two namely identical PSJFETs integrated on the same chip; (d) experimental electrical compensation of the sensitivity of PSJFET #2, for both the linear and saturation regions, obtained by increasing the gate voltage  $V_{GS}$  of PSJFET #2 from 0 V to 0.2 V so as to achieve the superposition of  $\Delta I_{R^-}[\text{NO}_2]$  curves for the two PSJFETs of Fig. 3(c).

Fig. 3c shows the relative current variation  $\varDelta I_R$  of two PSJFETs as a function of the  $NO<sub>2</sub>$  concentration for several  $V<sub>GS</sub>$  values, both in the linear ( $V_{DS}$  = -0.1 V) and saturation ( $V_{DS}$  = -3 V) regions. The two PSJFETs, namely PSJFET #1 and #2, are simultaneously integrated on the same silicon chip and are in principle identical. However, due to unavoidable fabrication process tolerances, the sensitivity of the two sensors toward  $NO<sub>2</sub>$  results to be different, as it is clear from the calibration curves of Fig. 3c. In particular, the sensitivity value of the PSJFET #2 is always lower than that of the PSJFET #1 (see slope of the curves in Fig. 3c), for any  $V_{GS}$  voltage. The reduction of the sensitivity of PSJFET #2 with respect to PSJFET #1 can be fully compensated in real-time by properly setting the gate operation voltage  $V_{GS}$  of the PSJFET #2, so as to match the sensitivity value of the PSJFET #1. Fig. 3d shows the experimental proof of electrical compensation of the sensitivity value of PSJFET #2 to that of PSJFET #1 biased at  $V_{GS} = 0$  V. For instance, at  $V_{GS} = 0$  V, the two sensors feature sensitivity values that differ by about 30% before compensation (see curves for  $V_{GS} = 0$  V in Fig. 3c). By increasing the  $V_{GS}$  value of PSJFET #2 from 0 V to 0.2 V, the difference in sensitivity is fully compensated, both in the linear and saturation

regions, as it is clear from the superposition of the  $\Delta I_R$ -[NO<sub>2</sub>] curves in Fig. 3d over the whole range of  $NO<sub>2</sub>$  concentrations.

To the best of our knowledge, this is the first report demonstrating that chief issues of up-to-date gas sensors, such as fabrication reliability and aging effects, can be effectively addressed by adjusting the sensitivity of sensors in real-time. As proof of concept, in this work compensation of the effect of fabrication tolerances on the sensitivity of two PSJFETs integrated on the same chip is demonstrated. In fact, though identical in principle, the two PSJFETs feature different sensitivities to  $NO<sub>2</sub>$  before compensation, at a given  $V<sub>GS</sub>$  value, due to slightly different PS thicknesses. Electrical tuning of the sensitivity of one of the PSJFETs through  $V_{GS}$  allows the effect of fabrication tolerances to be fully compensated, so that the two sensors feature same sensitivity values after compensation. Once compensation of fabrication tolerances of gas sensors by sensitivity tuning is successfully demonstrated, compensation of sensitivity degradation with aging by electrical tuning of the gate voltage is straightforward. In fact, sensitivity degradation by aging can be addressed likewise sensitivity variation due to fabrication tolerances, at least until the sensor is able to reach a saturation over time. After the PSJFET sensor is



Figure 4  $\mid$  (a) Theoretical  $\Delta I_R$  as a function of the NO<sub>2</sub> concentration (0–500 ppb) for different  $V_{GS}$  values (0–1 V), calculated for the PSJFET of Fig. 3a both in the saturation ( $V_{DS} = -3$  V) and (inset) linear ( $V_{DS} = -0.1$  V) regions; (b) Theoretical electrical compensation of the sensitivity of the PSJFET #2 of Fig. 3c, for both the linear and saturation regions, obtained by increasing the gate voltage  $V_{GS}$  of PSJFET #2 from 0 V to 0.2 V so as to achieve the superposition of theoretical  $\Delta I_R - [NO_2]$  curves for the two PSJFETs of Fig. 3c.

fabricated and initially calibrated, aging effects usually induce a reduction of the slope of the calibration curve (i.e. the sensitivity), being the saturation values for aged sensors generally different from those of the initial calibration curve, at the same gas concentrations. Sensitivity degradation over time due to aging effects can be then compensated likewise sensitivity variation due to fabrication tolerances by properly tuning the gate voltage of the PSJFET sensor.

A semi-analytical model<sup>10</sup> of the PSJFET sensor is used to theoretically corroborate experimental results on the electrical tuning of the sensitivity by gate-voltage control. The model takes into account changes in both conductance  $G_{PS}$  and fixed-charge density  $Q_{PS}$  of the PS layer upon exposure to  $NO<sub>2</sub>$ . Under the assumption that these two effects give rise to two independent contributions to the sensor current, the PS is schematized with a resistance  $R_{PS} = 1/G_{PS} = L/$ ( $\sigma_{PS}$ <sup>.</sup> W<sup>+</sup> $t_{PS}$ ) (being  $\sigma_{PS}$  the PS conductivity) operating in parallel to a JFET with a p-channel cross-section that is reduced by a factor  $x_2 =$  $Q_{PS}t_{PS}/qN_A$ , being q the elementary charge and  $N_A$  the p-doping concentration. The current flowing into the PSJFET sensor  $I_{PSIFET}$  is therefore given by the sum of the current flowing into  $R_{PS}$  ( $I_{PS}$ ) and into the JFET  $(I<sub>IFET</sub>)$ , both depending on the NO<sub>2</sub> concentration. In addition, two resistors  $R_S$  and  $R_D$  in series to the JFET/ $R_{PS}$  branch are also included to model the silicon resistive paths from the external source (S) and drain (D) contacts to the PSJFET active region (right underneath the PS layer), and the contact resistances at the S and D terminals. An analytical equation can be written for  $I_{PSIFET}$  that theoretically describes the PSJFET electrical behavior as a function of both  $V_{DS}$  and  $V_{GS}$  voltages and  $NO_2$  concentration. This equation is used to perform the best-fitting of experimental current-voltage output curves measured for several  $V_{GS}$  values at different  $NO<sub>2</sub>$  concentrations so as to obtain quantitative data on  $G_{PS}$  and  $Q_{PS}$  as a function of the  $NO<sub>2</sub>$  concentration in the range under investigation and, in turn, on the variation of  $I_{PS}$  and  $I_{IFET}$  with NO<sub>2</sub>. Fig. S1 shows an example of theoretical (dotted lines) current-voltage output curves best-fitting the experimental (solid lines) curves, measured for one of the PSJFET used in this work. A good agreement between theoretical and experimental curves is clearly obtained for all tested  $V_{GS}$  values and  $NO<sub>2</sub>$  concentrations, thus validating the proposed semi-analytical model. Details of analytical equations describing the PSJFET and of the best-fitting procedure are provided in the section Methods, subsection Theoretical modeling and fitting.

known from the best-fitting of experimental data (Fig. S2), the effect of PS thickness variations  $\Delta t_{PS}$ , due to fabrication tolerances, on the  $I_{PSFET}$  current of different PSJFETs, integrated either on the same or different chips, can be easily calculated, being both  $R_{PS}$  and  $x_2$ depending on the thickness of the PS layer  $t_{PS}$ . Calculation of theoretical current-voltage output curves of PSJFETs with different PS thickness values is thus performed as a function of the  $V_{GS}$  value, for  $NO<sub>2</sub>$  concentrations in the range 0–500 ppb. Fig. 4a shows theoretical relative current variation  $\varDelta I_R$  of a PSJFET with PS thickness of 800 nm as a function of the  $NO<sub>2</sub>$  concentration for different  $V<sub>GS</sub>$ values, in the linear ( $V_{DS} = -0.1$  V) and saturation ( $V_{DS} =$  $-3$  V) regions. The theoretical output curves well agree with the experimental curves measured for the PSJFET of Fig. 3a. By considering a PS thickness variation of  $-1.5%$  for the PSJFET #1 and PSJFET #2 of Fig. 3c, according to SEM observation of the PSJFET cross-section, a theoretical analysis aimed at demonstrating sensitivity compensation of the two sensors through  $V_{GS}$  tuning is carried out. Fig. 4b shows theoretical compensation of the sensitivity value of PSJFET #2 to that of PSJFET #1 biased at  $V_{GS}$ =0 V. The compensation is obtained by increasing the gate-voltage value of the PSJFET #2 at 0.2 V, which allows a pretty perfect matching of the  $\Delta I_R$ -[NO<sub>2</sub>] curves of the two PSJFETs to be achieved, both in the linear and saturation regions. Fig. 4b well agrees with experimental compensation data in Fig. 3d, thus strongly corroborating electrical sensitivity compensation/tuning of PSJFETs from a theoretical point of view. In nanostructured chemitransistor gas sensors, the adjustment of the gate bias has been shown to induce a significant modulation of the chemical interactions that occur between sensing material and

Once  $G_{PS}$  and  $Q_{PS}$  as a function of the NO<sub>2</sub> concentration are

target analyte<sup>20–24</sup>. Consequently, gate bias also affects the charge transfer processes occurring between adsorbates and the nanostructured material, thus causing a variation of the sensor response times besides sensitivity tuning<sup>20-24</sup>. Conversely to state-of-the-art nanostructured chemitransistor sensors, which are mostly based on MOS (Metal Oxide Semiconductor) FET<sup>30</sup> structures that feature a strong interaction between electrical and sensing (the nanostructured material) gates mostly because of the presence of the insulating layer (capacitor effect), in the case of the PSJFET sensor the two gates, electrical and sensing, independently operate on the depletion regions  $x_1$  and  $x_2$  at the p-channel/n-silicon interface and at the

PS/p-channel interface, respectively. This has been demonstrated both with experimental data and theoretical analyses (see Ref. 10 and section Methods, subsection Theoretical modeling and fitting, of this manuscript). In the PSJFET sensor the voltage  $V_{GS}$  applied between gate and source terminals mostly drops across the depletion region  $x_1$ , at the p-channel/n-silicon interface, with two main advantages. The former is an efficient modulation of the width of the depletion region  $x_1$  with the  $V_{GS}$  value (typical of Junction FET structures), which allows the PSJFET sensitivity to be tuned with high efficacy. The latter is an effective minimization of the electrical interaction between gate terminal and sensing material, which allows to strongly reduce the effect of the gate voltage on the chemical interaction occurring between adsorbates and nanostructured material.

In order to experimentally investigate the dependence of the PSJFET response times on the  $V_{GS}$  voltage values, several PSJFET sensors able to work with a large dynamic range of  $V_{GS}$  values, from 0 to 10 V, are fabricated with a smaller PS thickness with respect to PSJFET #1 and #2. They are continuously tested for several days by monitoring the sensor current over time, in the linear region ( $V_{DS}$  =  $-0.1$  V) at constant NO<sub>2</sub> concentration (500 ppb), for different  $V_{GS}$ values (0 V, 1 V, and 10 V). Typical time-resolved curves of the PSJFET current over time for the different tested  $V_{GS}$  values are reported in Fig. 5a and 5b for two different PSJFETs (#3 and #4) integrated on the same chip. All the tested sensors show a reliable behavior independently of the gate voltage values, with the sensor current (absolute value) quickly increasing when  $NO<sub>2</sub>$  is injected in the test chamber to reach the saturation value in a few minutes, and quickly decreasing when  $NO<sub>2</sub>$  is removed from the test chamber to restore the quiescent current value. The quiescent current value (in synthetic air) of PSJFET #3 and #4 is significantly larger than that of PSJFET #1 and #2, and the absolute (and relative) current variation is smaller, due to the smaller PS thickness and, in turn, higher doping concentration of the p-channel (see Eq. 3 of section Methods, subsection Theoretical modeling and fitting) of PSJFET #3 and #4, compared to PSJFET #1 and #2, needed to ensure a larger dynamic  $V_{GS}$ range (up to 10 V) of investigation. A slight drift of the quiescent current over time is observed for all the tested sensors. Fig. 5c shows both rise and fall times (mean value and standard deviation) of PSJFET #3 and #4 for each tested  $V_{GS}$  value. A maximum variation of about 20% and 15% for the fall and rise time, respectively, is measured over a  $V_{GS}$  variation from 0 V to 10 V. Note that such a high variation of the gate voltage, up to 10 V, is usually neither necessary nor exploited for the sensitivity tuning process, for which a variation of the gate voltage from 0.1 V to 1 V (at least one order of magnitude below 10 V) is usually adequate. If the  $V_{GS}$  variability range is restricted to the voltage values from 0 V to 1 V, then the effect of the gate voltage on the response times of the PSJFET is negligible, at least within the experimental error of performed measurements.

#### Discussion

Summarizing, in this work, electrical tuning of the sensitivity of porous silicon (PS) junction field-effect transistor (JFET) gas sensors by bias-control of the JFET gate-terminal is shown to effectively address two chief problems of solid-state gas sensors, namely fabrication reliability and aging effects, which represent a major bottleneck towards in-field applications. Proof of concept is given by fully compensating the effect of fabrication errors on the sensitivity of two PSJFETs integrated on the same chip, which, though identical in principle, feature different sensitivities to  $NO<sub>2</sub>$  (about 30%) before compensation. The sensitivity tuning process does not significantly affect the response times of the PSJFETs over a large range (0 V– 10 V) of gate voltage. Although fabrication tolerance compensation of chemitransistor sensors by sensitivity tuning is here demonstrated for the specific case of the PSJFET, the general concept can be in



**Figure 5**  $\vert$  (a, b) Time-resolved curves (current versus time) for two PSJFETs biased in the linear region ( $V_{DS} = -0.1 \text{ V}$ ) upon exposure to NO<sub>2</sub> at concentration of 500 ppb, for different gate voltage values ( $V_{GS} = 0$  V, 1 V, 10 V); (c) rise and fall times of the two PSJFETs in (a) and (b) as a function of the gate voltage values in the range from  $V_{GS} = 0$  V to  $V_{GS} = 10$  V.

principle applied to other chemitransistor sensors that exploit sensing materials different from porous silicon.

This work breaks a new ground in the gas sensor arena by demonstrating that concepts of self-repairing (self-tuning) materials and

electronics can be successfully applied to sensor design, thus envisaging a novel class of self-tuning gas sensors with increased lifetime, enhanced reliability, and improved environmental sustainability.

#### **Methods**

Fabrication and experimental measurements. The fabrication process of the PSJFET consists of the following main steps: i) n-type silicon substrate with doping of  $10^{15}$  cm<sup>-3</sup>; ii) boron implantation and diffusion to form a p-type layer, 2.4  $\mu$ m deep with a maximum surface doping of  $10^{17}$  cm<sup>-3</sup>; iii) definition of aluminium interdigitated electrical contacts for drain and source terminals (1<sup>st</sup> mask); iv) aluminium evaporation for electrical gate contact formation on the back of the chip; v) source and drain contacts protection by using a photoresist mask ( $2^{\text{nd}}$  mask) hardbaked at 140°C for 30 minutes, which is a crucial step for making the resist layer able to withstand the subsequent electrochemical etching (anodization); vi) selective anodization in aqueous HF-based solution of the  $p$ -type material, through the photoresist-free spaces, to form a PS layer with uniform thickness. Several PSJFET sensors with channel length  $L = 10 \mu m$ , different channel width,  $W$ , ( $W_1 = 9.6 \mu m$ ,  $W_2$  = 5.8 mm, and  $W_3$  = 3.9 mm) and same nominal PS thickness are integrated on the same chip. Several chips are prepared using the same anodization current density and only changing the anodization time in order to produce PSJFETs featuring PS layers with different chip-to-chip thicknesses, ranging from 800 nm and 1000 nm, but same morphological mesostructured features. Each chip is then mounted on a TO12 metal-package and placed into a sealed chamber with temperature fixed at 30  $\pm$  $0.5^{\circ}$ C for testing.

The flow-through technique is used to expose the PSJFETs to a constant flux of 200 sccm alternating synthetic air and mixtures of synthetic air  $NO<sub>2</sub>$  at different  $NO<sub>2</sub>$ concentrations between 100 ppb and 500 ppb. The sensors are exposed to  $NO<sub>2</sub>$ mixtures for 30 min and then to synthetic air for 60 min. Response time, which is defined as the time required for the sensor output to change from its previous state to a final settled value within a tolerance band of  $\pm 10$ %, ranges from a few minutes to several minutes at room temperature for NO<sub>2</sub> adsorption and desorption cycles, respectively. Response times are estimated by monitoring the sensor current  $I_{PSIET}$ over time (up to a few days) for different NO<sub>2</sub> concentrations, at constant  $V_{DS}$  and  $V_{GS}$ values. The effect of the electrical gate on the response time is evaluated by monitoring the sensor current  $I_{PSFET}$  over time (up to a few days) for constant  $NO<sub>2</sub>$  concentration and  $V_{DS}$  value, and different  $V_{GS}$  values in the range from 0 V to 10 V. PSJFETs with different L and W values show similar qualitative behavior and a quantitative behavior that is coherent with the ratio W/L, as expected.

Output current-voltage curves  $I_{PSFET}$   $\!\!V_{DS}$  of the PSJFETs are measured using a source-measure unit (Keithley 2400), by sweeping the  $V_{DS}$  value between 0 V and  $-5$  V, with step of  $-0.05$  V, and simultaneously monitoring the current flowing between the drain and source terminals, for a given  $V_{GS}$  value. The effect of the electrical gate on the sensor current is measured by changing the  $V_{GS}$  voltage between 0 V and 1 V, with a minimum step of 0.1 V. Once the response times of the sensors at room temperature are known from the time-resolved analysis, the measurement of the output current-voltage curves are carried out by exposing the sensors to  $NO<sub>2</sub>$ mixtures for 30 min and then to synthetic air for 30 min.

Theoretical modeling and fitting. The PSJFET electrical behavior is described by a set of equations that include the dependence on the  $NO<sub>2</sub>$  concentration of the current flowing through the JFET and structure and the PS layer<sup>10</sup>.

As for the JFET structure, the current  $I_{I\#E T}$  can be modeled in the linear region by:

$$
I_{JFET\_lin} = k_1(V_{Di} - V_{Si}) + (3/4)k_2[(V_{bi} + V_G - V_{Di})^{4/3} - (V_{bi} + V_G - V_{Si})^{4/3}] \tag{1}
$$

where  $V_{Di}$  and  $V_{Si}$  are the drain and source voltages corrected of the voltage drop across the resistive paths  $R_D$  and  $R_S$  from external source and drain contacts to the JFET active region right underneath the PS layer, respectively,  $V_{bi}$  is the built-in voltage at the  $pn$  junction,  $k_2$  is a constant that depends on the doping profile of the  $pn$ junction, and  $k_1$  is a parameter that takes into account the geometrical dimension of the p-channel and the effect of the adsorbed  $NO<sub>2</sub>$  molecules on the depletion region  $x<sub>2</sub>$ at the PS/p-channel interface:

$$
k_1 = \frac{W}{\rho L} \cdot (h - x_2) \tag{2}
$$

where  $\rho$  is the resistivity of the channel, W, L, and h are the width, length, and metallurgical height of the *p*-channel, respectively, and  $x_2$  is given by:

$$
x_2 = \frac{Q_{PS}([NO_2])}{qN_A} \cdot t_{PS}
$$
\n<sup>(3)</sup>

being q the elementary charge,  $N_A$  the p-doping concentration of the channel, and  $t_{PS}$ the thickness of the PS layer.  $Q_{PS}$  is the fixed-charge density in the PS layer and represents the charge distributed on the PS layer surface and here macroscopically schematized as a volume charge uniformly distributed in the PS layer.

The pinch-off occurs when, at the JFET drain (Di), the sum of the depletion regions,  $x_1$  and  $x_2$  equals h. This occurs when  $V_{Di}$  reaches the value:

$$
V_{Di\_sat} = V_{bi} + V_G - (k_1/k_2)^3
$$
\n(4)

Under this condition, the JFET current reaches its saturation value, which is independent of any further increase of the drain voltage:

As for the current flowing through the PS layer  $I_{PS}$ , this is given by:

$$
I_{PS} = G_{PS}([NO_2]) \cdot (V_{Di} - V_{Si})
$$
\n(6)

where  $G_{PS}([NO_2]) = 1/R_{PS}([NO_2])$ , being  $R_{PS}([NO_2])$  the PS resistance.

Equations 1–6 are used to best-fit experimental output current-voltage curves for a number of PSJFETs, integrated both on the same and different chips, and extract quantitative information on the effect of NO<sub>2</sub> on the sensor current through  $G_{PS}$ ,  $k_1$ , and  $k_2$  that are used as fitting parameters. For all the devices  $R_S = R_D = 25 \Omega$ , which is estimated from the sensor geometrical dimensions and doping profiles. Fig S1 shows experimental output curves (solid lines) of a PSJFET for several  $V_{GS}$  values at different  $NO<sub>2</sub>$  concentrations superimposed to theoretical output curves (dotted lines) obtained by the best-fitting of experimental data. Fig. S2(a–c) show mean values of  $G_{PS}$ ,  $k_1$ , and  $k_2$  as a function of the NO<sub>2</sub> concentration, for the different  $V_{GS}$  values, resulting from the best-fitting procedure of data in Fig. S1, with error bars representing one standard deviation. As expected,  $k_1$  and  $G_{PS}$  are independent of the gate voltage  $V_{GS}$ , though they significantly depend on the NO<sub>2</sub> concentration. In particular,  $k_1$  increases from 0.99  $\times$  10<sup>-3</sup> A $\cdot$ V<sup>-1</sup> for [NO<sub>2</sub>] = 0 ppb to 1.38  $\times$  $10^{-3}$  A $\cdot$ V<sup>-1</sup> for [NO<sub>2</sub>] = 500 ppb; G<sub>PS</sub> increases from 9.7  $\times$  10<sup>-6</sup> S for [NO<sub>2</sub>] = 0 ppb to  $1.67 \times 10^{-4}$  S for  $[NO_2] = 500$  ppb. On the contrary,  $k_2$  only depends on the geometrical characteristics and doping profiles of the JFET structure, and its value is expected to be independent of both NO<sub>2</sub> concentration and gate voltage  $V_{GS}$ . In fact,  $k_2$  has a mean value of 7.9  $\times$  10<sup>-4</sup> A $\cdot$ V<sup>3/4</sup> for [NO<sub>2</sub>] between 0 ppb and 500 ppb, with a standard deviation of 0.2  $\times$   $10^{-4}$  A  $\rm V^{3/4}.$ 

Equations 2 and 3 are then used to extract information on  $x_2$  and, in turn, on  $Q_{PS}$  as a function of the  $\mathrm{NO}_2$  concentration. Remarkably, together with an increase of  $G_{PS}$ the model confirms that  $x_2$  reduces from 1.1  $\mu$ m for [NO<sub>2</sub>] = 0 ppb to 0.9  $\mu$ m for  $[NO<sub>2</sub>] = 500$  ppb, due to a reduction of the charge density  $Q<sub>PS</sub>$  in the PS layer from 1  $\times 10^{15}$  cm<sup>-3</sup> for [NO<sub>2</sub>] = 0 ppb to 8.1  $\times$  10<sup>14</sup> cm<sup>-3</sup> for [NO<sub>2</sub>] = 500 ppb (Fig. S2(d– e)). By best-fitting experimental data obtained for  $G_{PS}$  and  $Q_{PS}$  versus NO<sub>2</sub> concentration with a second-order polynomial function the following equations are obtained:

$$
G_{PS}([NO_2]) = 1.85 \cdot 10^{-10} \times [NO_2]^2 - 2.26 \cdot 10^{-7} \times [NO_2] + 9.75 \cdot 10^{-6} \tag{7}
$$

$$
Q_{PS}([NO_2]) = 1.97 \cdot 10^8 \times [NO_2]^2 - 4.56 \cdot 10^{11} \times [NO_2] + 9.94 \cdot 10^{14} \tag{8}
$$

with  $[NO<sub>2</sub>]$  in ppb (red curve in Fig.  $S2(a)$  and (d)).

Equations 7 and 8 are exploited to perform the quantitative evaluation of the effect of fabrication process tolerances, such as variation of the PS thickness with respect to the nominal value, on the sensitivity of PSJFET sensors. To this end,  $t_{PS}$  is defined as  $t_{PS} = t_{PS0}(1 + \Delta t_{rel})$ ,  $\Delta t_{rel}$  being the relative variation of the PS thickness with respect to the nominal value  $t_{PS0}$ . Being  $Q_{PS}$  by definition independent of the thickness of the PS layer,  $k_1$  and  $G_{PS0}$  can be expressed as:

$$
k_1 = \frac{W}{\rho L} \times (h - \frac{Q_{PS}}{qN_A} \cdot t_{PS}(1 + \Delta t_{rel}))
$$
\n(9)

$$
G_{PS} = G_{PS0}(1 + \Delta t_{rel})\tag{10}
$$

where  $G_{PS0}$  is the PS conductance value at  $\Delta t_{rel} = 0$ .

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# Author contributions

G.B. conceived and designed the research. G.B. and G.M.L. worked out the theory. G.M.L. and L.M.S. performed the experiments. All authors analyzed the data and wrote the manuscript.

# Additional information

Supplementary information accompanies this paper at [http://www.nature.com/](http://www.nature.com/scientificreports) [scientificreports](http://www.nature.com/scientificreports)

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