

Fig.1I

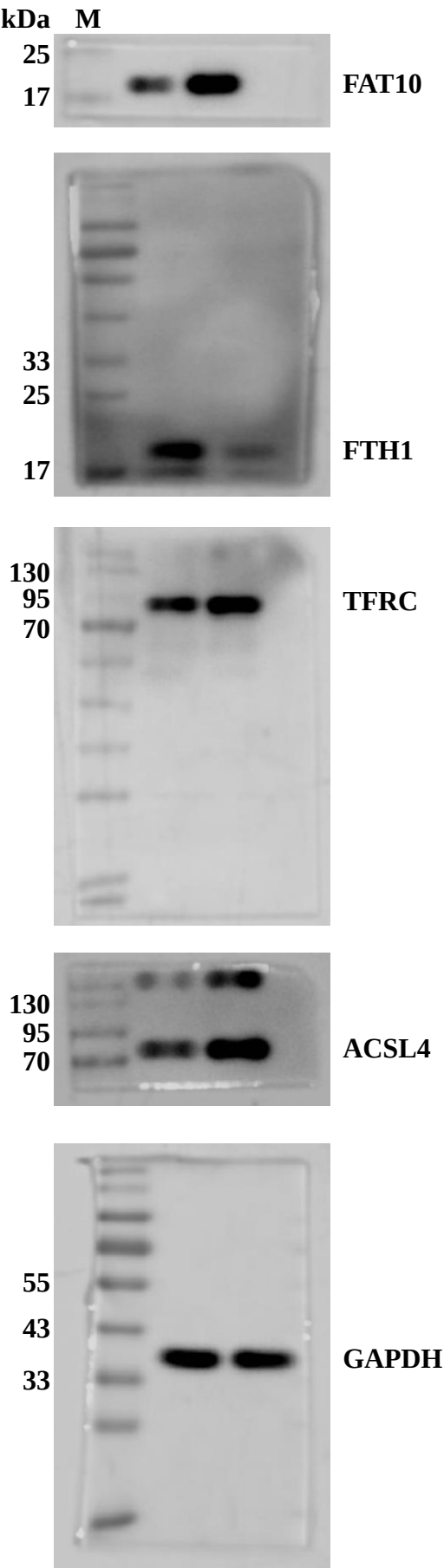


Fig.2A

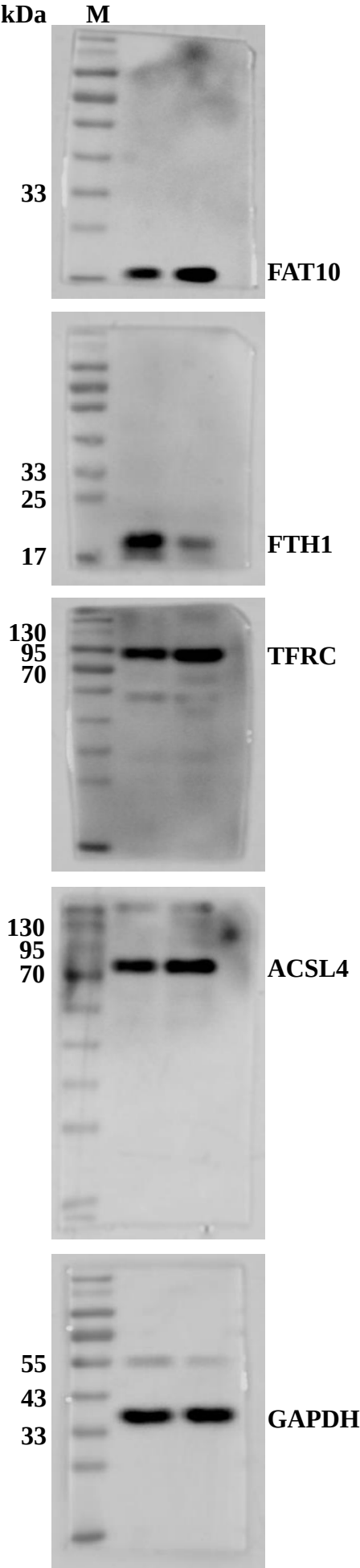


Fig. 2E

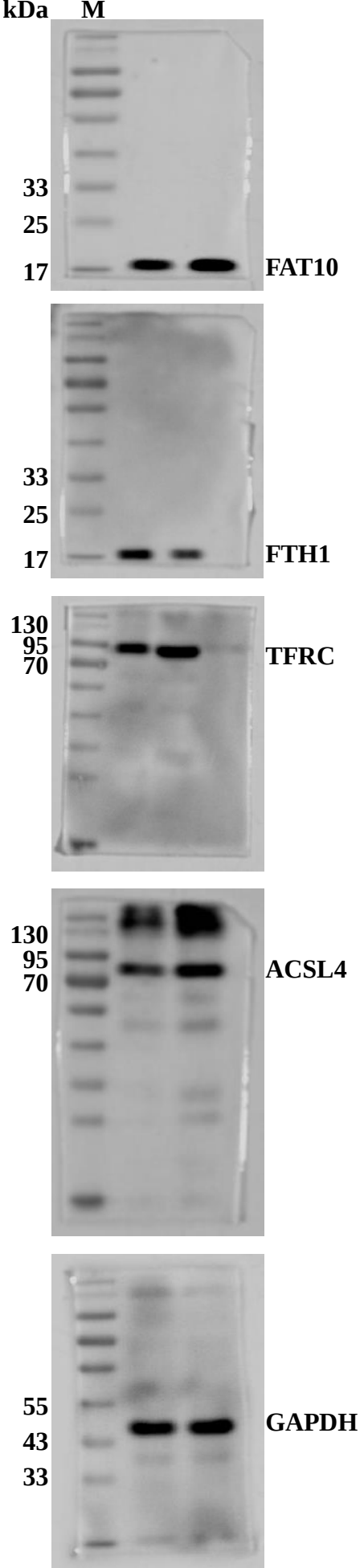


Fig.2I

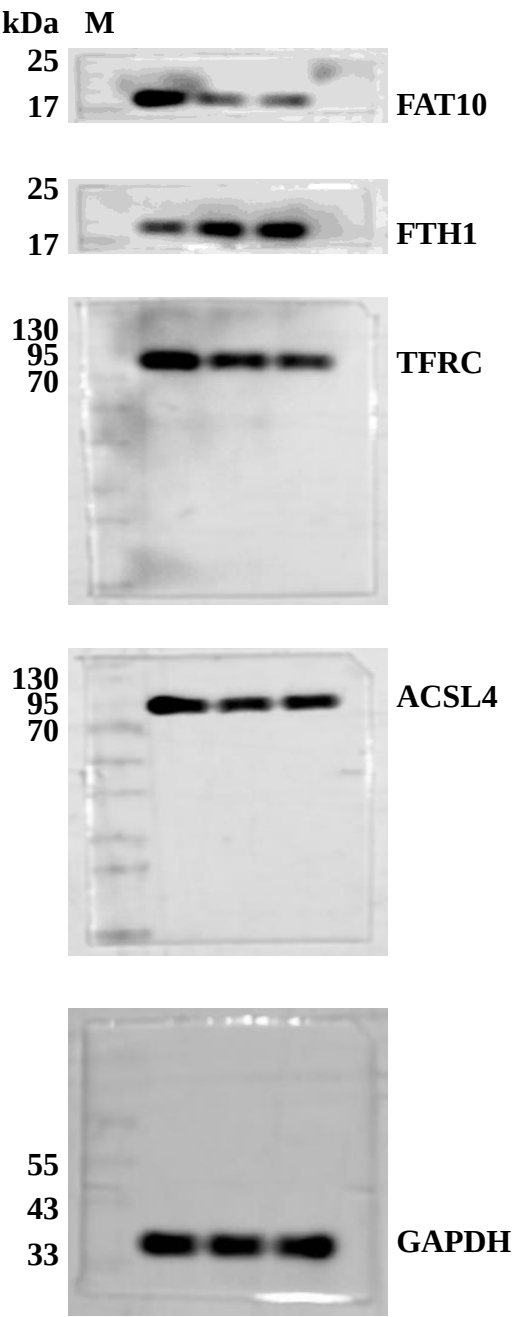


Fig.2M

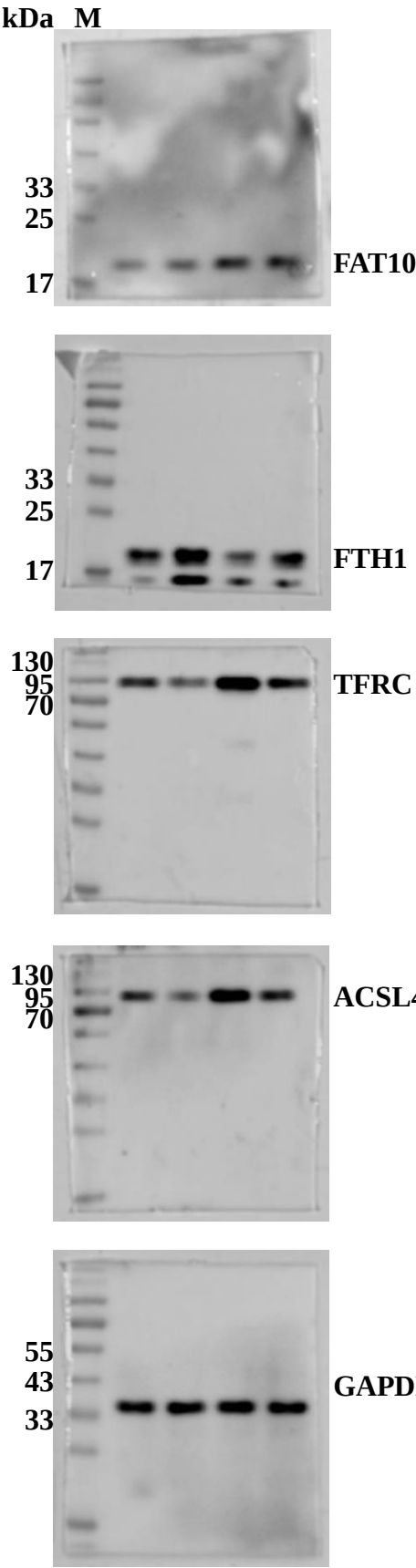


Fig.2N

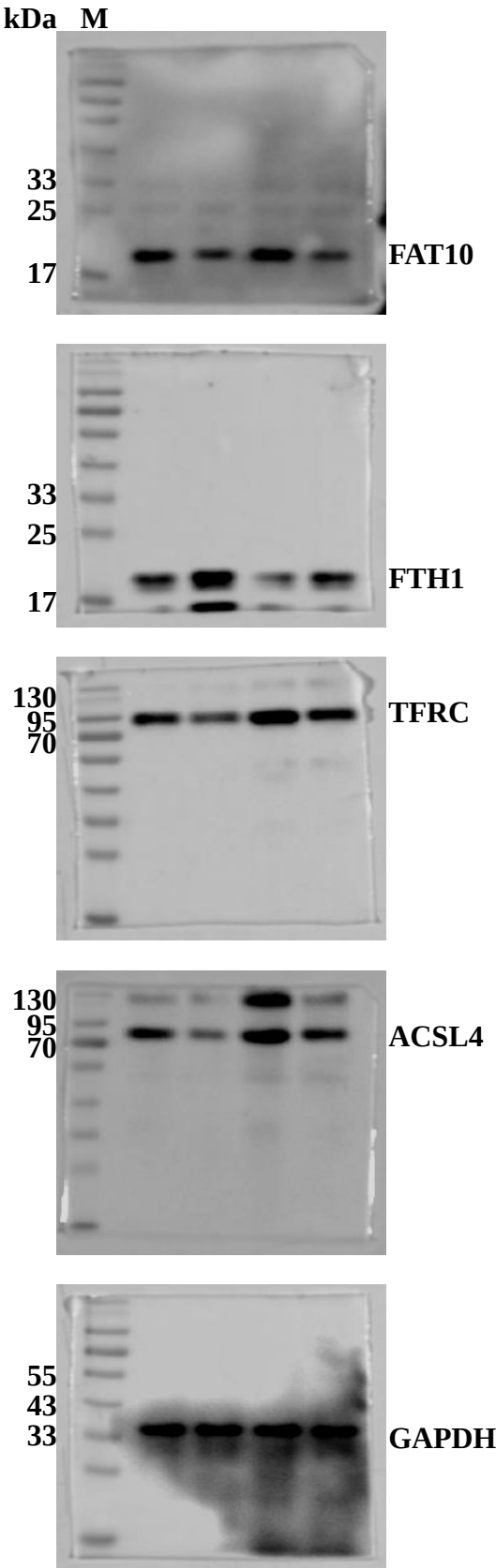


Fig.3A

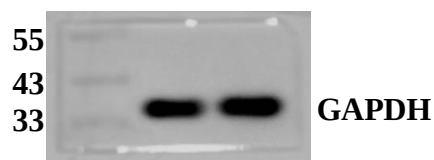
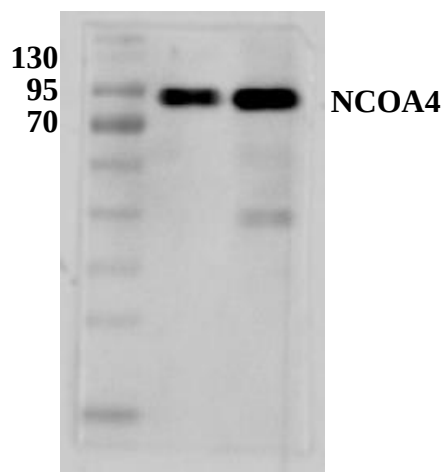
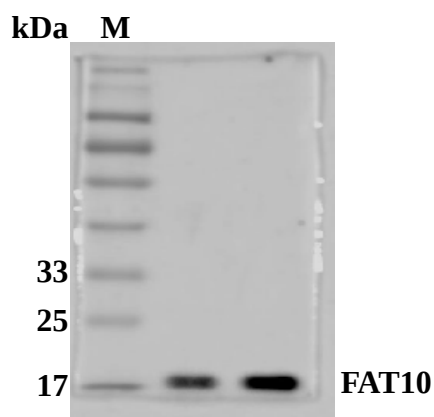


Fig.3B

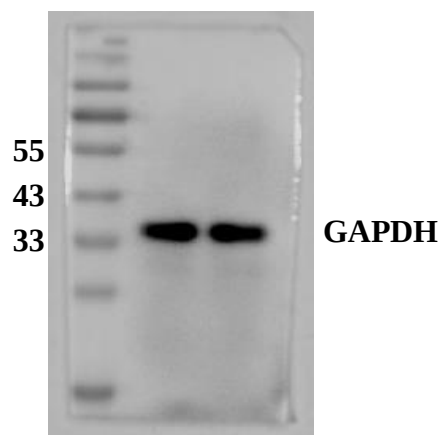
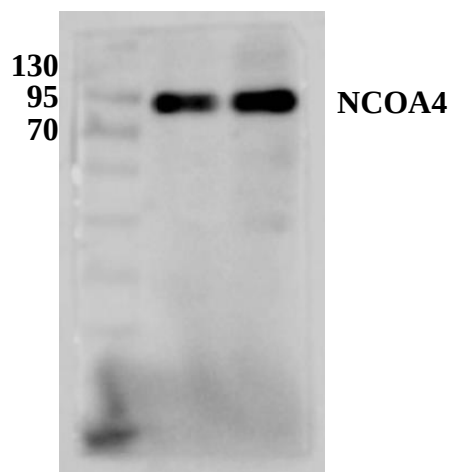
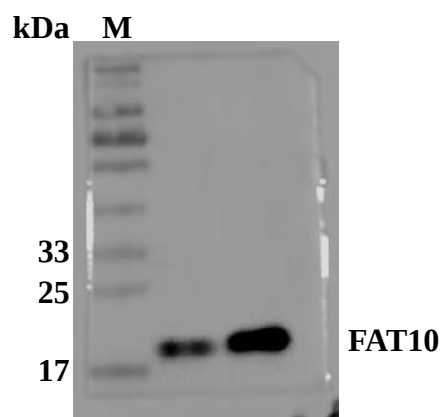


Fig.3C

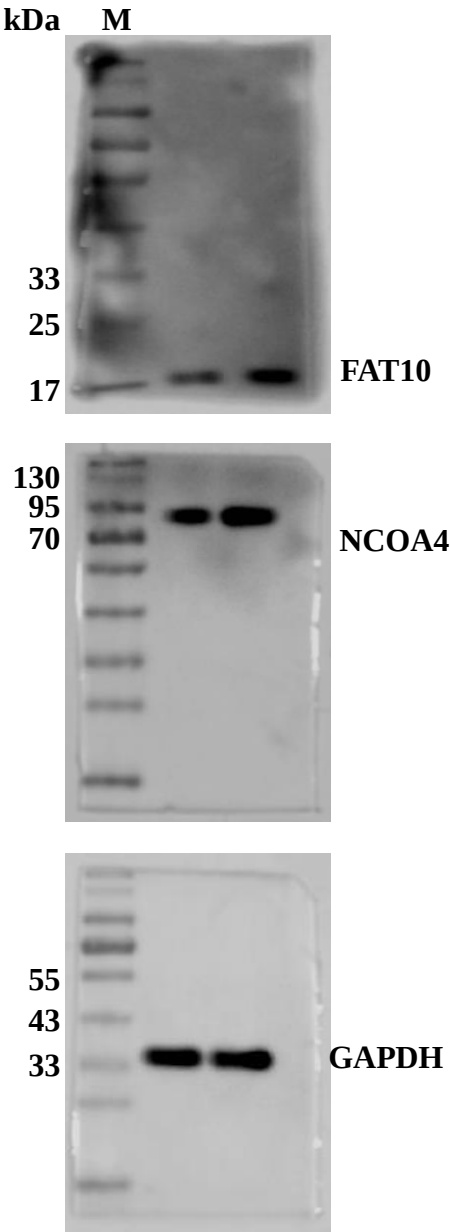


Fig.3D

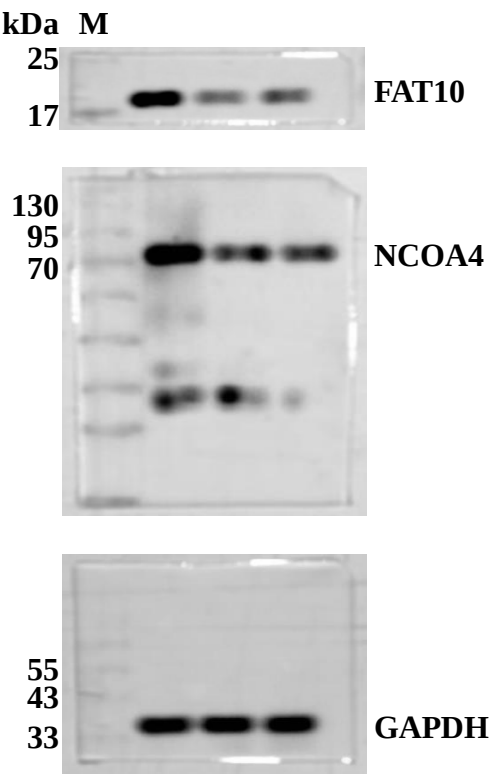


Fig.3E

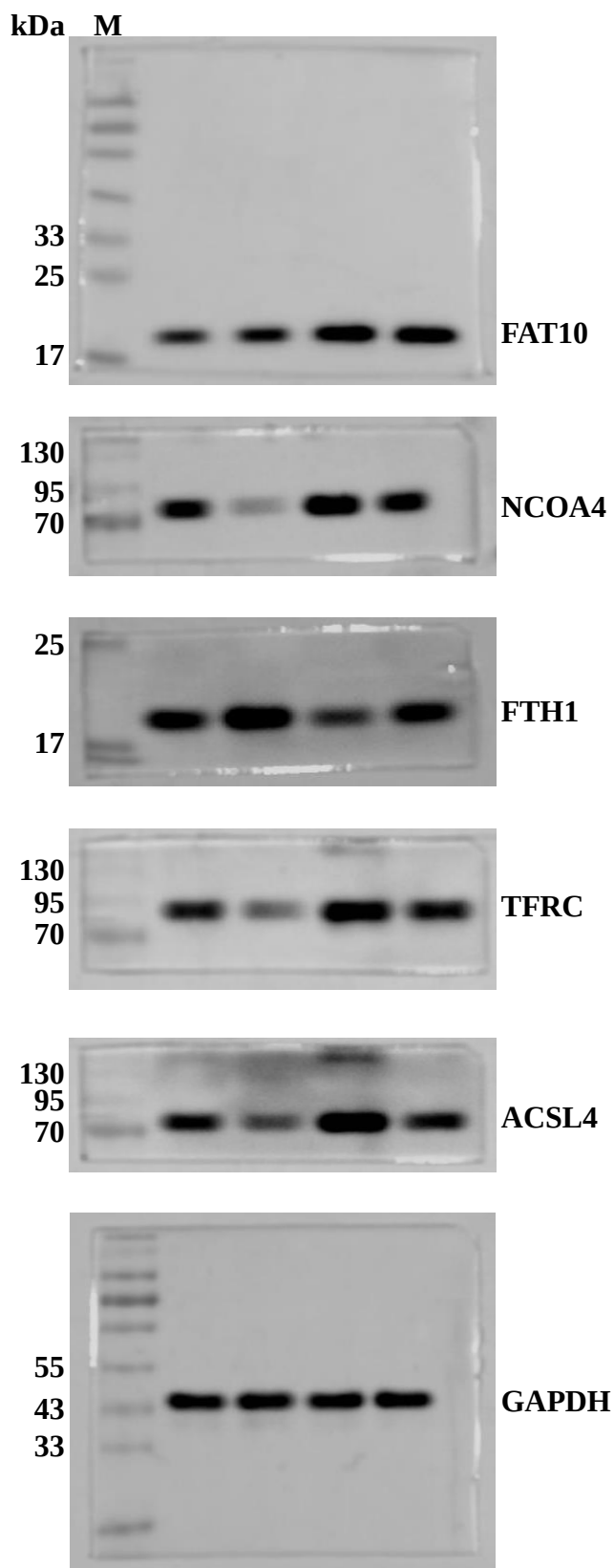


Fig.3H

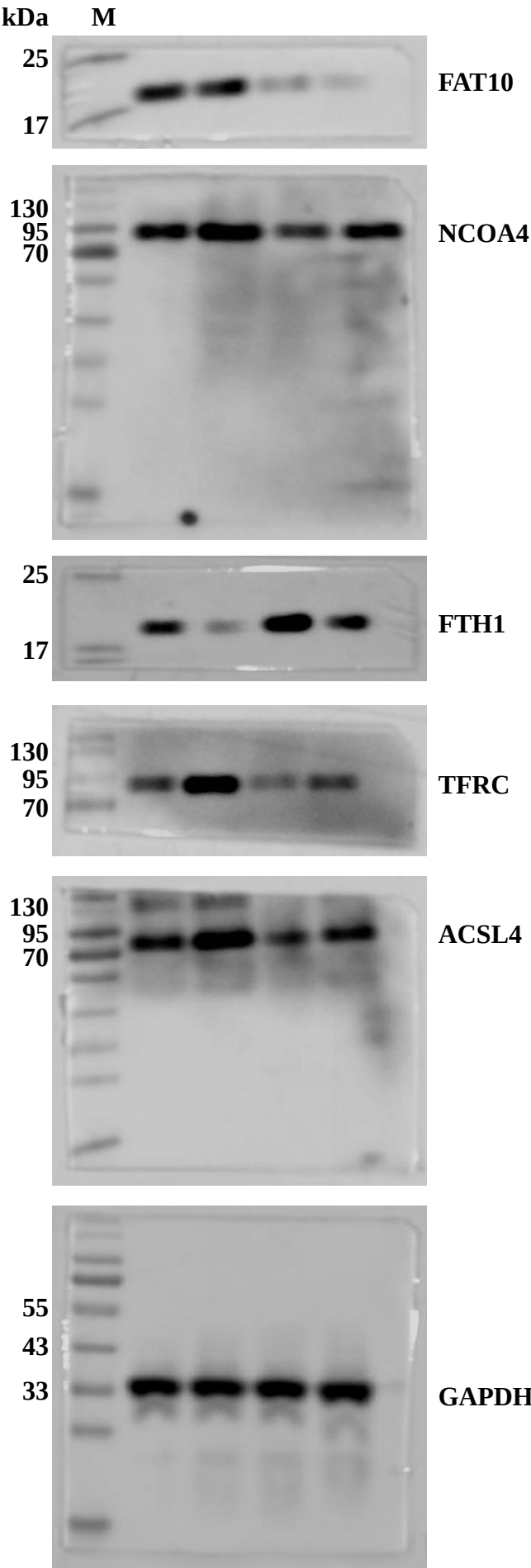


Fig.3K

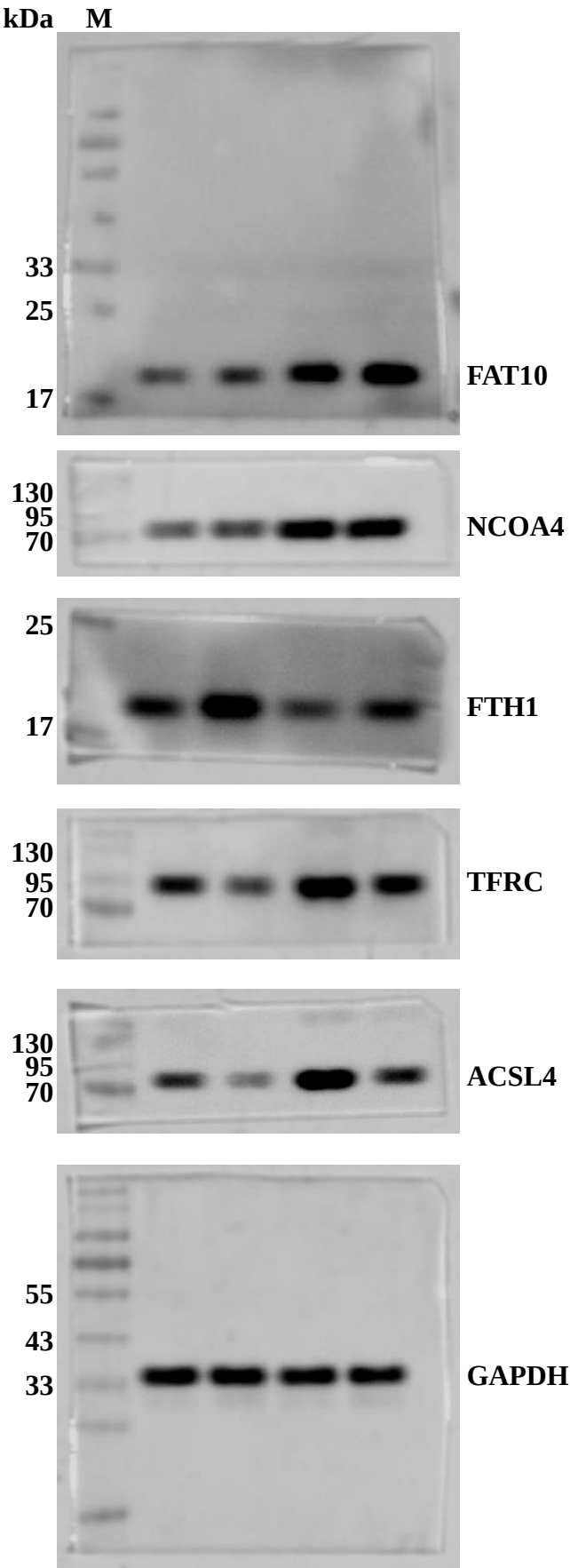


Fig.4A

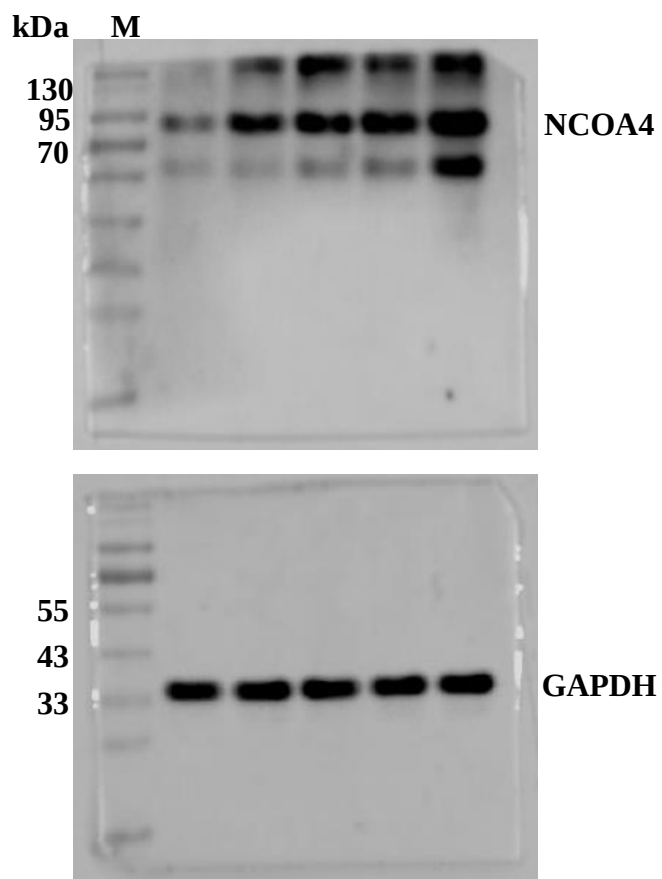


Fig.4C

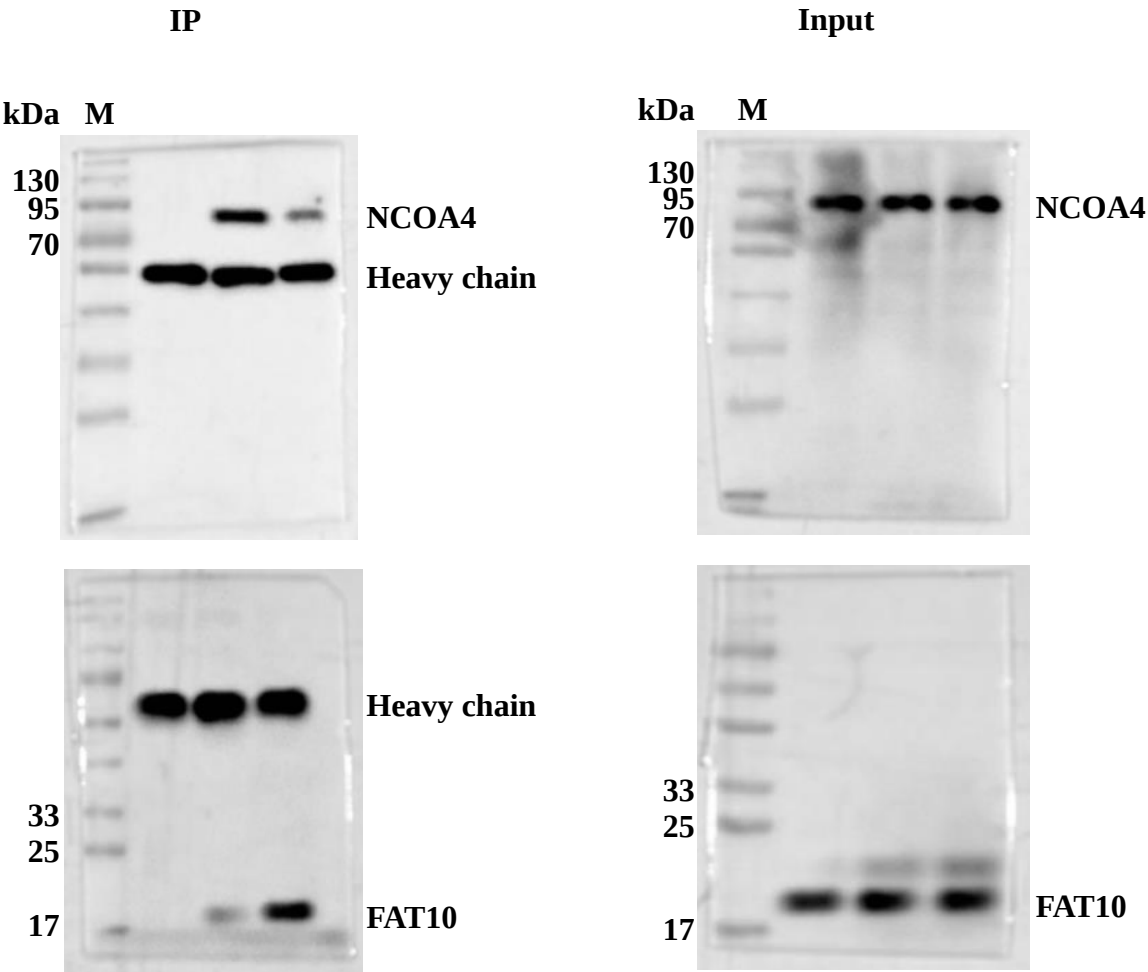


Fig.4D

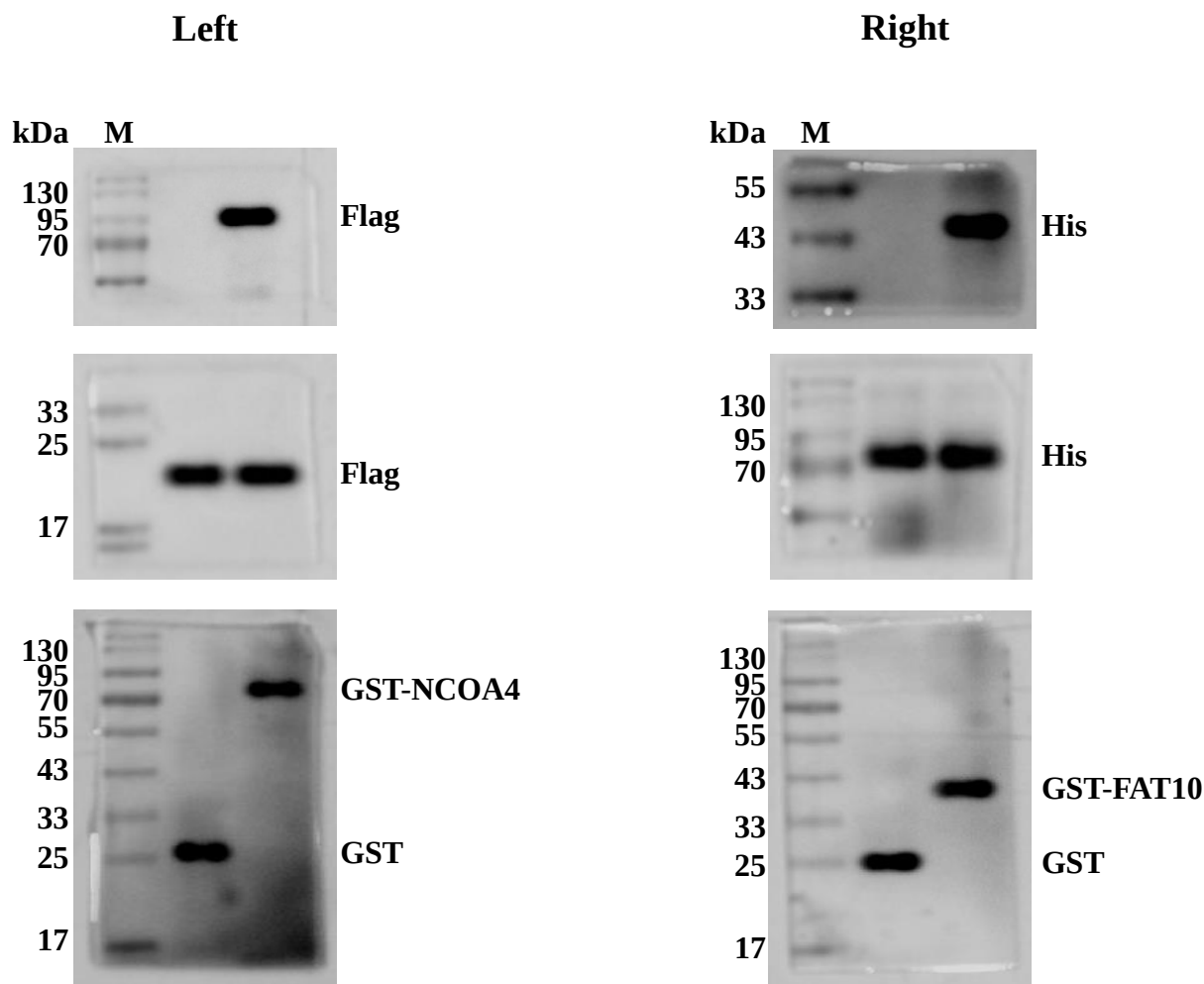


Fig.4F

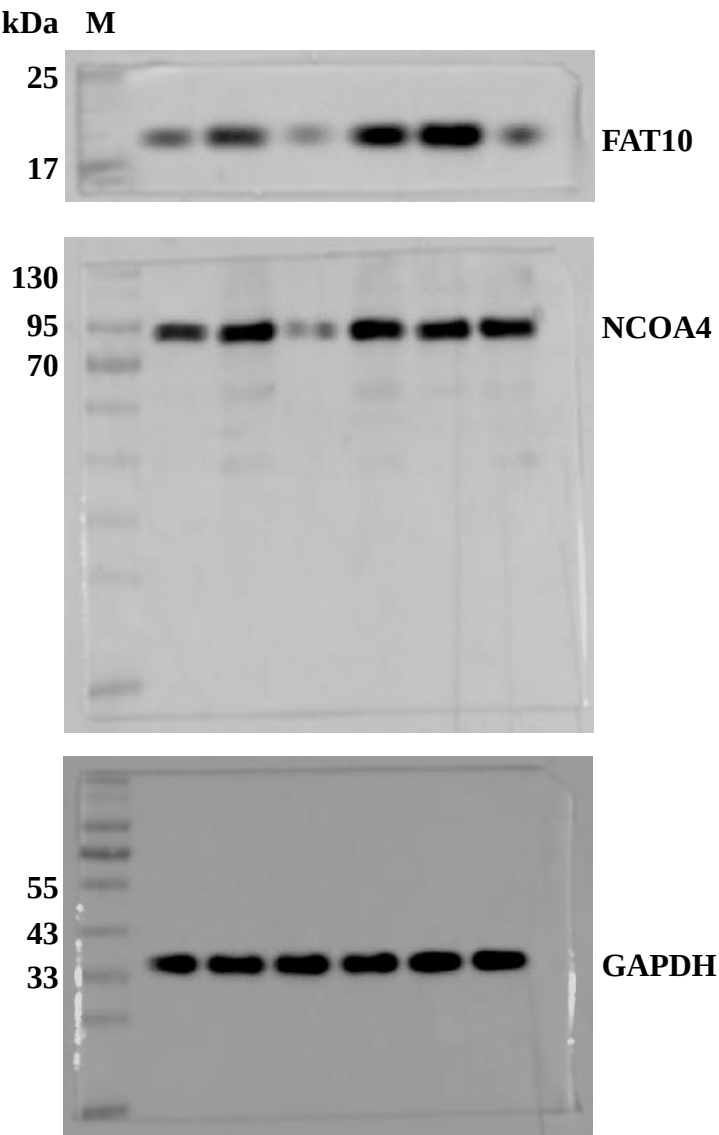


Fig.4G

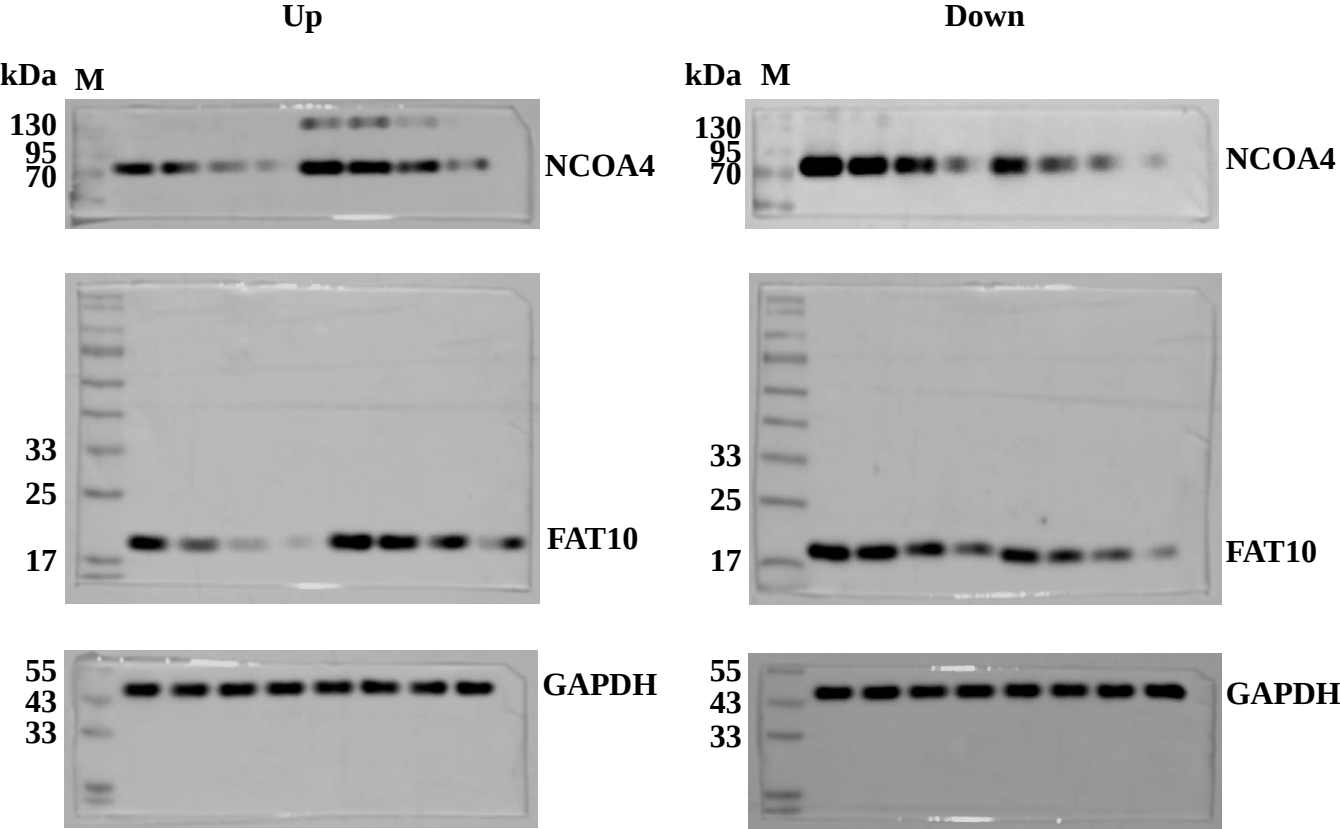


Fig.4H

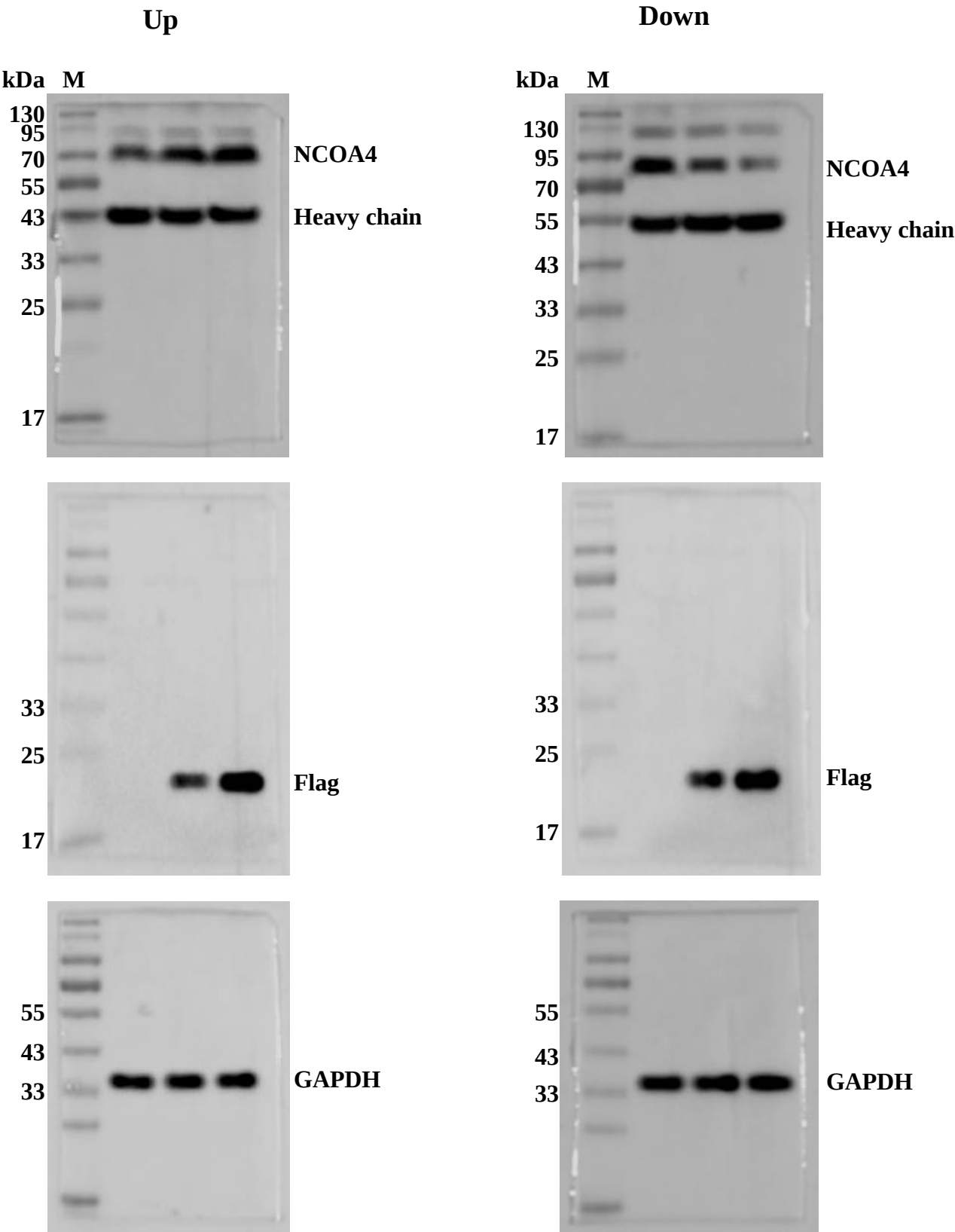


Fig.4I Left

IP

Input

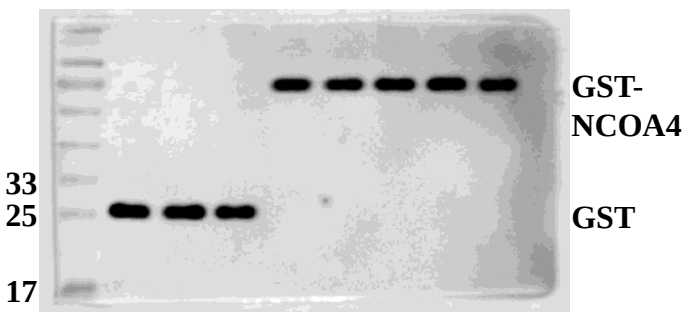
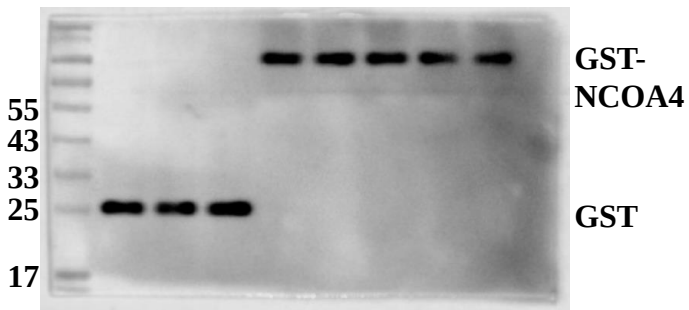
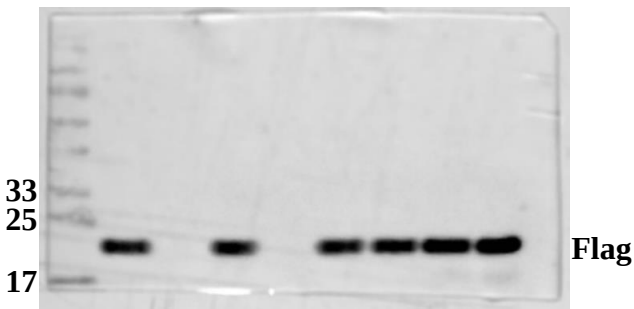
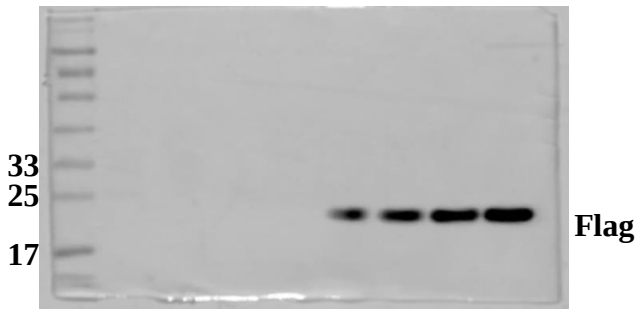
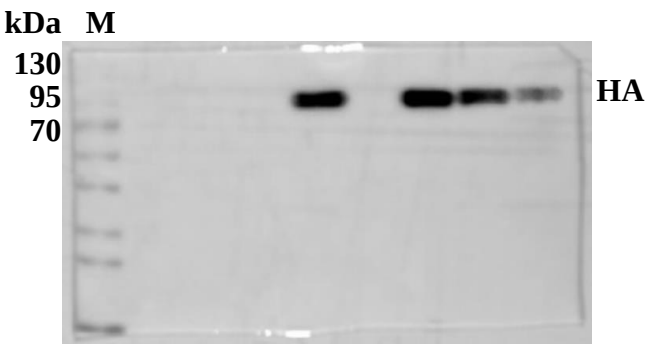


Fig.4I Right

IP

Input

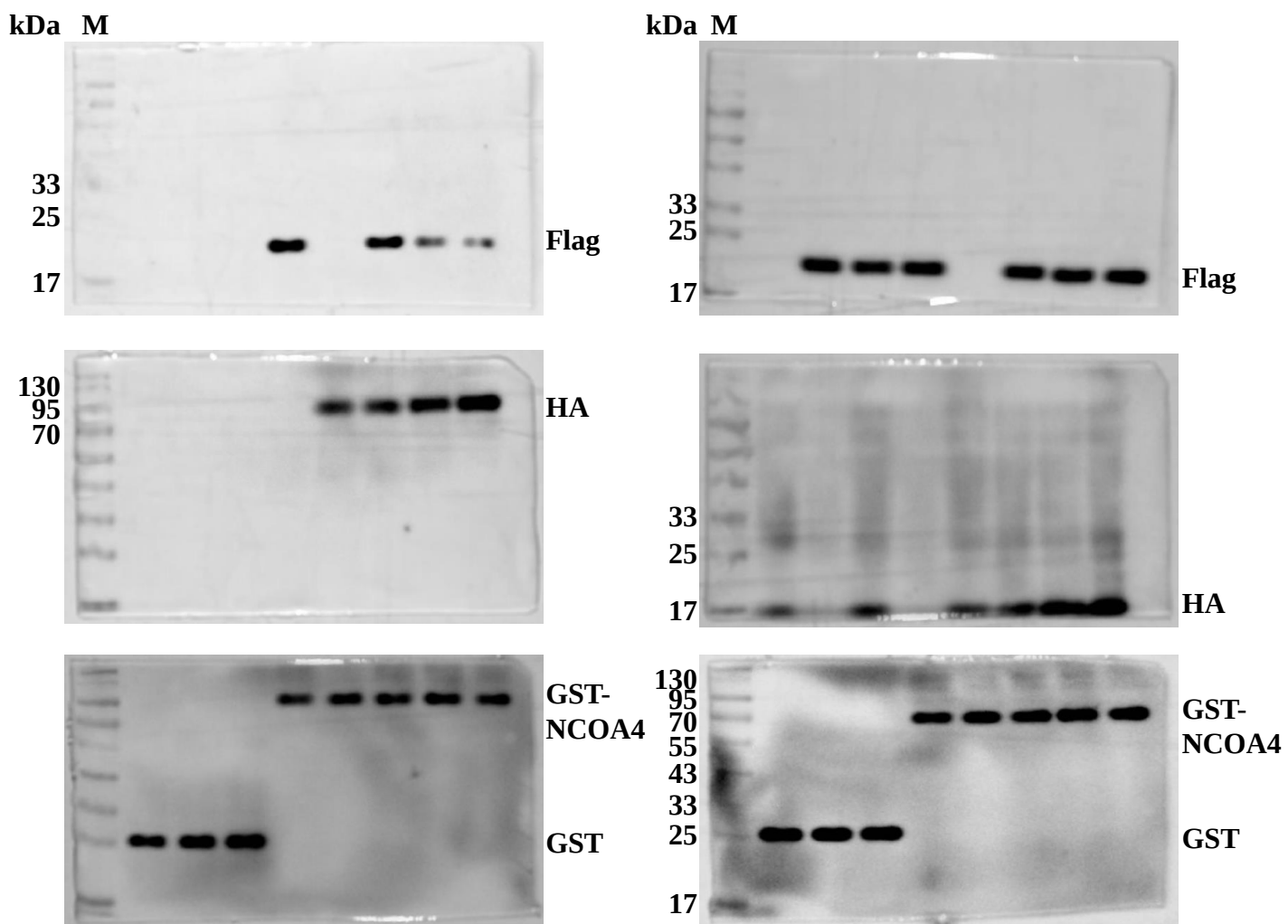


Fig.4J

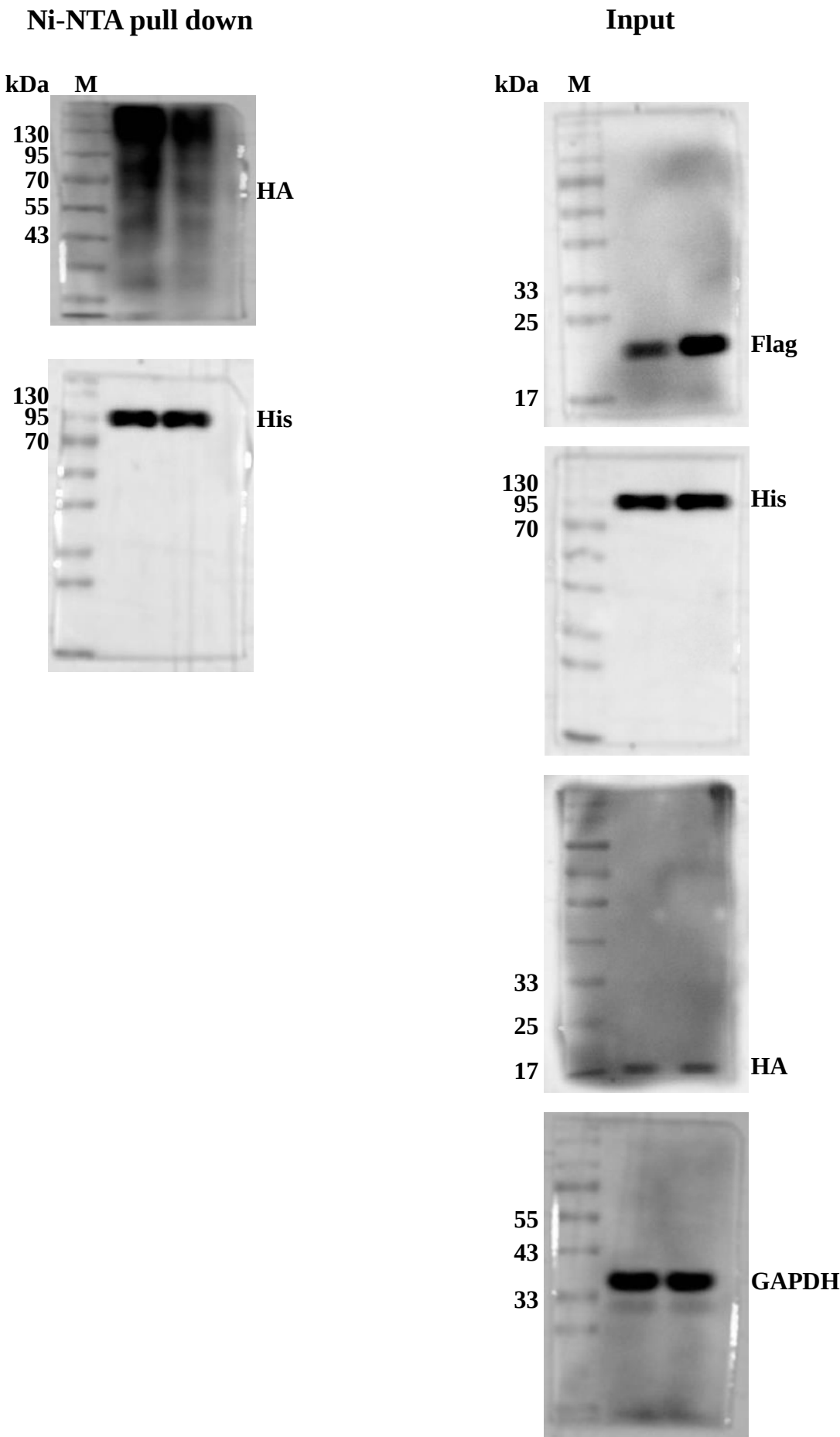
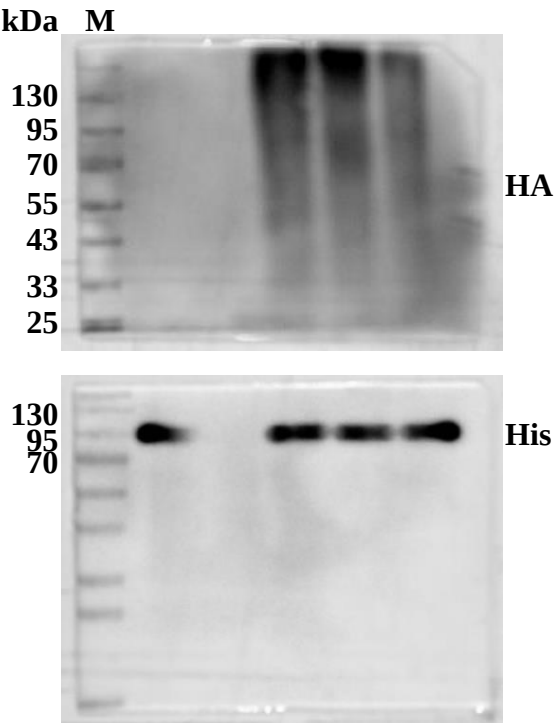


Fig.4K

Ni-NTA pull down



input

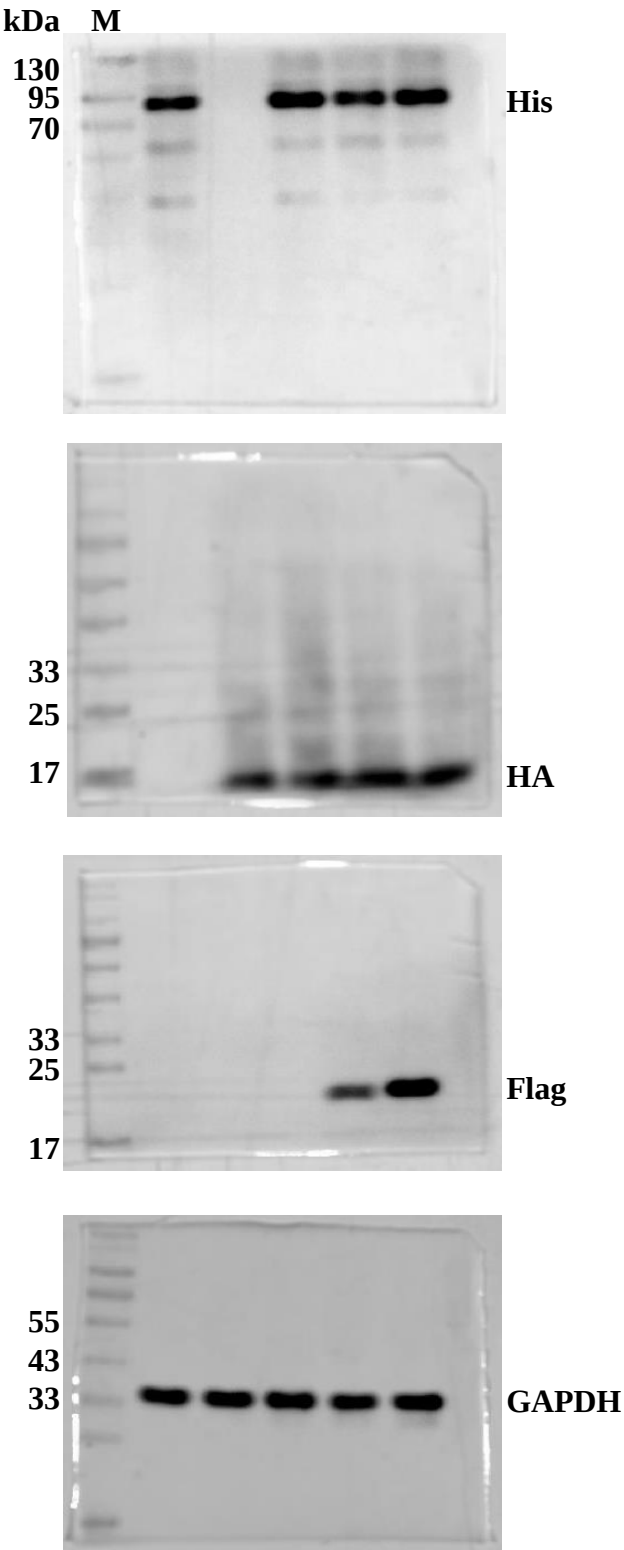


Fig.5A Left

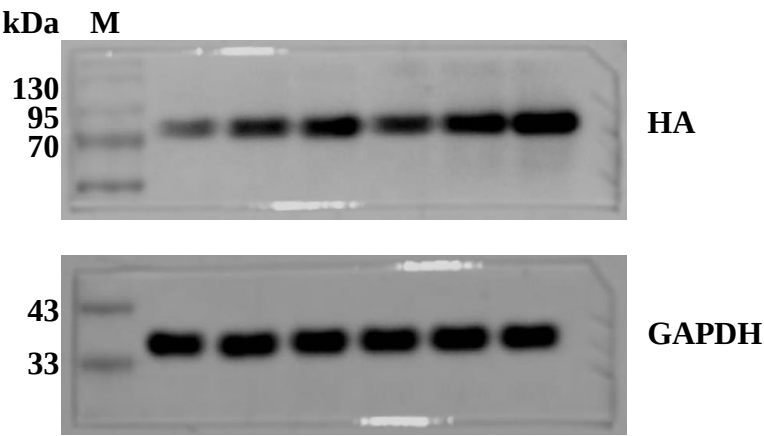


Fig.5A Right

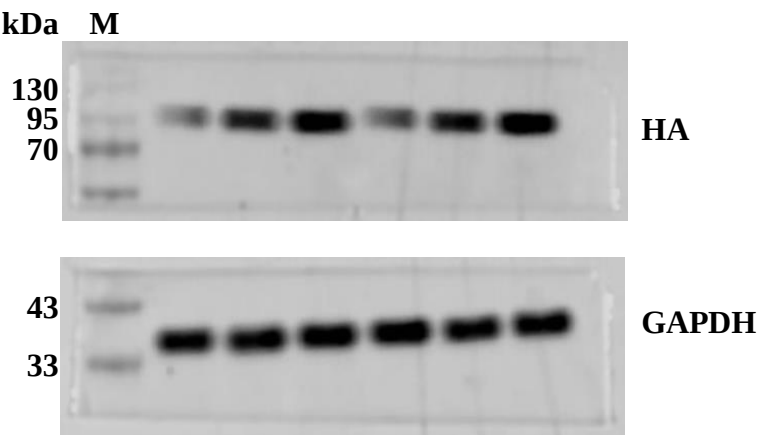


Fig.5B

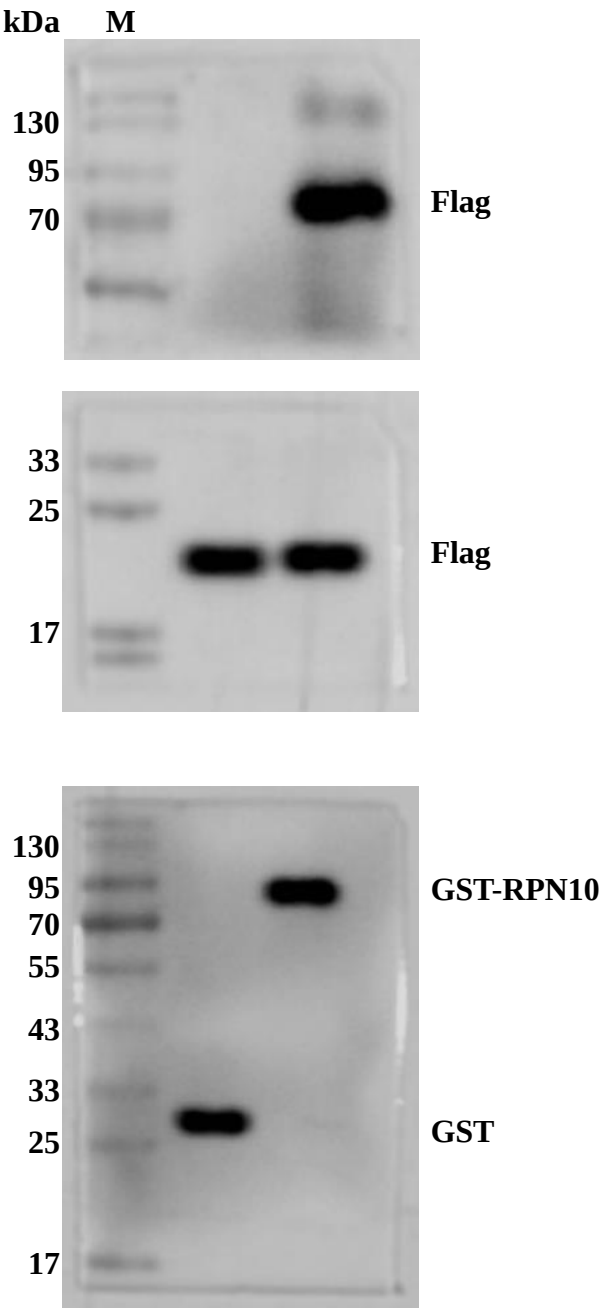


Fig.5D Up

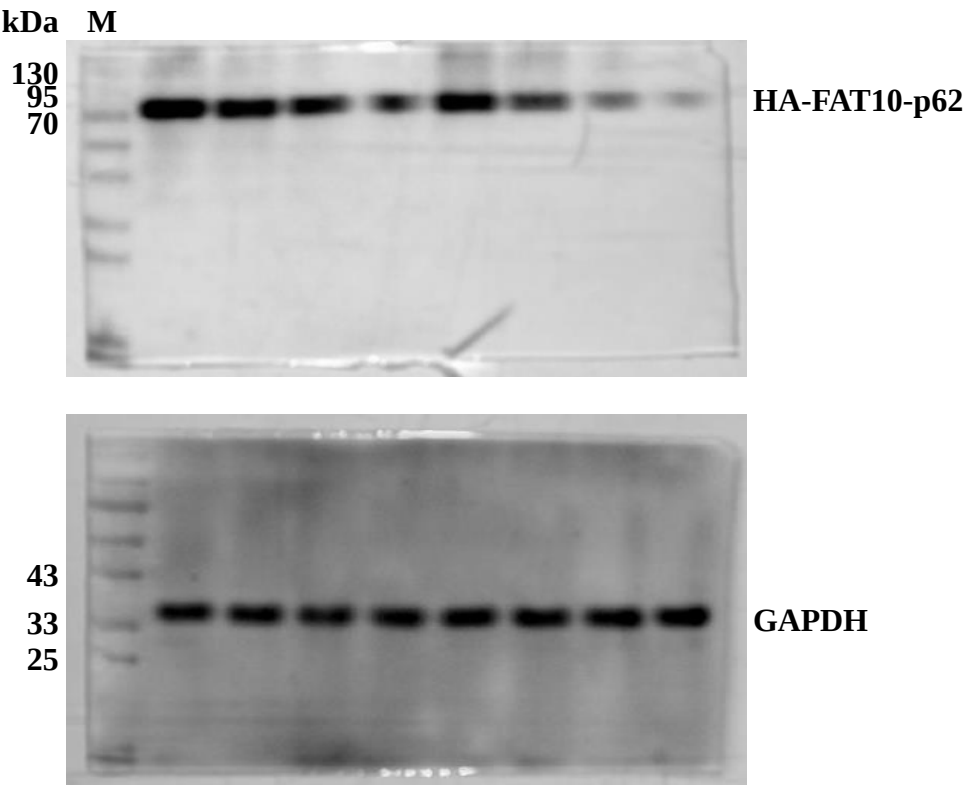


Fig.5D Down

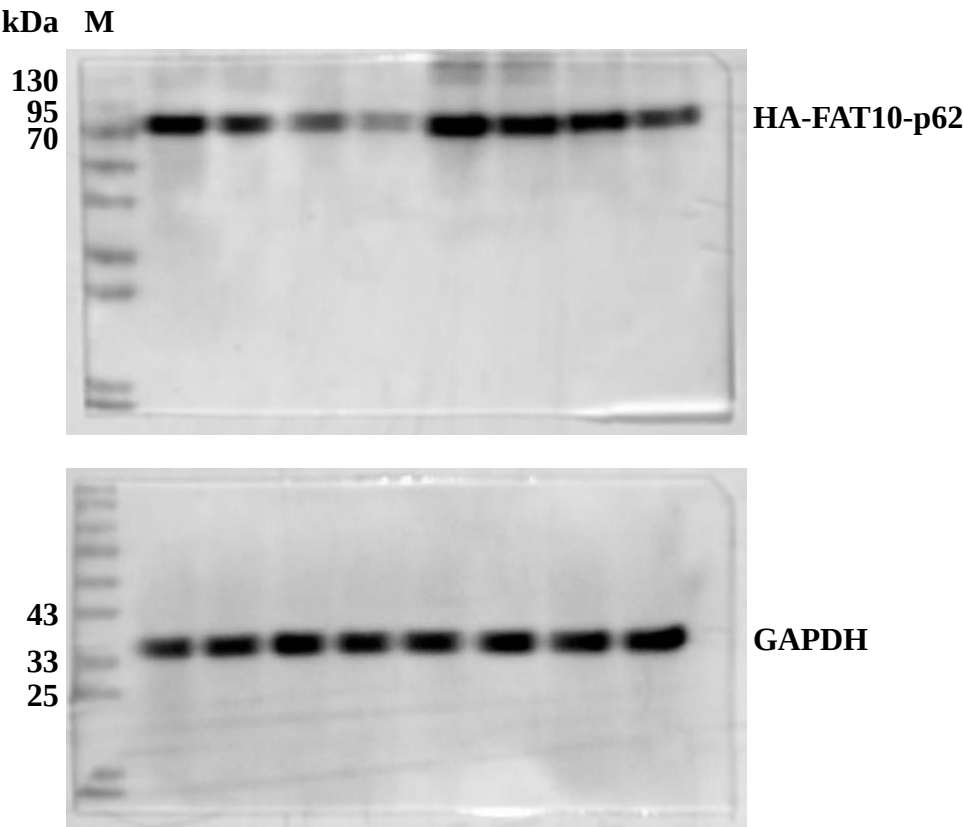


Fig.5E Up

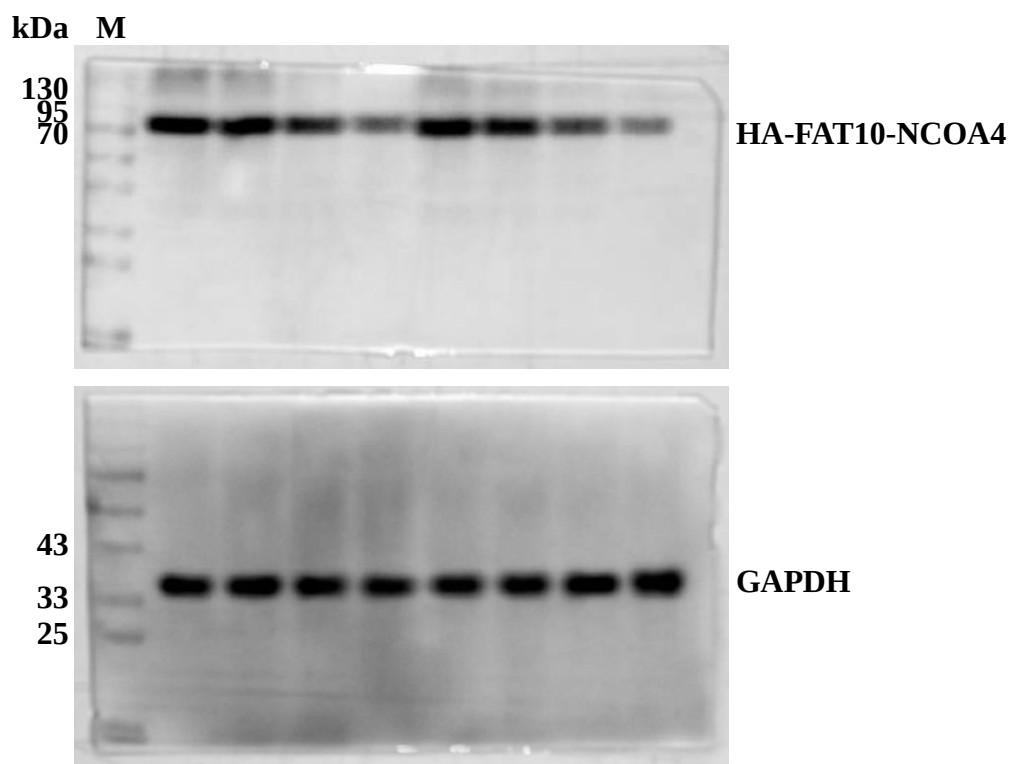


Fig.5E Down

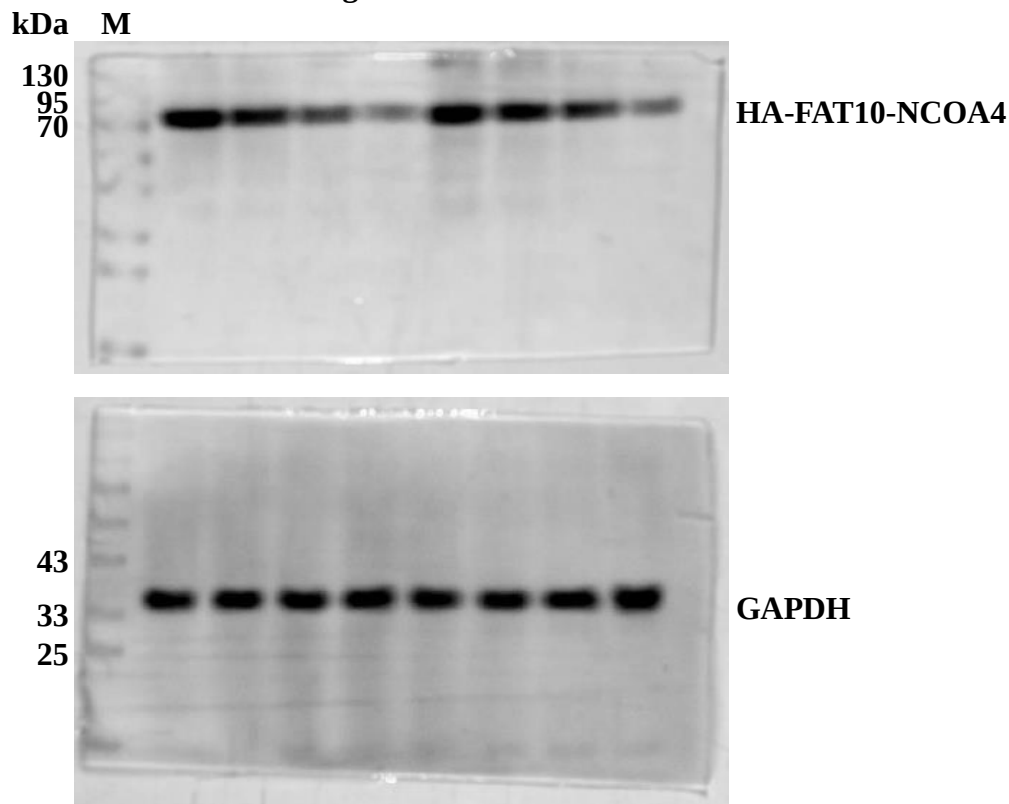


Fig.6I

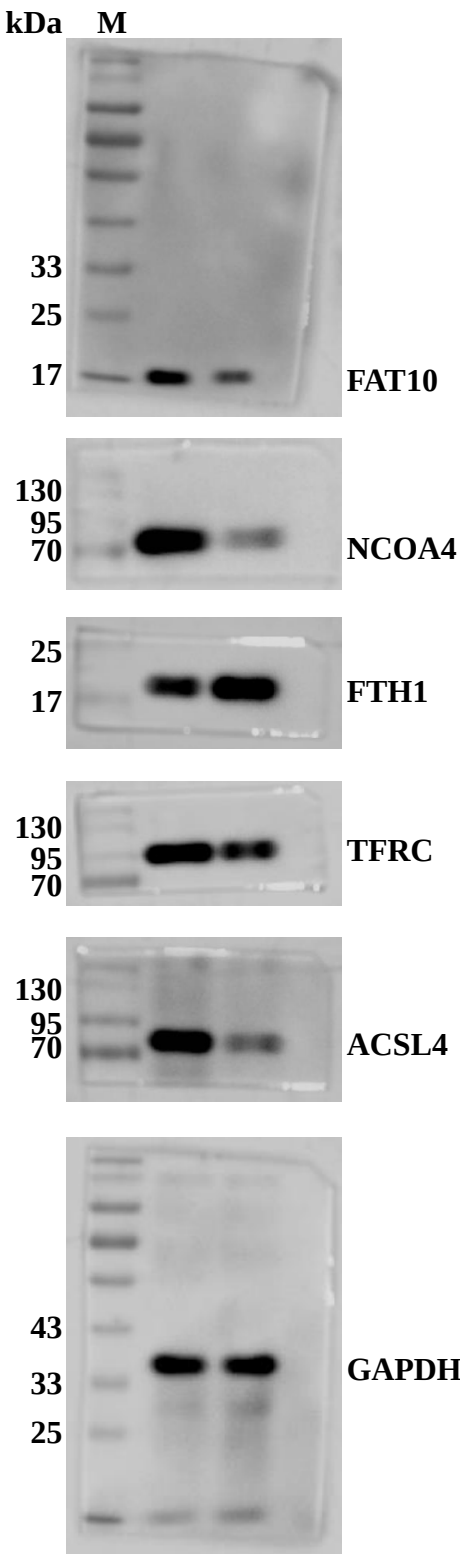
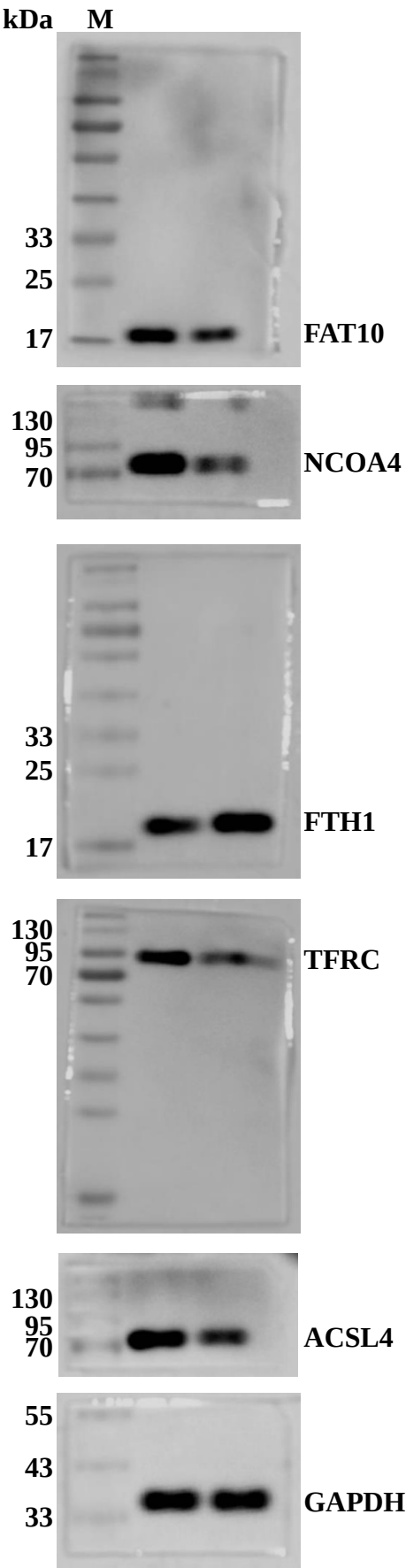
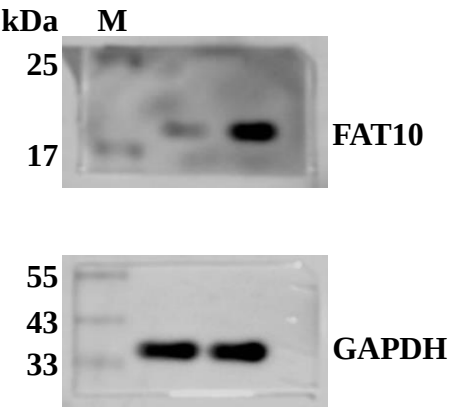


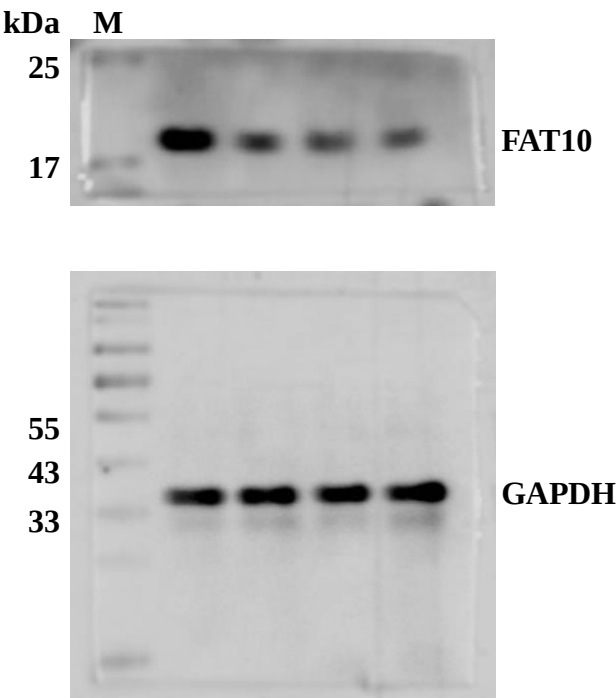
Fig.6L



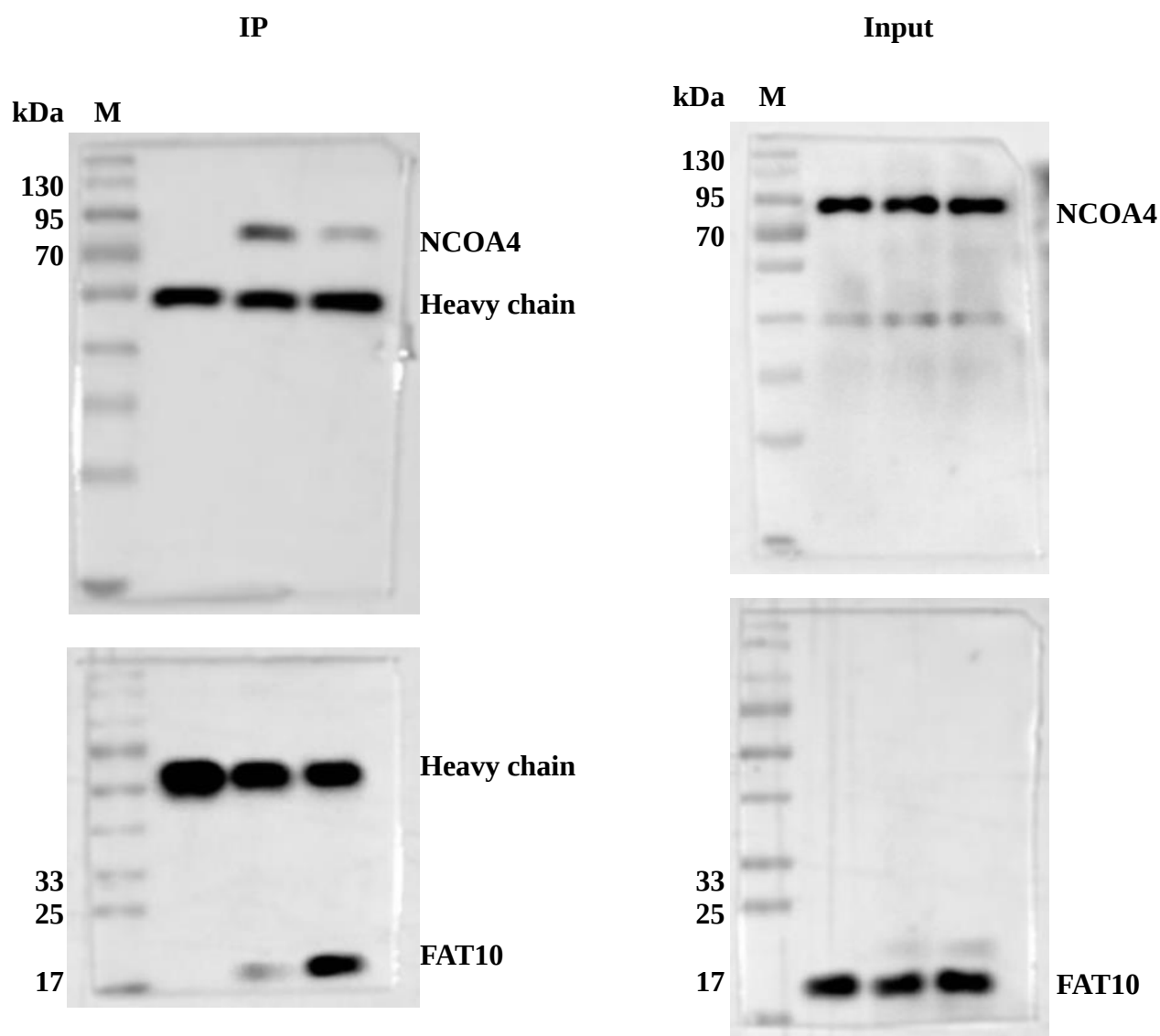
Supplementary Fig. 2A



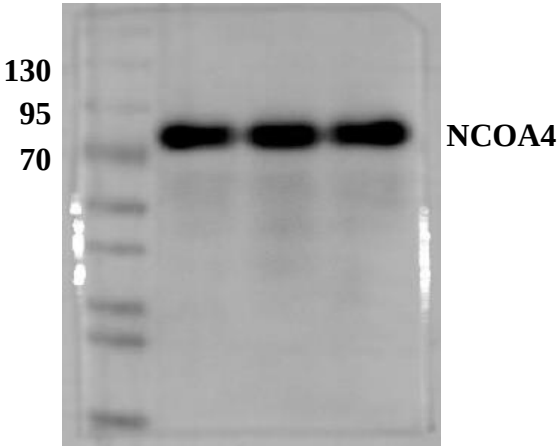
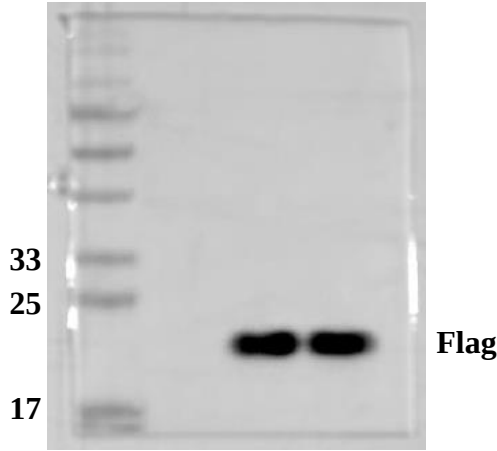
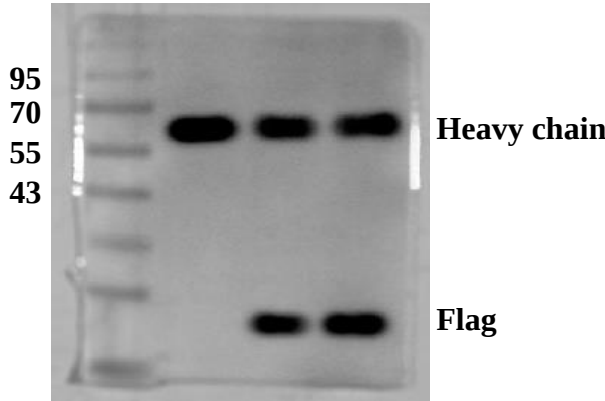
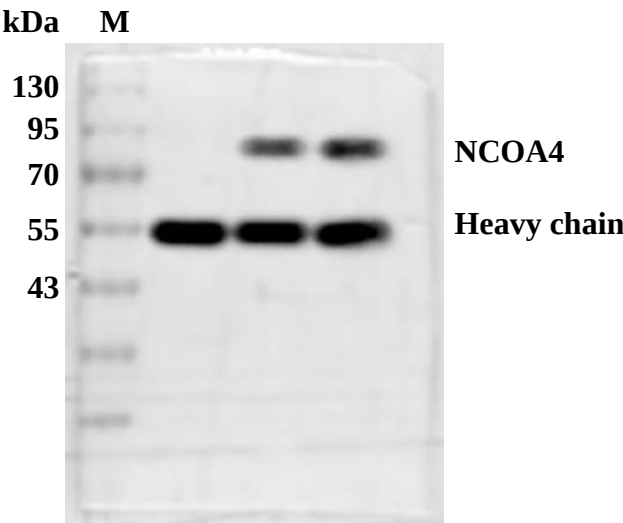
Supplementary Fig. 2H



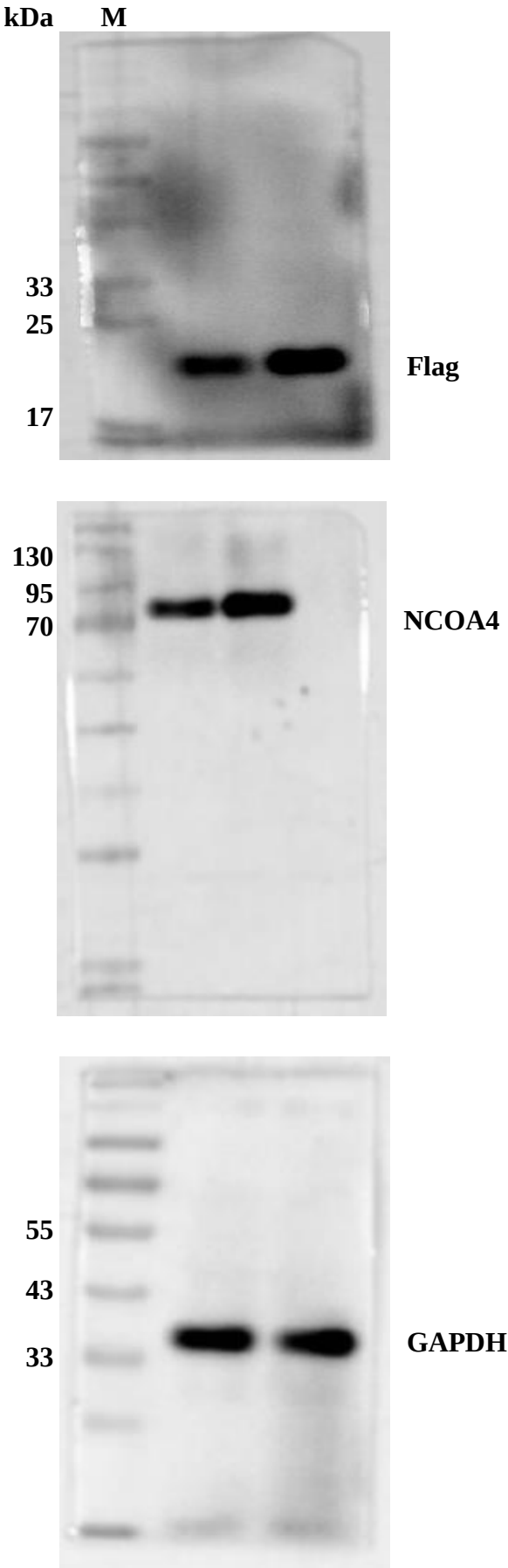
Supplementary Fig.4A



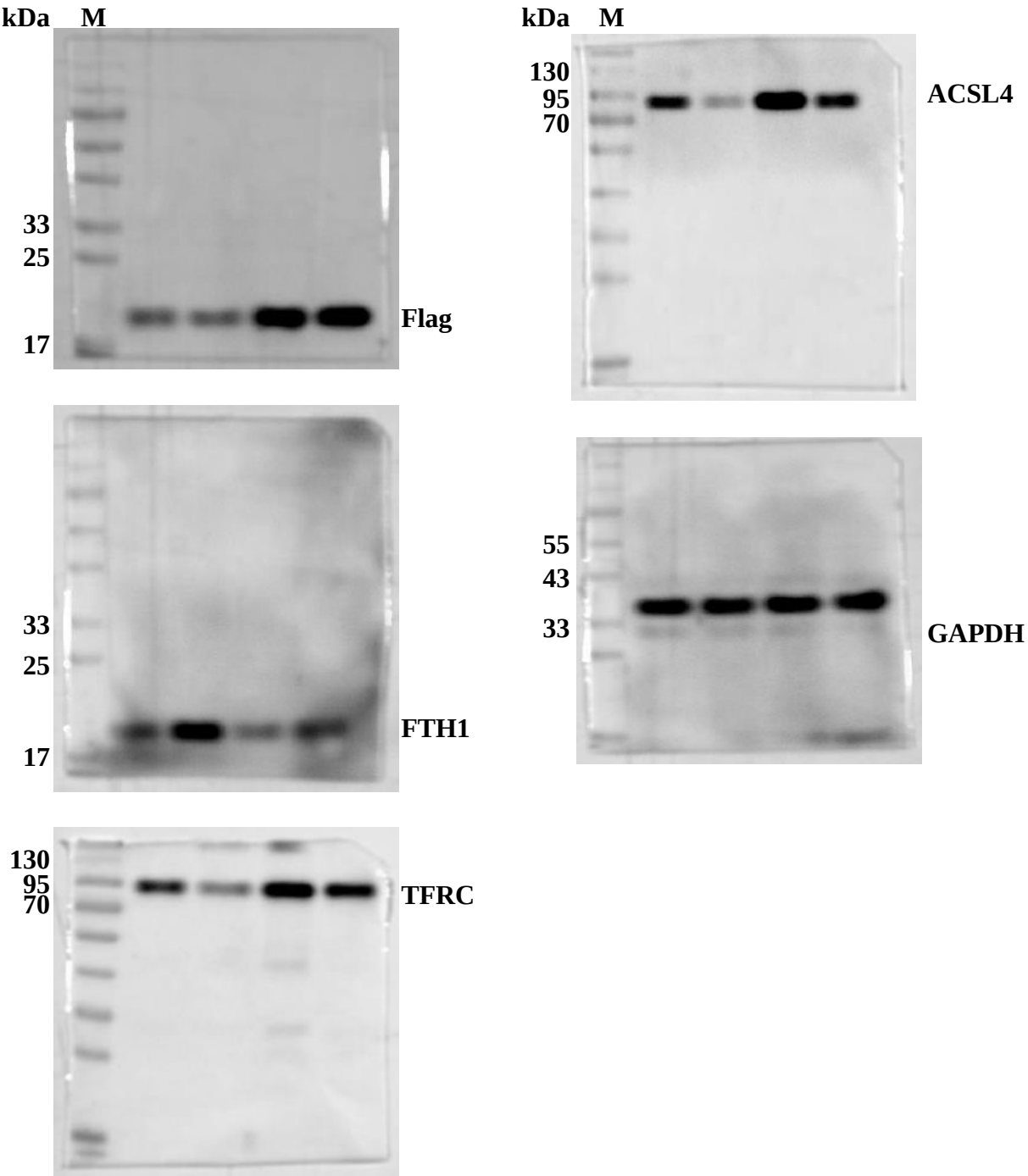
Supplementary Fig. 6A



Supplementary Fig. 6B



Supplementary Fig. 6D



Supplementary Fig. 6G

