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Correspondence and requests for materials should be addressed to J.K.L. (leejk@kist.re.kr)

* These authors contributed equally to this work.

† Current address: School of Materials Science and Engineering, University of Shanghai for Science and Technology. Shanghai 200093, PR China.

Silicon/copper dome-patterned electrodes for high-performance hybrid supercapacitors

Xuyan Liu¹*†, Hun-Gi Jung¹*, Sang-Ok Kim¹, Ho-Suk Choi², Sangwha Lee³, Jun Hyuk Moon⁴ & Joong Kee Lee¹

¹Center for Energy Convergence, Green City Technology Institute, Korea Institute of Science and Technology (KIST), Hwarangno 14-gil 5, Seongbuk-gu, Seoul 136-791, Republic of Korea, ²Department of Chemical Engineering, Chungnam National University, Daejeon 305764, Republic of Korea, ³Department of Chemical and Biochemical Engineering, Gachon University, San 65 Bokjeong-Dong, Sujeong-Ku, Seongnam, Gyeonggi-Do 461-701, Republic of Korea, ⁴Department of Chemical and Biomolecular Engineering, Sogang University, Seoul 121-742, Republic of Korea.

This study proposes a method for manufacturing high-performance electrode materials in which controlling the shape of the current collector and electrode material for a Li-ion capacitor (LIC). In particular, the proposed LIC manufacturing method maintains the high voltage of a cell by using a microdome-patterned electrode material, allowing for reversible reactions between the Li-ion and the active material for an extended period of time. As a result, the LICs exhibit initial capacities of approximately 42 F g⁻¹, even at 60 A g⁻¹. The LICs also exhibit good cycle performance up to approximately 15,000 cycles. In addition, these advancements allow for a considerably higher energy density than other existing capacitor systems. The energy density of the proposed LICs is approximately nine, two, and 1.5 times higher than those of the electrochemical double layer capacitor (EDLC), AC/LiMn₂O₄ hybrid capacitor, and intrinsic Si/AC LIC, respectively.

new type of Li-ion hybrid supercapacitor that combines the advantages of Li-ion batteries (LIBs) and supercapacitors is attractive as an energy storage system because it would have high energy density and would work well for high-power applications, such as hybrid, plug-in hybrid, and electric vehicles. Highperformance materials, particularly high-power materials, such as the LiMnO₄, Li₄Ti₅O₄, and TiO₂ used in conventional LIBs, have been considered as potential electrode materials for Li-ion hybrid supercapacitors that require a higher energy density. The high-performance materials used for LIBs include Si-based materials that can form alloys with Li. These materials are some of the most promising materials for electrodes in large rechargeable batteries because of their exceptionally high gravimetric capacity $(3,572 \text{ mAh g}^{-1})^{1-3}$. However, the structural problems that occur during Li insertion and extraction must be resolved for Si materials to be used in commercial Li-ion capacitor and battery applications.

Despite its high capacity, the large volume change (ca. 400%) in Si during the charge (Li dealloying) and discharge (Li alloying) processes generally leads to anode pulverization, which results in a lower reversible capacity and causes dramatic capacity fading. These effects in turn lead to cell failure within a few cycles. Several approaches have been suggested to overcome this problem, including the use of nanosized particles⁴⁻⁸, active/inactive composite materials^{9,10}, and Si-based carbon composites¹¹⁻¹⁴. These approaches yielded improvements in the electrochemical performance of Si-based anodes, but only to a limited extent. Recently, new studies demonstrated the use of Si nanowires as electrodes that minimize the shear stress for volume expansion^{15,16}. Although this advancement significantly improved the wires' electrochemical performance, cracks in the Si nanostructure at weak points had different volume expansion directions under high current. Furthermore, the detachment between the current collector and Si electrode may also be problematic because it breaks the electrical connection. Thus, from a practical perspective, the large volume exchange is the main limiting factor, particularly for high-power applications, such as Li-ion capacitors. This issue could be resolved by using a new type of nanostructure.

Polylactic acid (PLA) patterning techniques have been used to create promising substrates for cell growth with excellent biocompatibility and biodegradability. Although these substrates have good cell adhesion and proliferation properties¹⁷, the surface morphology of a 2D patterned film can affect cell adhesion, proliferation, and

differentiation^{18,19}. The highly ordered pattern of polymer films could enhance the activation of cell signaling²⁰. Therefore, integrating designer surface structures with the properties of PLA could be useful for biomedical applications. In previous studies, porous structures of PLA film have been prepared by a number of different methods, including thermally induced phase separation (TIPS)²¹, immersion precipitation²², and the breath figure (BF) method²³. Although porous PLA films can be generated by these methods, fabricating a PLA film with a regularly ordered structure remains difficult. To improve the regularity of the films, Fukuhira et al.24 used dioleoylphosphatidyl-ethanolamine as a surfactant to prepare honeycomb-structured PLA films by the BF method. In this case, the surfactant is essential to stabilize the water droplets, and it must be washed out from the porous films before they are used as scaffolds for tissue engineering. Thus, there will inevitably be a need to synthesize surfactant-free PLA honeycomb films for tissue engineering.

The PLA technique is a facile and simple approach for fabricating highly ordered honeycomb-patterned thin films of large area (at least a few cm²). The proposed fabrication method involves simply dipping a pre-coated PLA substrate into a binary mixture of solvent/ non-solvent (chloroform/methanol) under ambient conditions (i.e., a temperature of 15-20°C and relative humidity of 25-60%). This approach offers several distinct advantages. First, the honeycomb structure can be made in open air without controlling the humidity. Second, a highly ordered pattern of biodegradable PLA film can be self-organized by a simple dipping process in a mixture of two volatile solvents without using any surfactants or stabilizing agents. Third, the pore size can be easily controlled by simply varying the ratio of solvent to non-solvent. Finally, and most promising, the film has a nearly perfect structure with a long-range order, and it has through-pores that reach the surface of the substrate, which make it useful for fabricating photonic and electronic devices.

Commercial supercapacitors also require a higher amount of energy. Thus, the capacitance (C) and operation voltage (V) of the capacitor must be increased because the energy is determined by the formula $E = 1/2 \text{ CV}^2$. In studies of electrochemical double-layer capacitors (EDLCs), an operation voltage greater than 3.0 V was achieved by using new types of materials for electrolytes and electrodes. In the case of Si electrodes, which have a high specific capacity, increasing the operating cell voltage is a more convenient method.

In this study, we have developed a high-performance Si electrode that exhibits minimal shear stress during extreme high-current



Figure 1 | Transfer of the self-assembled PLA template pattern onto a dielectric film. Schematic diagrams of the micropatterned transfer process. The appropriate PLA thin film thickness was coated onto Cu foil as a current collector. Etching was then used to induce micro-phase separation of the PLA film in the chloroform and methanol mixture, and the patterned metallic seeds were transferred to the current collector by electroplating. Finally, the patterned Si electrode that followed the shape of the surface (contour coating) was formed by vapor deposition, and the surface of the electrode was modified.

cycling. We constructed a Li-ion capacitor by using a microdomepatterned Si electrode. The dome-patterned metallic Cu seeds and Si electrode were formed by using a PLA patterning technique to relieve the stress of volume expansion. It is also possible to increase the contact strength by increasing the contact surface area between the current collector and Si electrode. Because the patterned Cu/Si film was fully pre-lithiated prior to cell fabrication, it had high energy and power, and it could be used to build a Li-ion capacitor with a voltage greater than 3 V.

Results

Formation and characterization of Si/Cu dome pattern films. Fig. 1 schematically illustrates the process used to prepare the dome-patterned Si electrode. The microstructures in each step are presented in Fig. 2. A PLA thin film on Cu foil can self-assemble into hexagonal arrays with dimensions of 8-10 µm (Figs. 1a and 2a) when immersed in a chloroform and methanol mixture for 5 s. The patterned metallic seeds are then formed on the current collector by electroplating (Fig. 1b), and the Cu lattices are arranged regularly on the current collector after Cu plating and removal of the PLA template (Figs. 1c and 2b). The patterned Si electrode is then formed via vapor deposition, and the pattern follows that of the surface. The Si prepared on the current collector consists of regular spheres with a diameter of approximately 2 µm (Figs. 1d and 2c). The patterned Si consisted of hemispheres, and the diameter and height of each lattice were approximately 3 and 2 μ m, respectively. The method of directly plating Cu on a current collector (intrinsic Si electrode) and the method of forming a polymer template were compared, as described below.

Fig. 3a presents cross-sections milled into the electrode, which consisted of layered Si and Cu with a top layer of Pt. The typical



Figure 2 | **SEM images.** (a) Micropatterned PLA on Cu, (b) after Cu plating. and (c) phosphorus-doped silicon deposited on a latticed current collector.



Figure 3 \mid (a) FIB images of cross-sections of the dome-shaped Cu-plated silicon layer on the Cu substrate. EDX analysis of Si (b) and Cu (c) cross-sections.

trench width was 20 µm, and the depth was 10 µm. The perpendicular cross-sections clearly show the dome-shaped Cu clusters (ca. 1 µm of height) on the plane Cu substrate and electron cyclotron resonance (ECR)-deposited Si layer, which ranged in thickness from 2.3 to 3.4 µm. The elemental compositions of the Si and Cu crosssections obtained from energy-dispersive X-ray spectrometer (EDX) measurements were confirmed to be 94% and 99%, respectively. Figs. 3b and 3c present the EDX element maps taken at the crosssections of the layered Si and Cu, respectively. The EDX mappings in Figs. 3b and 3c clearly illustrate that the boundary between the Si layer and plated Cu layer corresponds to the cross-section images shown in Fig. 3a. The coated Si layer on the surface formed an additional Si mapping over the Pt layer, as shown in Fig. 3a. In the mapping of Cu shown in Fig. 3c, the shaded area on the right is caused by the screening effect of the external wall in a square-milled structure.

Electrochemical properties. A half-cell was manufactured and used for testing to study the electrochemical characteristics of the patterned Si/Cu electrode. Its electrochemical performance was compared to that of a non-patterned Si/Cu electrode (intrinsic Si electrode) with the same Si layer thickness. A cyclic voltammogram (CV) acquired at a scan rate of 0.1 mV s⁻¹ over the potential window of 3–0.001 V is shown in Fig. 4a. The dome-patterned Si electrode was found to have a lower cathodic current (Li alloy) and a higher anodic current (Li de-alloy) than the intrinsic Si electrode. The shapes and peaks of the CV curves are in agreement with the previously reported results for nanostructured Si electrodes⁴. The



Figure 4 | Electrochemical characterization of an intrinsic Si electrode and dome-patterned Si electrode in the Li-ion cell. (a) CVs from 3 to 0.001 V at a 0.1 mV s⁻¹ scan rate. (b) Charge and discharge voltage profiles for the first cycle for the intrinsic Si electrode and dome-patterned Si electrode (partial enlarged inset) at 0.05 C from 3 to 0.001 V. SEM images of (c) a pristine dome-patterned Si electrode and (d) an electrochemically lithiated Si electrode.





Figure 5 | Electrochemical characterization of the intrinsic Si electrode and dome-patterned Si electrode in the Li-ion hybrid capacitor. (a) CVs from 2.2 to 3.8 at a 1 mV s⁻¹ scan rate and at various scan rates for (b) the intrinsic Si electrode and (c) the dome-patterned Si electrode. (d) Galvanostatic charge-discharge cyclic curves for the first three cycles. (e) Cycle performance at a current density of 20 C and a voltage range of 2.2–3.8 V. (f) Ragone plot of different capacitor types at different power densities.

anodic current of the dome-patterned Si electrode was associated with the formation of the Li-Si alloy. The electrode began with a potential of 480 mV, representing an increase compared to the potential of the intrinsic Si electrode, which is below 200 mV. The current peak for the Li de-alloy (oxidation process) appeared at approximately 790 mV, which is lower than the current peak for the intrinsic Si electrode. This observation suggests that the reaction in the early stages of the Li de-alloy process was caused by a preferable periodic microdome pattern that created a higher electrochemical active area for a smaller polarization. For the electrochemical lithiation, the Li-ion half-cell was assembled by using the dome-patterned Si electrode as a working electrode and Li metal as a counter electrode. Fig. 4b presents voltage profiles for the first galvanostatic cycles of the intrinsic Cu-Si electrode and dome-patterned Cu-Si electrode. During the insertion of Li into Si in the first cycle, the voltage initially dropped quickly to 0.2 V and then declined gradually to 0.15 V as the crystalline Si reacted with Li to form amorphous Li_xSi. In particular, the observed capacity during this first charging operation in the dome-patterned Si was approximately 3,500 mAh g⁻¹. The voltage slowly decreased in a long potential plateau, as shown in Fig. 4b. This plateau occurred because of the formation of Li-Si alloys, which co-exist with Si as two-phase regions. After lithiation with the electrochemical discharge process, the diameter of the patterned Si increased to approximately 5 µm, but the hemisphere shape remained. This trend is shown in Fig. 4c (before lithiation) and Fig. 4d (after lithiation), and the same behavior was observed even when the electrode was fully discharged to a voltage of 0.001 V. The hemisphere shape of the patterned Si and Cu improved the interfacial properties between the electrode and current collector. It provided a greater contact area and reduced the stress associated with severe volume change during Li insertion.

Galvanostatic charge-discharge tests were carried out by using an automated battery cycler to evaluate the electrochemical properties of various LIC systems composed of intrinsic Si/activated carbon (AC) and dome-patterned Si/AC. Intrinsic Si and dome-patterned Si electrodes were fully electrochemically lithiated using Li-ion half-cells prior to the assembly of the LIC. Figs. 5a-c present the CVs of

two different Si electrode hybrid capacitors that were run at different scan rates (from 1 to 50 mV s⁻¹). The CVs were considered to be an appropriate tool for estimating the difference between the non-Faradaic and Faradaic reactions. According to the shapes of these CV curves, the capacitative behaviors of the Si electrodes in terms of patterned and intrinsic morphology differed significantly. At a low scan rate of 1 mV s⁻¹, the CV response of the dome-patterned Si electrode had a better rectangular shape than that of the intrinsic Si electrode (see Fig. 5a). Although the dome-patterned Si electrode exhibited a gradual deviation from the ideal rectangular shape at scan rates as high as 50 mV s⁻¹, it maintained its capacitative performance due to the overlap of the non-Faradaic reaction (surface reaction) and Faradaic reaction (Li insertion/extraction reaction). However, the CVs of the intrinsic Si electrode became distorted dramatically as the voltage sweep rate increased. This redox behavior is caused mainly by the Faradaic reaction, which means that the intrinsic Si electrode is not suitable for a quick charge-discharge operation. Improving the accessibility of ions to the electrochemically active surface area typically leads to greater capacitance at a high voltage sweep rate. Increasing the electrochemical active area between the Si electrode and electrolyte by changing the intrinsic shape from 2D to a 3D dome pattern causes surface charge accumulation at the Si electrode/electrolyte interface, which is in agreement with the results of other groups^{25,26}. The cell resistance was obtained from the Nyquist plots by using electrochemical impedance spectroscopy (EIS), and the results indicated that reducing the charge transfer resistance at a high frequency response causes a faster electrode and electrolyte interface reaction, as shown in Fig. S3.

Figs. 5d and 5e present the galvanostatic charge-discharge voltage profiles and cycle performances of LICs in the range of 2.2–3.8 V (compared to Li/Li⁺ capacitors) at a current density of 20 C. Unlike common LIC systems, our LICs had a higher open circuit voltage (approximately 3 V), which is similar to that of the Li metal electrode due to the pre-lithiation of the Si electrode and the higher operation voltage of up to 3.8 V. The higher operation voltage could be achieved because of the increased energy density. In addition, the unique dome-patterned Si electrode exhibited a higher capacity and lower I-R drop than the intrinsic Si electrode due to its larger electrochemical active area (see Fig. 5d). As shown in Fig. 5e, all LICs exhibited a similar initial capacitance of approximately 42 F g⁻¹, even at a high specific current density with a 20 C-rate (60 A g^{-1}). However, the cell capacity for the intrinsic Si/AC LIC began to decrease after 6,000 cycles, and it subsequently exhibited severe capacity fading. By contrast, the dome-patterned Si/AC LIC exhibited excellent reversibility at over 15,000 cycles. Fig. 5f presents power density as a function of energy density for a Cu-Si capacitor containing a 1.5 µm-thick Si electrode with a uniform microstructure. The patterned Si electrode has superior energy density per unit volume and exhibits superior cycle performance even under high electrical current. Moreover, energy density and power density can be remarkably improved because more energy can be stored in Si than in graphite of the same thickness and because high-output performance can be achieved with the microstructure. The power density reached 90 Wh kg⁻¹ at a specific power of 90 W kg⁻¹ and was maintained at 55 Wh kg⁻¹ even at a specific power of 900 W kg⁻¹. Compared to other existing capacitors, this power density is nine, two, and 1.5 times higher than those achieved by an EDLC, AC/LiMn₂O₄ hybrid capacitor, and intrinsic Si/AC LIC, respectively (see Fig. 5f).

Discussion

A PLA thin film on Cu foil can be used to generate an array of ordered microscopic pores with good control of size, orientation, and structure. Fig. S1 presents a typical scanning electron microscopy (SEM) image of an ordered PLA array. Each 8 µm micro-well was in contact with six other spheres and arrayed in a classic hexagonal lattice (Fig. S1a) or round lattice (Fig. S1b). Intrinsic point and line defects typically appeared in the resulting structure, and the typical defect-free domain sizes were on the order of 400 cm², which is greater than that observed for other domains. The pattern style changed with changes to the ratio of chloroform to methanol (Figs. S1a-c). Furthermore, the bottomless micro-well patterns were difficult to create when the concentration of PLA was too high (Fig. S1d). The best micro-well patterns are shown in Fig. S2, which indicates the presence of Cu metal at the bottom of the porous layer of the PLA film. The principal²⁷⁻³⁰ steps of this method involve casting a polymer solution onto an inert support, followed by immersing the support with the cast film into a bath filled with a mixture of solvent and non-solvent for the polymer. During the evaporation of the solvent, capillary action caused the PLA film to assemble into a close-packed array on the substrate. The subsequent exchange of solvent and non-solvent induced phase separation in the polymer solution. The liquid-liquid demixing by nucleation and growth of a polymer-poor phase is what created the porous cellular structure. A considerable amount of methanol is required to induce demixing of the solutions. At low PLA concentrations (3 wt%), the cloud point can only be attributed to the liquid-liquid demixing processes³¹. This statement was confirmed by the fact that two completely separated transparent layers were obtained after prolonged aging of the phase separated solution. In principle, the liquid-liquid phase separation can proceed until two fully separated layers are obtained. The liquid-liquid phase separation process must be arrested before completion to obtain patterns with a stable porous morphology. The demixed solution solidifies when a gel transition point is passed in the course of the demixing process. Several physical phenomena can cause the formation of a gel. Examples include gelation by association of components in the mixture, gelation by vitrification, and gelation by crystallization. The solid-liquid demixing process also plays an important role in determining the morphology because it occurs in the chloroform/methanol mixture. The structure formation in this system proceeds almost entirely via solid-liquid demixing, resulting in the formation of hexagonal array structures. Hexagonal array structures were only obtained with casting solutions that had very low polymer concentrations. The importance of the solid-liquid demixing processes

during immersion precipitation was also demonstrated for the wetspinning of poly-L-lactide hollow fibers³². Depending on the solventnon-solvent combination used, fibers could be obtained with cellular morphologies in the sublayer (a structure largely caused by liquidliquid demixing) or with a leafy morphology (a structure due caused by solid-liquid demixing). In particular, fibers with a morphology that was induced by solid-liquid demixing are useful for the controlled delivery of drugs. The morphology of the PLA is the result of a complex interplay between solid-liquid demixing and liquid-liquid demixing. The glass transition did not play an important role in structure formation because of the high swelling of the polymers in the non-solvent. The PLA concentration in the casting solution determined whether the structure formation occurred by liquidliquid demixing followed by solid-liquid demixing or by solid-liquid demixing followed by liquid-liquid demixing.

Stable cycling with high energy and high power density has been a long-standing goal in research on Li-ion cells. Because delamination of the electrode active material from the current collector compromises the performance of the battery, it is necessary to maximize the interfacial area between the electrode active material and current collector. Simple strain relaxation in the patterned Si allows the diameter and length to increase without the electrode breaking during cycling. The intrinsic mechanical stability of thin films relative to their bulk equivalent and the electronic and mechanical properties of the patterned Cu prevent pulverization of the Si and result in an improved cycle life. The dome architecture also modestly increases the effective surface area and thus increases the overall energy density. Here, by effectively folding a patterned Si thin film with a thickness of 1.5 µm into a 1D electrically connected structure, it is possible to create an anode with good cycling capabilities due to its high Si volume fraction, large surface area, high energy density, and reasonable power density. The dome-patterned Si-based supercapacitor has an energy density and power density of 90 Wh kg⁻¹ and 1,000 W kg⁻¹, respectively, thus exhibiting better performance than other capacitor devices (see Fig. 5f). Moreover, the specific capacitance of this device is 42 F g^{-1} at 60 A g^{-1} for 15,000 cycles, which is greater and longer lasting than the values reported by Choi *et al.*²⁵ (30 F g^{-1}) and Thissandier et al. (250 cycles) using an Si-based electrode³³.

To explain the electrochemical results, it is important to consider the dome-patterned Si that exerts such a significant influence on charge storage. The dome pattern is closely related to the surface area. The poor cycling performance of the intrinsic Si anodes is caused by the breakdown of the electrically conductive network, which results from the large Si volume expansion³⁴. During de-alloying, the resistance increases because of volume contraction, reducing the electronic contact between particles and increasing the contact resistance and charge transfer resistance. As a result, complete dealloying cannot occur because the Li⁺ ion is trapped inside the anode host particles. Even if the operating voltage window of this supercapacitor device were approximately 2 V, with little volume change in the Si electrode, the repeated charge and discharge cycling at extremely high current could cause surface cracks and delamination from the current collector (see Fig. S4). The patterned dome had a larger surface area than the intrinsic dome by approximately 1.5-fold according to surface area calculations for a hemisphere and square. This larger surface area allows the inner active sites of the electrode to be fully accessed and firmly bound to the current collector.

The features and advantages of the process proposed in this paper may be summarized as follows:

The process enables polymer patterns to be readily controlled by selecting a suitable ratio of solvent and non-solvents based on the solubility properties of the polymer. This method is easily repeatable and inexpensive. This process is also important because on different substrates, these large-area micro-polymer patterns may find potential applications as semiconductors, display fabrications, and biomaterial interfaces for large-area preparations. Self-assembled polymer patterns are used as sacrificial layers for the transfer of dense microscale patterns to more robust materials. All techniques are compatible with standard semiconductor fabrication processes. The contact area maximized with Si, which is the electrode active material, enhances physical adhesion and thus improves the mechanical stability of the electrode active material. The increased contact area between the electrolyte and electrode active material and between the electrode active material and current collector increases the transport of Li ions per unit time, improving electrode efficiency. Because the patterned Si material experiences less shear stress, the stress associated with the volume change accompanying the reaction with Li is reduced and electrode stability is improved. Because the patterned Si electrode has superior energy density per unit volume and exhibits superior cycle performance even under high electrical current, a Liion secondary battery made with this type of patterned Si electrode would have high capacity and high output and could be used as the power supply for large or small mobile devices.

Methods

Preparation of the PLA template on Cu. Surface control of the Cu current collector with a PLA template was performed as follows. The polymer resin PLA was dissolved in chloroform to approximately 3 wt% and coated on the Cu current collector to a thickness of approximately 7 µm using a doctor blade. The Cu current collector was immersed in a chloroform-methanol mixture (volume ratio of 17/3) for 5 s and then extracted, forming lattices of PLA on the Cu current collector.

Cu plating after the formation of the PLA template on the current collector. Cu electroplating was conducted on the PLA lattice on the surface of the Cu current collector. The (-) electrode of the PLA-latticed Cu current collector was connected to a Cu solution comprising 60 g/L CuSO4 \cdot H₂O, 150 g/L H₂SO₄, and 50 ppm HCl, and the (+) electrode was connected to a highly pure Cu plate. A latticed Cu pattern was then prepared by electroplating for 20 s at a current density of 10 mA/cm² using a DC rectifier. The Cu current collector was immersed in chloroform for 10 s to remove the PLA lattice remaining on the surface.

Si negative electrode on a shape-controlled surface. Si was used for the negative electrode of the secondary battery, and a manufactured surface-controlled Cu current collector was used as the current collector. A Si thin-film negative electrode was prepared directly on the Cu current collector by electron cyclotron resonance chemical vapor deposition. The surface-controlled Cu current collector was curt to a size of 10×10 cm² and dried at 80° C for 1 h after removing the organic matter present on the surface by cleansing with ethanol. The dried surface-controlled Cu current collector was placed in the chamber of a deposition apparatus, and the substrate temperature was adjusted to 200° C while maintaining a high-vacuum state of 1×10^{-5} Torr. After flowing 30 sccm of argon gas into the chamber, plasma was generated with 700 W of microwave power while maintaining the pressure at 15 mTorr. A phosphorus-doped Si thin-film electrode was prepared by injecting 5 sccm of silane (SiH₄) gas and 0.2 sccm of phosphine (PH₃) while controlling the reflected power to within 5 W. The thickness of the prepared Si thin film was 1.5 μ m, and the phosphorous content in the Si thin film was approximately 1% by weight.

Cell fabrication. For the AC electrode, 85 wt% activated carbon (YP-50F, Kuraray), 5 wt% DB-100, and 10 wt% polyvinylidene fluoride (PVdf) were mixed in a homogenizer at 5,000 rpm for 15 min. The mixed slurry was cast on aluminum foil (20 µm, Sam-A Aluminum) or aluminum mesh with a thickness of 80-100 µm, and it was dried in an oven at 80 $^\circ\mathrm{C}$ for at least 2 h. The dried foil was cut to a size of 2 \times 2 cm² and pressed to a thickness of 40–50 μ m using a hot roller press at 120°C. This foil was used as the positive electrode. For the pre-lithiation of Si in LIC, half-cells were fabricated in a dry room, including the Si thin film that was used as the working electrode, the polyethylene used as the separator, and the Li metal foils used as counter electrodes. The liquid electrolyte was 1 M LiPF₆ in ethylene carbonate, ethyl methyl carbonate, and dimethyl carbonate (1:1:1 volume ratio). The electrode area was 4 cm² (2 cm \times 2 cm). The half-cell was galvanostatically charged at C/20 to 0.001 V. The Li-ion capacitor was then assembled with the pre-lithiated Si electrode, AC electrode, and aforementioned electrolyte. The active material weight ratio of the Si and AC electrodes was close to 1:10. The specific capacity of the cells and the current density were normalized by the total weight of the electrode materials.

Characterization and electrochemical measurement. SEM was performed on a Hitachi S-5500 to characterize the morphology of the micropattern on the Cu. A focused ion beam (FIB) device (NOVA200) operating at 30 keV and 1 nA was used to obtain a cross-sectional view of the layered structure generated by FIB milled trenches. The Pt metal line was then deposited on the area of interest to prevent damage and spurious sputtering of the top portion of the specimen. The electrochemical characteristics were evaluated based on a charge-discharge test in the voltage range of 2.2 to 3.8 V using a battery cycle (WBCS3000, Won-A Tech.) under a constant current of 20 C (60 A g⁻¹). CV was performed in the potential range of

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Author contributions

J.K. Lee designed the experiments. X.Y. Liu, S.O. Kim, S.W. Lee and H.S. Choi performed the experiments. H.-G. Jung, X.Y. Liu, J.H. Moon, and J.K. Lee discussed the interpretation of results and co-wrote the paper. All authors discussed the results and commented on the manuscript.

Additional information

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