



Article Unified Model of Shot Noise in the Tunneling Current in Sub-10 nm MOSFETs

Jonghwan Lee



Citation: Lee, J. Unified Model of Shot Noise in the Tunneling Current in Sub-10 nm MOSFETs. *Nanomaterials* **2021**, *11*, 2759. https:// doi.org/10.3390/nano11102759

Academic Editor: Paolo Marconcini

Received: 1 September 2021 Accepted: 16 October 2021 Published: 18 October 2021

Publisher's Note: MDPI stays neutral with regard to jurisdictional claims in published maps and institutional affiliations.



Copyright: © 2021 by the author. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). Department of System Semiconductor Engineering, Sangmyung University, Cheonan 31066, Korea; jhlee77@smu.ac.kr

Abstract: A single unified analytical model is presented to predict the shot noise for both the sourceto-drain (SD) and the gate tunneling current in sub-10 nm MOSFETs with ultrathin oxide. Based on the Landauer formula, the model is constructed from the sequential tunneling flows associated with number fluctuations. This approach provides the analytical formulation of the shot noise as a function of the applied voltages. The model performs well in predicting the Fano factor for shot noise in the SD and gate tunneling currents.

Keywords: shot noise; source-to-drain tunneling current; gate tunneling current; sequential tunneling; Fano factor

1. Introduction

Advances in nanofabrication technology have opened up the possibility of surpassing the ultimate performance limits of modern CMOS devices [1,2]. However, the sub-10 nm channel length in MOSFETs gives rise to tunneling current and associated shot noise through the source-to-drain (SD) potential barrier [3-15]. With the downscaling of device dimensions, the continuous reduction in gate oxide thickness leads to enormous gate tunneling currents and noise [16–23]. Therefore, the off-state currents, from which the static power dissipated by MOSFETs originates, are enhanced by the direct tunneling current through the SD potential and gate oxide in the subthreshold region and consequently prevent low voltage/power operation [5–7]. In addition, the miniaturization of nanoscale CMOS logic devices has made the quantum nature of current flow more pronounced, leading to higher fault rates due to shot noise in the subthreshold region [14]. These factors set fundamental limits on future CMOS technologies, because significant tunneling currents and shot noise are expected under normal operating conditions. The intriguing properties of shot noise, which have been extensively studied in mesoscopic devices, should manifest themselves in sub-10 nm MOSFETs, as the devices enter the ballistic transport regime [9–15]. However, analytical and highly predictive noise models in ballistic MOSFETs for circuitlevel simulation are still lacking [10–12]. Although the need for a unified model of shot noise in the SD and gate tunneling current arises frequently in quasi-ballistic MOSFET operation, these currents are typically treated separately for each case [9–15,21–23]. In this work, a single unified analytical expression is presented for the SD and gate tunneling currents and the associated shot noise, emphasizing their similarities and differences. It provides a fully analytical and explicit function of bias conditions and device structural parameters and can be suitably implemented into a circuit simulator. Moreover, the analytical model under a single Landaur transport formula can be helpful to gain easy insights into the shot noise properties of nanoscale MOSFETs in the ballistic regime.

2. Tunneling Current Model

For extremely short channel lengths and ultrathin gate oxide thickness, the tunneling current with 2D (n = 2) and 3D (n = 3) density-of-states (DOS) between two electron reservoirs separated by the energy barrier is given by the Landauer formula [4,24]

$$I_{\alpha} = \frac{2q}{h} \left(\frac{2\pi m^{*}}{h^{2}}\right)^{\frac{n-1}{2}} \iint T_{\alpha}(E_{s})(f_{L} - f_{R}) \frac{dE_{t}}{(\pi E_{t})^{\frac{3-n}{2}}} dE_{s}$$
(1)

where *q* is the charge; *h* is the Planck's constant; m^* is the effective mass, and n = 2 and 3 correspond to source-drain and the channel-gate tunneling, respectively. f_L and f_R are the Fermi–Dirac distribution functions of the transversal energy E_t and longitudinal energy E_s at the left and right reservoirs, respectively. For 1D DOS based on carbon nanotubes and Si nanowires, the tunneling current can be expressed as an integral over the longitudinal energy E_s [7,24,25]. By an extension of the formulation of (1), the model for p-channel Ge nanowire is modified to take into account phonon and surface roughness scattering in the channel and SD tunneling [25,26]. $T_{\alpha}(E)$ is the tunneling probability of the energy barrier between source and drain ($\alpha = sd$) or between channel and gate ($\alpha = cg$). The tunneling probability is given by the WKB integral for the energy barrier profile $E_b(s)$ as

$$T_{\alpha}(E_s) = \exp\left[-\frac{2\sqrt{2m^*}}{\hbar} \int_{s1}^{s2} \sqrt{E_b(s) - E_s} ds\right]$$
(2)

where, \hbar is the reduced Planck's constant; s_1 and s_2 are the turning points at which E_s is equal to $E_b(s)$; and s is in x-direction for the channel-gate tunneling and in y-direction for source-drain tunneling, respectively, as shown in Figure 1. Using the WKB approximation of (2) for parabolic and trapezoidal barriers of the SD and gate tunneling, respectively, compact models for the tunneling probability can be obtained to derive analytical formulations of the tunneling current and its shot noise [4,7,16,19]. For the source-drain tunneling path, when the channel length L is sufficiently short, the barrier potential $V_b(y) = -E_b(y)/q$ can be approximately represented by a parabolic function of y as [5,7]

$$V_{b,sd}(y) = -A(y - y_{max})^2 + \phi_{b,sd}$$
(3)

where $V_b(y)$ has a local maximum $\phi_{b,sd}$ at $y = y_{max}$. The boundary conditions are $V_b(0) = V_{bi}$ and $V_b(L) = V_{bi} - V_{ds}$, which yield

$$A = q(\phi_{b,sd} - V_{bi}) / y_{max}^2 \tag{4}$$

$$y_{max} = (L/V_{ds}) \cdot (V_{bi} - \phi_{b,sd}) \left(1 - \sqrt{\frac{V_{bi} - V_{ds} - \phi_{b,sd}}{V_{bi} - \phi_{b,sd}}} \right)$$
(5)

where V_{bi} is the built-in potential of the source-channel junction, and V_{ds} is the drain-source voltage. The maximum barrier height $\phi_{b,sd}$ is expressed as a function of V_{gs} and V_{ds}

$$\phi_{b,sd}(V_{gs}, V_{ds}) = -\psi_s^{QM}(V_{gs}) - \Delta V_{th}^{DIBL}(V_{ds})$$
(6)

where ψ_s^{QM} is the surface potential calculated by an explicit model [27], and ΔV_{th}^{DIBL} is the threshold voltage shift due to drain-induced barrier lowering (DIBL) [4]. By substituting (3) into (2) and performing the integral in (2), the source-drain tunneling probability is obtained as

$$T_{sd}(E_y) = \exp\left[-\frac{\pi\sqrt{2m_{Si}}}{\hbar}\frac{y_{max}(q\phi_{b,sd} - V_{bi} - E_y)}{\sqrt{q(\phi_{b,sd} - V_{bi})}}\right]$$
(7)

where m_{Si} is the electron effective mass along the *y*-direction.



Figure 1. Representation of energy barrier profile (**a**) along the *y*-axis in the source-drain direction and (**b**) along the *x*-axis in the channel-gate direction.

The gate tunneling mechanisms can be primarily divided into Fowler–Nordheim (FN), Poole–Frenkel (PF), and direct tunneling [17,19,23]. For non-stressed devices with a gate oxide thickness below 3 nm, direct tunneling is a dominant mechanism of the gate leakage current [23]. This direct tunneling consists of current components such as channel-gate, S/D-gate, and substrate-gate current. However, for $V_{gs} > 0V$, the channel-gate tunneling current becomes a main component, since a higher oxide voltage between the channel and the gate causes the current to be proportional to the channel length [17,23]. For the channel-gate tunneling path through the oxide layer, the barrier potential $V_b(x)$ will be $V_{b,cg}(x) = \phi_{b,cg} - V_{ox}x/t_{ox}$, where $\phi_{b,cg}$ is the barrier height above the conduction band edge at the Si-SiO₂ interface; V_{ox} is the oxide voltage, and t_{ox} is the oxide thickness. Integration of (2) from x = 0 to $x = t_{ox}$ for the gate tunneling probability with $V_{b,cg}(x)$ gives [16,17]

$$T_{cg}(E_x) = e^{-\frac{4\sqrt{2m_{ox}}t_{ox}}{3hqV_{ox}}[(q\phi_{b,cg} - E_x)^{\frac{3}{2}} - (q\phi_{b,cg} - E_x - qV_{ox})^{\frac{3}{2}}]}$$
(8)

where m_{ox} is the electron effective mass in the oxide.

Introducing the ballistic efficiency γ associated with the carrier flows and integrating over E_t in (1), the general form of the tunneling currents is given by

$$I_{\alpha} = I_0 \gamma \int_0^\infty T_{\alpha}(E_s) \left[F_{\frac{n-3}{2}} \left(\frac{E_{fL} - E_s}{k_b T} \right) - F_{\frac{n-3}{2}} \left(\frac{E_{fR} - E_s}{k_b T} \right) \right] dE_s \tag{9}$$

$$I_0 = \frac{2q\pi^{n-2}}{h} \left(\frac{2m^*k_b T}{h^2}\right)^{\frac{n-1}{2}}$$
(10)

where k_b is the Boltzmann constant; T is the temperature; E_{fL} and E_{fR} are the Fermienergy level from the ground energy at the left and right reservoirs, respectively, and $F_{(n-3)/2}(u_0) = \int_0^\infty u^{(n-3)/2}/(1+e^{u-u_0})du$ is the Fermi–Dirac integral. For the sourcedrain current, (9) gives the total current incorporating both the tunneling current I_{ds}^{tun} $(0 \le E_y \le q(\phi_{b,sd} - V_{bi}))$ in the subthreshold region and the thermionic current I_{ds}^{the} $(q(\phi_{b,sd} - V_{bi}) \le E_y \le \infty)$ in the inversion region [1–5].

3. Shot Noise Model

Shot noise is expressed by introducing the Fano factor Γ defined as $\Gamma = S_{I_{\alpha}}(0)/2qI_{\alpha}$, $S_{I_{\alpha}}(0)$ being the spectral density of current fluctuations at low frequency ($f \approx 0$). Full shot noise ($\Gamma = 1$) is expected if the electrons tunneling through the barrier follow Poisson statistics. The Fano factor is suppressed ($0 < \Gamma < 1$) or enhanced ($\Gamma > 1$), depending on correlations associated with the carrier behavior. The microscopic mechanisms responsible for shot noise have been associated with sequential or coherent tunneling through the potential barrier [28,29]. In the sequential tunneling (ST) model, the interaction of carriers

results in other tunneling events due to fluctuations in the potential [16,21,28,29]. In the case of stress-induced leakage currents (SILCs) through the gate oxide, the noise properties are explained by trap-assisted tunneling, a two-step process in which electrons first tunnel from the channel to a trap in the oxide, then from the trap to the gate [12,21–30]. Analogously, by introducing a two-step tunneling process for the single barrier in both the sourcedrain and channel-gate direction, we can define four different flow rates, namely, the generation flow rate injected from the left or right (F_L^+ or F_R^-) and the recombination flow rate transmitted to the left or right (F_L^- or F_R^+), as shown in Figure 1a,b [31–33]. The electron transport is governed by the total number of carriers between the left reservoir and the barrier, N. In the presence of a generation-recombination process, the flow rates are modulated by the statistical fluctuation ΔN [21]. Using the Langevin equation for the number fluctuation ΔN and performing a Fourier transform, the Fano factor is obtained as [16,21,28,29]

$$\Gamma = 1 - \frac{2}{\tau_R} \frac{\tau}{1 + 4\pi^2 f^2 \tau^2} + \frac{2}{\tau_R^2} \frac{\tau^2}{1 + 4\pi^2 f^2 \tau^2}$$
(11)

$$\frac{1}{\tau} = \frac{1}{\tau_L} + \frac{1}{\tau_R}, \quad \frac{1}{\tau_L} = \frac{dF_L^-}{dN}\Big|_{\overline{N}} - \frac{dF_L^+}{dN}\Big|_{\overline{N}}, \quad \frac{1}{\tau_R} = \frac{dF_R^+}{dN}\Big|_{\overline{N}}$$
(12)

where \overline{N} is the average of N; and τ_L and τ_R are the characteristic times related to ΔN due to current fluctuations through the left and right reservoirs, respectively. If the flow rate of injected carriers from the left, F_L^+ , in the single barrier is independent of N, then the characteristic times τ_L and τ_R given in (12) are represented by the derivatives of the carrier flow to the left, F_L^- , and to the right, F_R^+ , respectively. By extending the Landauer Formula (1), F_L^- and F_R^+ take the forms

$$F_L^{-} = \frac{2}{h} \left(\frac{2\pi m^*}{h^2}\right)^{\frac{n-1}{2}} \gamma^2 \iint (1 - T_\alpha) f_L(1 - f_L) \frac{dE_t}{(\pi E_t)^{\frac{3-n}{2}}} dE_s$$
(13)

$$F_{R}^{+} = \frac{2}{h} \left(\frac{2\pi m^{*}}{h^{2}}\right)^{\frac{n-1}{2}} \gamma \iint T_{\alpha} f_{L} (1 - f_{R}) \frac{dE_{t}}{(\pi E_{t})^{\frac{3-n}{2}}} dE_{s}$$
(14)

In order to calculate the flow rate of carriers for 1D DOS, (13) and (14) can be replaced by integration over the longitudinal energy E_s [11,24]. Taking the derivatives of (13) and (14) with respect to N leads to the expression (i = 1 for L and i = 2 for R)

$$\frac{1}{\tau_{L,R}} = (-1)^{i} \frac{2}{h} \left(\frac{2\pi m^{*}}{h^{2}}\right)^{\frac{n-1}{2}} \gamma^{\frac{2}{t}} \iint \frac{dT_{\alpha}}{dN} f_{L}(1-f_{L,R}) \frac{dE_{t}}{(\pi E_{t})^{\frac{3-n}{2}}} dE_{s}$$
(15)

$$\frac{dT_{\alpha}}{dN} = \frac{q\partial T_{\alpha}}{\partial \phi_{b,\alpha}} \frac{\partial \phi_{b,\alpha}}{\partial N} + \frac{\partial T_{\alpha}}{\partial V_{\alpha}} \frac{\partial V_{\alpha}}{\partial N}$$
(16)

For source-drain tunneling, the source-drain voltage is approximately independent of N, and therefore $\partial V_{\alpha} / \partial N \approx 0$. By differentiating (7) with respect to $\phi_{b,sd}$ and combining all electrostatic effects in a geometrical gate capacitance C_g , the derivatives of the first term in (16) become

$$\frac{q\partial T_{sd}}{\partial \phi_{b,sd}} = -\frac{q\pi\sqrt{2m_{Si}}y_{max}}{\hbar} \frac{1 - \frac{q\phi_{b,sd} - E_y}{2q(\phi_{b,sd} - V_{bi})}}{\sqrt{q(\phi_{b,sd} - V_{bi})}} \cdot T_{sd}$$
(17)

$$\frac{\partial \phi_{b,sd}}{\partial N} = \frac{q}{C_g + C_{Qs} + C_{Qd}}, \quad C_{Qs,d} = q^2 \int_0^\infty D(E) \left(-\frac{\partial f_{s,d}}{\partial E_{fs,d}} \right) dE \tag{18}$$

where $C_g = C_{ox}C_{Qg}/(C_{ox} + C_{Qg})$; C_{ox} is the oxide capacitance; C_{Qg} , C_{Qs} , and C_{Qd} are the quantum capacitances of the gate, source, and drain, respectively, and D(E) is the 2D DOS

in the channel [10,30]. In the case of channel-gate tunneling through the oxide layer, the derivatives are obtained from (8) and (16) as

$$\frac{q\partial T_{cg}}{\partial \phi_{b,cg}} = -\frac{2\sqrt{2m_{ox}}t_{ox}}{\hbar V_{ox}} \left(a^{\frac{1}{2}} - b^{\frac{1}{2}}\right) \cdot T_{cg}$$
⁽¹⁹⁾

$$\frac{\partial T_{cg}}{\partial V_{ox}} = -\frac{4\sqrt{2m_{ox}}t_{ox}}{3\hbar q V_{ox}^2} \left[b^{\frac{1}{2}} \left(a + \frac{1}{2}q V_{ox} \right) - a^{\frac{3}{2}} \right] \cdot T_{cg}$$
(20)

where $\partial \phi_{b,cg} / \partial N \approx q/C_{Qg}$, $\partial V_{ox} / \partial N = -q/C_{Qg}$, $a = q\phi_{b,cg} - E_x$, and $b = q\phi_{b,cg} - E_x - qV_{ox}$ [16]. Note that the set of (11), (12), and (15)–(20) provide the analytical formulation of the shot noise as a function of the applied voltages.

In the coherent tunneling (CT) model, there is no electron scattering during electron transmission through the barrier and the transport is governed by the total transparency of the barrier [14,29]. In the zero frequency limit, the shot noise is given by

$$S_{I_{\alpha}} = 2\frac{2q}{h} \left(\frac{2\pi m^*}{h^2}\right)^{\frac{n-1}{2}} \iint \left\{ T_{\alpha} [f_L(1-f_L) + f_R(1-f_R)] + T_{\alpha}(1-T_{\alpha})(f_L-f_R)^2 \right\} \frac{dE_t}{(\pi E_t)^{\frac{3-n}{2}}} dE_s$$
(21)

where the first and second term describe the injection noise and the partition noise of coherent tunneling, respectively.

4. Results and Discussion

For the calculation of the current and noise, the Fermi-energy of the gate E_{fg} must be numerically solved using the relation [34]

$$e^{\frac{N}{C_{ox}V_{th}}}\left(e^{\frac{N}{C_{ox}V_{th}}}-1\right) = e^{\frac{V_{gs}-V_T}{V_{th}}}$$
(22)

$$N = \frac{C_{Qg}V_{th}}{q}\ln\left(1 + e^{\frac{E_{fg}}{k_bT}}\right)$$
(23)

where $V_{th} = k_b T/q$ is the thermal voltage, and V_T is the threshold voltage. The Fermi energy of the source E_{fs} for the entire on-state and subthreshold behavior is given by [34]

$$E_{fs} = \ln\left[\sqrt{(1+e^{v_d})^2 + 4e^{v_d}\left(e^{2N/N_{Qg}} - 1\right)} - (1+e^{v_d})\right] - \ln(2)$$
(24)

with $v_d = qV_{ds}/k_bT$ and $N_{Qg} = C_{Qg}V_{th}/q$. Figure 2a,b show the calculated SD and gate current, respectively, as functions of V_{gs} for different channel lengths and oxide thicknesses.

The simulation for an n⁺ poly-Si nMOSFET with nitrided oxide was performed using (9) and (10) with $\phi_{b,cg} = 2.6 \text{ eV} t_{ox,eq} = 1.5/2.0 \text{ nm}$, $m_{ox} = 0.4m_0$, $m_{Si} = 0.19m_0$, $V_T = 0.1 \text{ V}$, T = 300 K, $\gamma = 0.5$ for I_{sd} , and $\gamma = 0.9$ for I_{cg} . In the case of high- κ dielectric/metal gate stack, the equivalent oxide thickness $t_{ox,eq}$ should be carefully estimated by the capacitance measurement, because a replacement of gate stack alters the electrical thickness of oxide [35]. For the SD current in the subthreshold region, the tunneling contribution becomes dominant, and thermionic contribution is reduced owing to the high channel barrier.

Figure 3a,b show the comparisons between ST and CT model of the Fano factor for SD and gate tunneling shot noise, respectively, as functions of V_{gs} . As can be seen from Figure 3a, in the subthreshold region, the noise in the SD current is enhanced ($\Gamma > 1$), owing to the positive correlation between tunneling into the channel caused by the interplay between the DOS and electrostatics [11,14]. This is consistent with the results obtained by statistical Monte Carlo simulations [11,36], quantum-mechanical injection model [37,38], and Fermi statistics [39]. Actually, the noise enhancement in the subthreshold region is due to the fact that at low source-drain voltage ($V_{ds} = 0.1$ V), the current is small, while the spectral density of shot noise $S_{I_d}(0)$ tends to a finite value, because of the thermal noise contribution [14].



Figure 2. (a) Calculated SD current versus V_{gs} for $V_{ds} = 0.1V$ (b) calculated gate tunneling current versus V_{gs} for different $t_{ox,eq}$.



Figure 3. Comparison between ST model and CT model of Fano factor as a function of V_{gs} for (**a**) SD current noise and (**b**) gate tunneling current noise.

The results in Figure 3a point out the difference for 2D and 1D DOS when the sequential model is used. The Fano factor for 1D DOS is calculated by using the modified formulations of the SD current of (1), the flow rate of carriers of (13) and (14), and the quantum capacitances of (18). When the dimensionality of DOS is reduced, only few electrons take part in transport so that SD current fluctuations normalized to the SD current can heavily affect the noise behavior [40,41]. For moderately high voltages in the inversion region, f_R is so low that injection noise from the left reservoir is dominant, leading to noise suppression ($\Gamma < 1$) [14,40,41].

As shown in Figure 3b, for verification of the shot noise model for the gate tunneling current, the simulation results are compared with the measured Fano factor for an n+ poly-Si nMOSFET as a function of V_{gs} [21]. It can be seen that large enhancement in gate shot noise at low voltages is in good agreement with the experimental results. This is due to the fact that both of the inverse time constants of the ST model are nearly equal in magnitude, but of opposite signs [16,28,42,43]. As V_{gs} increases, the tunneling probability becomes higher, and noise following a partition process appears [14,44].

5. Conclusions

The general expressions of current and noise are found to be well-suited for computation in a unified manner of the Fano factor of both SD and gate shot noise in sub-10 nm MOSFETs. The sequential tunneling model associated with the number fluctuations predicts enhancement of the shot noise in the SD tunneling current in the subthreshold regime, thus extending the validity of the model to the enhancement of shot noise in the gate tunneling current at low voltages. The resulting analytical compact models may be considered suitable in circuit simulators for circuit-level simulation.

Funding: This work was supported by the National Research Foundation of Korea (NRF) grant funded by the Korea government (MSIT) (No. 2019R1F1A1050640).

Data Availability Statement: The data presented in this study are available on request from the corresponding author.

Conflicts of Interest: The author declares no conflict of interest.

References

- 1. Markov, S.; Kwok, Y.; Li, J.; Zhou, W.; Zhou, Y.; Chen, G. Fundamental limit to scaling Si field-effect transistors due to source-todrain direct tunneling. *IEEE Trans. Electron Devices* **2019**, *66*, 1167–1173. [CrossRef]
- 2. Natori, K. Ballistic metal-oxide-semiconductor field effect transistor. J. Appl. Phys. 1994, 76, 4879–4890. [CrossRef]
- 3. Naveh, Y.; Likharev, K.K. Modeling of 10nm-scale ballistic MOSFET's. IEEE Electron Device Lett. 2000, 21, 242–244. [CrossRef]
- Lin, Y.K.; Duarte, J.P.; Kushwaha, P.; Agarwal, H.; Chang, H.L.; Sachid, A.; Salahuddin, S.; Hu, C. Compact modeling sourceto-drain tunneling in sub-10nm GAA FinFET with industry standard model. *IEEE Trans. Electron Devices* 2017, 64, 3576–3581. [CrossRef]
- 5. Hiblot, G.; Rafhay, Q.; Boeuf, F.; Ghibaudo, G. Analytical relationship between subthreshold swing of thermionic and tunneling currents. *Electron. Lett.* **2014**, *50*, 1745–1747. [CrossRef]
- 6. Kawaura, H.; Baba, T. Direct tunneling from source to drain in nanometer-scale silicon transistor. *Jpn. J. Appl. Phys.* 2003, 42, 351–357. [CrossRef]
- 7. Cheng, H.; Liu, T.; Zhang, C.; Yang, Z.; Liu, Z.; Nakazato, K.; Zhang, Z. Nanowire gate-all-around MOSFETs modeling: Ballistic transport incorporating the source-to-drain tunneling. *Jpn. J. Appl. Phys.* **2020**, *59*, 1–9. [CrossRef]
- Seoane, N.; Martinez, A. A detailed coupled-mode-space non-equilibrium Green's function simulation study of source-to-drain tunneling in gate-all-around Si nanowire metal oxide semiconductor field effect transistors. J. Appl. Phys. 2013, 114, 104307. [CrossRef]
- 9. Naveh, Y.; Korotkov, A.N.; Likharev, K.K. Shot noise suppression in multimode ballistic Fermi conductors. *Phys. Rev. B.* **1998**, *60*, R2169–R2172. [CrossRef]
- 10. Mugnaini, G.; Iannaccone, G. Channel noise modeling of nanoMOSFETs in a partially ballistic transport regime. *J. Comput. Electron.* **2006**, *5*, 91–95. [CrossRef]
- 11. Iannaccone, G.; Betti, A.; Fiori, G. Suppressed and enhanced shot noise in one dimensional field-effect transistors. *J. Comput. Electron.* **2015**, *14*, 94–106. [CrossRef]
- 12. Iannaccone, G. Analytical and numerical investigation of noise in nanoscale ballistic field effect transistors. *J. Comput. Electron.* **2004**, *3*, 199–202. [CrossRef]
- 13. Jia, X.; He, L. Research of shot noise based on realistic nano-MOSFETs. AIP Adv. 2017, 7, 055202. [CrossRef]
- 14. Jannaty, P.; Sabou, F.C.; Le, S.T.; Donato, M.; Bahar, R.I.; Patterson, W.; Mundy, J.; Zaslavsky, A. Shot-noise-induced failure in nanoscale flip-flops-Part I: Numerical framework. *IEEE Trans. Electron Devices* **2012**, *59*, 800–806. [CrossRef]
- 15. Spathis, C.; Birbas, A.; Georgakopoulou, K. Semi-classical noise investigation for sub-40 nm metal-oxide-semiconductor fieldeffect transistors. *AIP Adv.* **2015**, *5*, 087114. [CrossRef]
- 16. Lee, J.; Bosman, G.; Green, K.R.; Ladwig, D. Model and analysis of gate leakage current in ultrathin nitrided oxide MOSFETs. *IEEE Trans. Electron Devices* **2002**, *49*, 1232–1241.
- 17. Ranuarez, J.C.; Deen, M.J.; Chen, C.H. A review of gate tunneling current in MOS devices. *Microelectron. Rel.* 2006, 46, 1939–1956. [CrossRef]
- 18. Clerc, R.; O'Sullivan, P.; McCarthy, K.G.; Ghibaudo, G.; Pananakakis, G.; Mathewson, A. A physical compact model for direct tunneling from NMOS inversion layers. *Solid-State Electron.* **2001**, *45*, 1705–1716. [CrossRef]
- 19. Gehring, A.; Selberherr, S. Modeling of tunneling current and gate dielectric reliability for nonvolatile memory devices. *IEEE Trans. Device Mater. Rel.* **2004**, *4*, 306–319. [CrossRef]
- Mao, L.F.; Li, X.J.; Wang, Z.O.; Wang, J.Y. The gate leakage current in graphene field-effect transistor. *IEEE Electron Device Lett.* 2008, 29, 1047–1049. [CrossRef]
- Lee, J.; Bosman, G.; Green, K.R.; Ladwig, D. Noise model of gate-leakage current in ultrathin oxide MOSFETs. *IEEE Trans. Electron Devices* 2003, 50, 2499–2506.
- 22. Deen, M.J.; Chen, C.H.; Asgaran, S.; Rezvani, G.A.; Tao, J.; Kiyota, Y. High-frequency noise of modern MOSFETs: Compact modeling and measurement issues. *IEEE Trans. Electron Devices* **2006**, *53*, 2062–2081. [CrossRef]
- 23. Manghisoni, M. Gate current noise in ultrathin oxide MOSFETs and its impact on the performance of analog front-end circuits. *IEEE Trans. Nucl. Sci.* 2008, 55, 2399–2407. [CrossRef]

- 24. Taur, Y.; Wu, J.; Min, J. Dimensionality dependence of TFET performance down to 0.1 V supply voltage. *IEEE Trans. Electron Devices* 2016, *63*, 877–880. [CrossRef]
- Chun, M.W.; Wong, K.L.; Hamzah, A.; Rusli, S.; Alias, N.E.; Lim, C.S.; Tan, M.L.P. A review of the top of the barrier nanotransistor models for semiconductor nanomaterials. *Superlatt. Microstruct.* 2020, 140, 106429. [CrossRef]
- 26. Tanaka, H.; Suda, J.; Kimoto, T. Analysis of ballistic and quasi-ballistic hole properties in germanium nanowires based on an extended "Top of the Barrier" model. *Solid-State Electron.* **2016**, *123*, 143–149. [CrossRef]
- 27. Pregaldiny, F.; Lallement, C.; van Langevelde, R.; Mathiot, D. An advanced explicit surface potential model physically accounting for the quantization effects in deep-submicron MOSFETs. *Solid-State Electron.* **2004**, *48*, 427–435. [CrossRef]
- 28. Aleshkin, V.Y.; Reggiani, L. Electron transport and shot noise in double-barrier resonant diodes: The role of Pauli and Coulomb correlations. *Phys. Rev. B.* **2001**, *64*, 245333. [CrossRef]
- 29. Aleshkin, V.Y.; Reggiani, L. Comparative analysis of sequential and coherent tunneling in resonant diodes. *Phys. Rev. B.* 2006, 73, 165320. [CrossRef]
- Iannaccone, G.; Crupi, F.; Neri, B.; Lombardo, S. Theory and experiment of suppressed shot noise in stress-induced leakage current. *IEEE Trans. Electron Devices* 2003, 50, 1363–1369. [CrossRef]
- 31. Datta, S.; Assad, F.; Lundstrom, M.S. The Si MOSFET from a transmission viewpoint. *Superlatt. Microstruct.* **1998**, *23*, 771–780. [CrossRef]
- 32. Rahman, A.; Lundstrom, M.S. A compact scattering model for the nanoscale double-gate MOSFET. *IEEE Trans. Electron Devices* **2002**, *49*, 481–489. [CrossRef]
- 33. Natori, K. Comapct modeling of ballistic nanowire MOSFETs. IEEE Trans. Electron Devices 2008, 55, 2877–2885. [CrossRef]
- 34. Marin, E.G.; Bader, S.J.; Jena, D. A new holistic model of 2-D semiconductor FETs. *IEEE Trans. Electron Devices* **2018**, *65*, 1239–1245. [CrossRef]
- 35. Robertson, J.; Wallace, R.M. High-k materials and metal gates for CMOS applications. *Materi. Sci. Eng. R.* **2015**, *88*, 1–41. [CrossRef]
- 36. Betti, A.; Fiori, G.; Iannaccone, G. Enhanced shot noise in carbon nanotube field-effect transistors. *Appl. Phys. Lett.* **2009**, *95*, 252108. [CrossRef]
- 37. Oriols, X. Quantum mechanical effects on noise properties of nanoelectronic devices: Application to Monte Carlo simulation. *IEEE Trans. Electron Devices* **2003**, *50*, 1830–1836. [CrossRef]
- 38. Oriols, X.; Martin, F.; Sune, J. Approach to study the noise properties in nanoscale electronic devices. *Appl. Phys. Lett.* **2001**, *79*, 1703–1705. [CrossRef]
- 39. Gonzalez, T.; Mateos, J.; Pardo, D.; Gomila, G.; Cantalapiedra, I.R.; Reggiani, L. Role of energy correlations on Coulomb suppression of shot noise in ballistic conductors contacted to degenerate reservoirs. *Phys. Rev. B.* **2003**, *68*, 075309. [CrossRef]
- 40. Betti, A.; Fiori, G.; Iannaccone, G. Shot noise suppression in quasi-one-dimensional field-effect transistors. *IEEE Trans. Electron Devices* **2009**, *56*, 2137–2143. [CrossRef]
- Oriols, X.; Fernandez-Diaz, E.; Alvarez, A.; Alarcon, A. An electron injection model for time-dependent simulators of nanoscale devices with electron confinement: Application to the comparison of the intrinsic noise of 3D-, 2D-, 1D-ballistic transistors. *Solid-State Electron.* 2007, *51*, 306–319. [CrossRef]
- 42. Aleshkin, V.Y.; Reggiani, L.; Reklaitis, A. Current instability and shot noise in nanometric semiconductor heterostructures. *Nanotechnology* **2000**, *73*, 370–374. [CrossRef]
- 43. Aleshkin, V.Y.; Reggiani, L. Reklaitis, Sub- and super-Poissonian shot noise in single-barrier semiconductor structures. *Semicond. Sci. Technol.* **2000**, *15*, 1045–1048. [CrossRef]
- 44. Bulashenko, O.M.; Rubi, J.M. Quantum suppression of shot noise in field emitters. Phys. Rev. B. 2003, 67, 115222. [CrossRef]