# Design and Fabrication of a Ka Band RF MEMS Switch with High Capacitance Ratio and Low Actuation Voltage 

Kun Deng ${ }^{1, *}$, Fuxing Yang ${ }^{1}$, Yucheng Wang ${ }^{2}$, Chengqi Lai ${ }^{2}$ and Ke Han ${ }^{2}$ ©<br>1 School of Automation, Beijing University of Posts and Telecommunications, Haidian District, Beijing 100876, China; yangfx@bupt.edu.cn<br>2 School of Electronic Engineering, Beijing University of Posts and Telecommunications, Haidian District, Beijing 100876, China; wangyucheng@bupt.edu.cn (Y.W.); laichengqi@bupt.edu.cn (C.L.); hanke@bupt.edu.cn (K.H.)<br>* Correspondence: dengkun@bupt.edu.cn; Tel.: +86-010-6119-8559

Citation: Deng, K.; Yang, F.; Wang, Y.; Lai, C.; Han, K. Design and Fabrication of a Ka Band RF MEMS Switch with High Capacitance Ratio and Low Actuation Voltage.
Micromachines 2022, 13, 37.
https://doi.org/10.3390/ mi13010037

Academic Editor: Niall Tait

Received: 30 November 2021
Accepted: 23 December 2021
Published: 28 December 2021
Publisher's Note: MDPI stays neutral with regard to jurisdictional claims in published maps and institutional affiliations.


Copyright: © 2021 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/).


#### Abstract

In this paper a high capacitance ratio and low actuation voltage RF MEMS switch is designed and fabricated for Ka band RF front-ends application. The metal-insulator-metal (MIM) capacitors is employed on a signal line to improve the capacitance ratio, which will not degrade the switch reliability. To reduce the actuation voltage, a low spring constant bending folding beam and bilateral drop-down electrodes are designed in the MEMS switch. The paper analyzes the switch pull-in model and deduces the elastic coefficient calculation equation, which is consistent with the simulation results. The measured results indicated that, for the proposed MEMS switch with a gap of $2 \mu \mathrm{~m}$, the insertion loss is better than -0.5 dB and the isolation is more than -20 dB from 25 to 35 GHz with an actuation voltage of 15.8 V . From the fitted results, the up-state capacitance is 6.5 fF , down-state capacitance is 4.3 pF , and capacitance ratios is 162 . Compared with traditional MEMS capacitive switches with dielectric material $\mathrm{Si}_{3} \mathrm{~N}_{4}$, the proposed MEMS switch exhibits high on/off capacitance ratios of 162 and low actuation voltage.


Keywords: RF MEMS switch; low voltage; capacitance radio; MIM capacitors

## 1. Introduction

In today's competitive wireless market, the compact, low cost, reconfigurable and multi-function radio frequency (RF) blocks is the main research focus. The radio frequency micro-electromechanical systems (RF MEMS) switch draws considerable attention owing to its attractive performance, as a key enabler for reconfigurable RF front-ends [1,2]. Compared to PIN diode or FET switches, an RF MEMS switch possess many advantages, such as high isolation, low insertion loss and low or near-zero power consumption [3], and it is widely applied in reconfigurable antenna, tunable filter, phase shifting network and automatic test equipment [4,5].

There are two important indices to measure the switch performance, Capacitance ratio and actuation voltage. A high capacitance ratio is beneficial to achieve high isolation and excellent RF performance, for example, in the application of the tunable filter, the capacitance ratio of the switch determines the adjustable range of the center frequency of the resonant unit in the tunable filter. The actuation voltage reflects the integration performance of the switch, which contributes to the monolithic microwave integrated circuit (MMIC) implementation.

To date, many RF MEMS switches with a high capacitance ratio have been proposed with a large gap between beam and signal transmission line [6,7], a relatively thinner dielectric layer [8] or high dielectric constant materials [9-12] including $\mathrm{HfO}_{2}\left(\varepsilon_{r}=20\right)$, STO $\left(\varepsilon_{r}=30-120\right), \mathrm{Ta}_{2} \mathrm{O}_{5}\left(\varepsilon_{r}=32\right)$. However, the mechanical property of an RF MEMS switch is altered when the height of the beam is changed. In capacitive MEMS switches when the pull-down voltage is applied and the beam comes in contact with the dielectric, charge
carriers are injected and then trapped in the dielectric layer, which leads to a change in the pull-down voltage or in the phenomenon of stiction which seriously limits the functionality of devices [12]. Furthermore, if the dielectric layer is thinner, the reliability of switches will be degraded due to dielectric breakdown. Meanwhile, many researchers have paid efforts to design low spring constants for a low actuation voltage, for example, Ya, M.L. et al. [13] designed a COMS RF MEMS switch with a relatively wide beam to achieve a low pull-down actuation of 3 V , but it cannot be easily released. Yong, Q . X. et al. [14] designed a switch with 14 V pull-down voltage, which can be used on frequency reconfigurable antenna. Li-Ya Ma et al. [15] design a $1.397 \mu$ m-thick switch beam with a low actuation voltage switch of 3 V , however, the beam is very thin, which is prone to warpage or fracture.

In this paper, a high capacitance ratio and low actuation voltage RF MEMS switch with a $\mathrm{Si}_{3} \mathrm{~N}_{4}\left(\varepsilon_{r}=7.6\right)$ standard dielectric material and a relatively lower gap of $2 \mu \mathrm{~m}$ is designed and fabricated for Ka band RF front-end application, as shown in Figure 1. To reduce the actuation voltage, a low spring constant bending folding beam and bilateral drop-down electrodes are designed in the MEMS switch. The measured results indicated that, for the proposed MEMS switch with a gap of $2 \mu \mathrm{~m}$, the insertion loss is better than -0.5 dB and the isolation is more than -20 dB from 25 to 35 GHz with an actuation voltage of 15.8 V . From the fitted results, the up-state capacitance is 6.5 fF , down-state capacitance is 4.3 pF , and capacitance ratios is 162 .


Figure 1. Switch diagram. Adapted with permission from ref. [16]. Copyright 2020, Springer Nature.

## 2. Design of the High Capacitance Ratio and Low Actuation Voltage Radio Frequency Micro-Electromechanical Systems (RF MEMS) Switch

2.1. Metal-Insulator-Metal (MIM) Fixed Capacitor Design to Improve Switch Capacitance Ratios

A traditional capacitive RF MEMS switches include a coplanar waveguide (CPW) to transmission line, a floating membrane as an actuator, and an electrode to provide a bias voltage. By loading a bias voltage, the switch can switch between on and off states. The up-state capacitance $C_{u}$, down-state capacitance $C_{d}$ and capacitive ratio $C_{r}$ can be calculated as follows [17]:

$$
\begin{equation*}
C_{r}=\frac{C_{d}}{C_{u}}=\frac{\frac{\varepsilon_{0} \varepsilon_{r} A}{t_{d}}}{\frac{\varepsilon_{0} A}{g+\frac{t_{d}}{\varepsilon_{r}}}+C_{f}}=\frac{g \varepsilon_{r}}{t_{d}}+1 \tag{1}
\end{equation*}
$$

where $\varepsilon_{r}$ and $\varepsilon_{0}$ are the relative permittivity of the dielectric layer and the permittivity of free space, respectively, $t_{d}$ is the thickness of the dielectric layer, $g$ is the air gap, $C_{f}$ is the edge capacitance between the membrane and the transmit signals when the switch is in up-state, and it can be omitted in most cases.

The surface roughness of switch beam has a great influence on $C_{d}$. When considering roughness, $C_{d}$ can be calculated by Equation (2):

$$
\begin{equation*}
C_{d}=\frac{\varepsilon_{0} A}{2}\left(\frac{1}{t_{d}+\frac{d_{1}}{\varepsilon_{r}}}+\frac{\varepsilon_{r}}{d_{1}}\right), \quad C_{p p}=\frac{\varepsilon_{0} \varepsilon_{r} A}{t_{d}} \tag{2}
\end{equation*}
$$

where $d_{1}$ is the roughness, as shown in Figure 2, when the roughness is $50 \mathrm{~nm}, C_{d}$ is reduced by $30 \%$. The dielectric thickness and $\varepsilon_{r}$ also have a greater impact on the capacitance.


Figure 2. Simulation curves of roughness and $C_{d}$.
A simple and effective approach to obtain a high capacitance ratio of a MEMS switch is to connect a shunt capacitor $[16,18]$. To improve the capacitance ratio without degrading the reliability of the switches, a metal-insulator-metal (MIM) fixed capacitors is adopted in the design, as shown in Figure 3. The MIM capacitor connects to a metal-air-metal (MAM) capacitor in series in up state, and the MAM is changed to resistance $R$ in down-state. $C_{r}$ can be calculated by the following equation:

$$
\begin{align*}
C_{r} & =\frac{C_{d}}{C_{u}}=\frac{C_{M I M}}{C_{M I M} \| C_{M A M}}=\frac{C_{M I M}}{C_{M A M}}+1  \tag{3}\\
& =\frac{\varepsilon_{r} g}{t_{d}} \frac{A_{M I M}}{A_{M A M}}+1=\frac{\varepsilon_{r} g}{t_{d}} \lambda+1 \\
C_{M A M} & =\frac{\varepsilon_{0}}{g} A_{M A M}, C_{M I M}=\frac{\varepsilon_{0} \varepsilon_{r}}{t_{d}} A_{M I M} \tag{4}
\end{align*}
$$

where $\lambda$ is equal to $A_{\text {MIM }} / A_{M A M}$, other parameters are defined in Equation (1).


Figure 3. The metal-insulator-metal (MIM) capacitor switch radio frequency (RF) model. Adapted with permission from ref. [16]. Copyright 2020, Springer Nature.

In order to compare the influence of MIM capacitor size on the capacitance ratio, simulate a switch based on silicon $\left(\varepsilon_{r}=11.9\right)$. A $0.15 \mu \mathrm{~m}$-thick $\mathrm{Si}_{3} \mathrm{~N}_{4}$ standard dielectric material is used. The CPW line has a size of $60 / 100 / 60(G / S / G) \mu \mathrm{m}$, and the size of the beam is $220 \mu \mathrm{~m}$ width and $500 \mu \mathrm{~m}$ length. For the switch without MIM capacitors, the up-state capacitance, down-state capacitance, and capacitance ratios can be calculated simply [19] as 57.8 fF and $3.18 \mathrm{pF}, 55$. Figure 4 shows the relationship among the parameter of $w_{1}, w_{2}$, and $C_{r}$ for the switch with MIM capacitors.


Figure 4. $C_{r}$ with and without MIM. (a) the relationship between $C_{r}$ and $w_{1}$. (b) the relationship between $C_{r}$ and $w_{2}$ [16].

It can be seen from the Figure 3 that the MIM capacitor can effectively improve the capacitance ratio of the switch. When $w_{1}=10 \mu \mathrm{~m}, C_{r}$ is increased with the increase of the value of $w_{2}$. And when $w_{2}=30 \mu \mathrm{~m}, C_{r}$ is decreased with the increase of the value of $w_{1}$. It can be found that $C_{r}=206$ with $g=2 \mu \mathrm{~m}, t_{d}=0.15 \mu \mathrm{~m}, w_{1}=10 \mu \mathrm{~m}, w_{2}=30 \mu \mathrm{~m}$.

### 2.2. Low Spring Constant Bending Folding Beam Switch Design and Pull-Down Model

Due to the small amount of deformation, the switch beam can be equivalent to a rigid body, and its mechanical properties can be described by linear elastic coefficients [16]. A simple and effective approach to obtain a low actuation voltage of a MEMS switch is to reduce the spring constant of the beam. This paper designs a switch structure as shown in Figure 4 a . In order to reduce the spring constant, the switch uses a bending folding beam, which is fixed on the ground by four anchor points. The switch structure and simplified model are shown in Figure 5.


Figure 5. (a) Detailed size of the switch; (b) equivalent model of switch [16].
The pull-down voltage of proposed switch is [18]:

$$
\begin{equation*}
V_{p}=\sqrt{\frac{8 k_{\text {total }}\left(g+t_{d} / \varepsilon_{r}\right)^{3}}{27 \varepsilon_{0} A_{\text {total }}}} \tag{5}
\end{equation*}
$$

$$
\begin{equation*}
A_{\text {total }}=2\left(S_{e}-N \pi\left(R_{i}\right)^{2}\right) \tag{6}
\end{equation*}
$$

where $A_{\text {total }}$ is the area of the driving force, $R_{i}$ is the radius of the circular slots, $N$ is the number of circular slots, $S_{e}$ is the area of driving electrode. Other parameters are the same as previously defined. The spring constants $k_{\text {total }}$ of the beam determines the pull-down voltage, and can be calculated as:

$$
\begin{gather*}
k=F / \delta, \quad \delta=\left(\partial V_{\varepsilon}\right) /(\partial F)  \tag{7}\\
V_{\varepsilon}=\sum_{i}^{n}\left(\int_{l} \frac{M_{i}(x)^{2}}{2 E I} \mathrm{~d} x+\int_{l} \frac{T_{i}(x)^{2}}{2 G I_{t}} \mathrm{~d} x\right) \tag{8}
\end{gather*}
$$

where $F$ is the applied force, $\delta$ is the displacement of the structure in the direction of the applied force, and $V_{\varepsilon}$ is the strain energy under the applied force. $M(x)$ is the bending moment of the structure, $T(x)$ is the torque, $E$ is the elastic modulus of the material, $G$ is shear modulus, $I=w t^{3} / 12$ is the moment of inertia of the section, and $I_{t}=0.3 w t^{3}$ is the polar moment of inertia.

As shown in Figure 6a, in order to calculate the spring constants of the supporting beam, the equivalent mechanical spring structure can be divided into (1) (2) (3) (4) four parts, one end is specified and the other end receives a force $F$ perpendicular to the surface of the paper. Then the strain energy of each part are calculated in Table 1.

$$
\begin{equation*}
 \tag{9}
\end{equation*}
$$



Figure 6. (a) Equivalent mechanical spring model; (b) switch beam simulation diagram.

Table 1. $M(x), T(x)$ and $V_{\varepsilon}$ of equivalent mechanical spring structure.

| Part | $\boldsymbol{M}(\boldsymbol{x})$ | $\boldsymbol{T}(\boldsymbol{x})$ | $\boldsymbol{V}_{\boldsymbol{\varepsilon}}$ |
| :---: | :---: | :---: | :---: |
| $(1)$ | $M_{1}(x)=F x_{1}$ | $T_{1}(x)=0$ | $V_{\varepsilon 1}=\frac{F^{2} a_{1}^{3}}{24 I}$ |
| (2) | $M_{2}(x)=F x_{2}$ | $T_{2}(x)=F a_{1}$ | $V_{\varepsilon 2}=\frac{F^{2} a_{1}^{3}}{24 I I}+\frac{F^{2} a_{1}^{2} a_{2}}{2 G I I_{t}}$ |
| (3) | $M_{3}(x)=F x_{3}$ | $T_{3}(x)=F a_{2}$ | $V_{\varepsilon 3}=\frac{F^{2}\left(\left(a_{1}+a_{3}\right)^{3} / 8-a_{1}^{3}\right)}{6 E I}+\frac{F^{2} a_{2}^{2} a_{3}}{2 G I_{t}}$ |
| (4) | $M_{4}(x)=F x_{4}$ | $T_{4}(x)=F\left(a_{1}+a_{3}\right)$ | $V_{\varepsilon 4}=\frac{F^{2} a_{4}^{3}}{24 E I}+\frac{F^{2} a_{3}^{3}}{24 E I}+\frac{F^{2}\left(a_{1}+a_{3}\right)^{2}\left(a a_{4}+a_{5}\right)}{2 G I_{t}}$ |

The switch is equivalent to the parallel connection of four mechanical spring as shown in Figure 6a, the total spring constant of the switch can be calculated as:

$$
\begin{equation*}
k_{\text {total }}=\left(k_{1}\left\|k_{2}\right\| k_{3} \| k_{4}\right) \tag{11}
\end{equation*}
$$

In this paper, the switch design is based on $\mathrm{Au}(E=78 \mathrm{Gpa}$, Poisson ratio $v=0.44)$, length and width of the beam of $500 \mu \mathrm{~m}$ and $220 \mu \mathrm{~m}$ respectively, the gap of $2 \mu \mathrm{~m}$, and other physical dimensions of the switch shown in Table 2. The $K_{\text {total }}$ is $1.3967 \mathrm{~N} / \mathrm{m}$ calculated from Equations (6)-(11), and the simulation value is $1.2 \mathrm{~N} / \mathrm{m}$ with the help of finite element analysis software COMSOL in the electromagnetic environment, and as shown in Figure 6b, the calculated values match well with the simulated ones. The voltage can be calculated to 9.2 V and 9 V , from Equation (5).

Table 2. Physical dimensions of the switch [16].

| Symbol | Description | Value |
| :---: | :---: | :---: |
| $l_{1}$ | Length of the anchor | $15 \mu \mathrm{~m}$ |
| $l_{2}$ | Length in the mechanical spring | $170 \mu \mathrm{~m}$ |
| $l_{3}$ | Length in the mechanical spring | $30 \mu \mathrm{~m}$ |
| $l_{4}$ | Length in the rectangular plate | $150 \mu \mathrm{~m}$ |
| $l_{5}$ | Length in the mechanical spring | $20 \mu \mathrm{~m}$ |
| $l_{6}$ | Length of the rectangular hole | $60 \mu \mathrm{~m}$ |
| $l_{7}$ | Length in the rectangular plate | $50 \mu \mathrm{~m}$ |
| $\mathrm{l}_{\text {beam }}$ | Length of the beam | $500 \mu \mathrm{~m}$ |
| $\mathrm{w}_{1}$ | Width of the anchor | $30 \mu \mathrm{~m}$ |
| $\mathrm{w}_{2}$ | Width in the mechanical spring | $20 \mu \mathrm{~m}$ |
| $\mathrm{w}_{3}$ | Width in the mechanical spring | $10 \mu \mathrm{~m}$ |
| $\mathrm{w}_{4}$ | Width in the mechanical spring | $10 \mu \mathrm{~m}$ |
| $\mathrm{w}_{5}$ | Width in the rectangular plate | $70 \mu \mathrm{~m}$ |
| $\mathrm{w}_{6}$ | Width of the rectangular hole | $30 \mu \mathrm{~m}$ |
| $\mathrm{w}_{7}$ | Width in the MIM capacitor | $30 \mu \mathrm{~m}$ |
| $\mathrm{w}_{8}$ | Width in the MIM capacitor | $10 \mu \mathrm{~m}$ |
| $\mathrm{w}_{\mathrm{beam}}$ | Width of the beam | $220 \mu \mathrm{~m}$ |
| N | Number of circular slots | 56 |
| $\mathrm{R}_{\mathrm{i}}$ | Radius of circular slots | $4 \mu \mathrm{~m}$ |
| $\mathrm{t}_{\mathrm{beam}}$ | Thickness of the beam | $1.6 \mu \mathrm{~m}$ |
| $\mathrm{t}_{\mathrm{d}}$ | Thickness of the dielectric layer | $0.15 \mu \mathrm{~m}$ |
| $\mathrm{t}_{\mathrm{MIM}}$ | Thickness of the MIM capacitor | $1 \mu \mathrm{~m}$ |
| $\mathrm{~S}_{\mathrm{e}}$ | Area of actuation electrodes | Air gap |
| g |  | $100 \times 150 \mu \mathrm{~m}$ |
| $\varepsilon_{\mathrm{r}}$ | Relative permittivity of dielectric layer | $2 \mu \mathrm{~m}$ |
|  |  | 7.6 |

## 3. Physical Dimensions of the Proposed Switch

Table 2 shows the detailed physical parameters of the proposed switches. In this paper, the switch is based on silicon $\left(\varepsilon_{r}=11.9\right)$, and a $0.15 \mu$ m-thick $\mathrm{Si}_{3} \mathrm{~N}_{4}$ standard dielectric material is used. Au material is used to make the switch beam.

## 4. Discussion

### 4.1. Fabrication

The fabrication process of the MEMS switch is shown in Figure 7. The switch is fabricated on $400 \mu$ m thickness high-resistance silicon, and a layer of $\mathrm{SiO}_{2}$ is located above it. Furthermore, photolithograph negative glue was used to photoetching DC bias lines pattern, electron beam evaporation $C_{r}$ DC bias lines, then stripped photolithograph negative glue, and $0.15 \mu \mathrm{~m}$-thickness $\mathrm{Si}_{3} \mathrm{~N}_{4}$ is deposited on top of the bias lines. Au layer of $0.2 \mu \mathrm{~m}$ thickness, as CPW lines, is sputtered on the substrate, and a layer of $\mathrm{Si}_{3} \mathrm{~N}_{4}$ is located on the top of the transmission lines as a dielectric layer. A $2 \mu$ m-thick polyimide is used as the sacrificial layer after the thermal curing process. The $1.6 \mu \mathrm{~m}$-thick anchors and beams are formed by electroplating technology. The sacrificial layer is released using supercritical dry release. Figure 8 show the micrograph of the fabricated MEMS switch.


Figure 7. The process of the device: (a) silicon substrate after oxidation; (b) after patterning of the high-resistance direct current (DC)-bias line; (c) after patterning of the $\mathrm{Si}_{3} \mathrm{~N}_{4}$ dielectric layer; (d) after patterning of coplanar waveguide (CPW) lines; (e) spinning of polyimide to form sacrificial layer; (f) Chemical mechanical polishing (CMP) to decrease surface roughness of polyimide; (g) patterning of gold layer to form MEMS beams and anchors; (h) removal of the sacrificial layer.


Figure 8. Micrograph of the fabricated MEMS switch.
The capacitance ratio of the switched capacitor is heavily influenced by the roughness of the capacitive area of the beam. In order to decrease surface roughness of polyimide, this paper proposes chemical mechanical polishing (CMP) with the condition of fine polishing pad, strong alkali ( PH 10 ), high temperature $\left(60^{\circ} \mathrm{C}\right)$, and speed $63 / 58$. Figure 9 a presents the thickness of the PI vary across the wafer before and after CMP. The roughness of polyimide before CMP is $300-500 \mathrm{~nm}$. It can be found that surface roughness of real contact areas for the beam is controlled within 10 nm that can effectively ensure the capacitance ratio.


Figure 9. (a) PI thickness changes before and after CMP (b) Roughness test results.

### 4.2. Measurement and Analysis

The measurements were carried out in an unpackaged and standard laboratory environment. The vector Network Analyzer (R\&S ZVA50, Rohde and Schwarz, Munich, Germany) is used to measure the S (S11 and S21) parameters of the device. Two gold ACP-A-GSG-150 probes (Cascade Microtech, Beaverton, OR, USA) are used to contact the two ends of the device, and the device is placed on a probe table (Cascade Summit 11000B-M, Cascade Microtech, Beaverton, OR, USA). The sweep frequency is from $10 \mathrm{MHz}-40 \mathrm{GHz}$. Simulating the proposed switch through the HFSS software.

As shown in Figure 10, $\mathrm{S}_{11}$ and $\mathrm{S}_{12}$ of the measured and simulated results are well matched. The measured results show that the return loss is better than -1 dB at 30 GHz in up stated condition, and it is more than -10 dB from 25 GHz to 35 GHz in down stated condition. The insertion loss is better than -0.5 dB at 30 GHz , and the isolation is more than -20 dB from 25 GHz to 35 GHz with an actuation voltage of 15.8 V . The maximum isolation is achieved -39.2 dB at 31.3 GHz .


Figure 10. S-parameters of the MEMS switch: (a) S11 of the measured and simulated results; (b) S12 of the measured and simulated results.

The measurement results show that the central frequency point and actuation voltage of the fabricated switch are higher than the simulation and theoretical calculation values, and these errors may be generated from the following two aspects: the switch beam warpage caused by stress gradient [17] leads to a larger gap between the beam and the electrodes, which increases the actuation voltage, and changes the downstate capacitance due to insufficient contact, as shown in Figure 11; due to the residual stress during the
fabrication process, the spring constants of the beam is increased, which leads to a larger actuation voltage. As shown in Figure 12, when the load is applied on both sides of the beam, the spring constants are composed of stiffness and residual stress, and total spring constant of the switch can be calculated as [17]:

$$
\begin{equation*}
K_{\text {total }}=K_{s}+K_{r}=32 E w(t / l)^{3} \frac{1}{(x / l)(1-(x / l))^{2}}+4 \sigma(1-v) w(t / l) \frac{1}{1-(x / l)} \tag{12}
\end{equation*}
$$

where $K_{s}, K_{r}$ are the spring constants caused by stiffness and residual stress respectively, $x$ is the position of the load, $\sigma$ is the residual stress. When $x=2 l / 3, \sigma=0 \mathrm{MPa}, 40 \mathrm{MPa}, 80 \mathrm{MPa}$, $l=200 \mu \mathrm{~m}, t / l=(0.002-0.006)$, as shown in Figure 12, the total spring constants of the Au ( $E=78 \mathrm{GPa}, v=0.44$ ) beam increases significantly with the increase of residual stress.


Figure 11. Micrograph of the fabricated MEMS switch.


Figure 12. Relationship between elastic coefficient and residual stress.
$C_{u}, C_{d}, L$ and $R$ can be extracted from the isolation $S_{12}$. The RF MEMS switch and CPW transmission line consist of five parts and can be expressed by the ABCD matrix [19]:

$$
\left(\begin{array}{ll}
A & B  \tag{13}\\
C & D
\end{array}\right)=\mathrm{M}_{1} \mathrm{M}_{2} \mathrm{M}_{3} \mathrm{M}_{2} \mathrm{M}_{1}
$$

where $\mathrm{M}_{1}$ and $\mathrm{M}_{2}$ represents the CPW transmission line part. $\mathrm{M}_{3}$ represents the lumped parameter model of the RF MEMS. They are:

$$
\begin{gather*}
\mathrm{M}_{1}=\left(\begin{array}{cc}
\cos \theta_{1} & \mathrm{Z}_{1} \tan \theta_{1} \\
j \frac{1}{Z_{1}} \sin \theta_{1} & \cos \theta_{1}
\end{array}\right) \mathrm{M}_{2}=\left(\begin{array}{cc}
\cos \theta_{2} & \mathrm{Z}_{2} \tan \theta_{2} \\
j \frac{1}{Z_{2}} \sin \theta_{2} & \cos \theta_{2}
\end{array}\right)  \tag{14}\\
\mathrm{M}_{3}=\left(\begin{array}{cc}
1 & 0 \\
Y_{3} & 1
\end{array}\right) \\
Y_{3}(\text { down - state })=\left[\left(j \omega C_{d}\right)^{-1}+j \omega L+R_{s}\right]^{-1}=\left[\left(j \omega C_{M I M}\right)^{-1}+j \omega L+R_{s}\right]^{-1} \\
Y_{3}(\text { up }- \text { state })=\left[\left(j \omega C_{u}\right)^{-1}+j \omega L+R_{s}\right]^{-1}=\left[\left(j \omega C_{M I M} \| C_{M A M}\right)^{-1}+j \omega L+R_{s}\right]^{-1}
\end{gather*}
$$

where $\left(Z_{1}, \theta_{1}\right)$ and $\left(Z_{2}, \theta_{2}\right)$ are the characteristic impedances and the electrical lengths of the transmission lines with different sizes, $Z_{1}=52.8 \Omega, \theta_{1}=7.65^{\circ}, Z_{2}=95.2 \Omega, \theta_{1}=3.82^{\circ}$ at 30 GHz , the resistance of $M A M$ is ignored, $S_{21}$ is:

$$
\begin{equation*}
S_{21}=\frac{2}{A+B / Z_{0}+C Z_{0}+D} \tag{15}
\end{equation*}
$$

From Equations (12)-(15), for the proposed switch, $C_{u}=26.5 \mathrm{fF}, C_{d}=4.3 \mathrm{pF}, C_{r}=162$, the MIM capacitors can improve the on/off capacitance ratio.

Compared with other capacitive RF MEMS switch, as shown in Table 3, the proposed switch in this work shows a lower actuation voltage and a significant increase of capacitance ration.

Table 3. Comparison of developed capacitive RF-MEMS switches. Adapted with permission from ref. [16]. Copyright 2020, Springer Nature.

| Author | $C_{\text {up }}$ (fF) | $C_{\text {down }}$ ( $\mathbf{p F}$ ) | $C_{r}$ | Insertion Loss (dB) | Isolation (dB) | Actuation Voltage (V) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Persano, A. [20] | 70 | 1.32 | 18.8 | $<0.8 @ 30 \mathrm{GHz}$ | $38 @ 23 \mathrm{GHz}$ | $15-20$ |
| Park, J. [10] | 83 | 50 | 600 | 0.08 | $42 @ 28 \mathrm{GHz}$ | 8 |
| Badia, M.F. [21] | - | 1.27 | - | $0.68 @ 40 \mathrm{GHz}$ | $35.8 @ 40 \mathrm{GHz}$ | 23.6 |
| Fall, M. [22] | 4.8 | 0.16 | 33.3 | $0.3 @ 20 \mathrm{GHz}$ | $<1.5 @ 20 \mathrm{GHz}$ | 38 |
| Deepak, B. [23] | - | - | - | $<0.1 @ 20 \mathrm{GHz}$ | $43 @ 9.5 \mathrm{GHz}$ | 20 |
| Li-Ya, M. [15] | 140 | 7.31 | 52 | $5.65 @ 40 \mathrm{GHz}$ | $24.38 @ 40 \mathrm{GHz}$ | 3.04 |
| Xu, Y.Q. [14] | - | - | - | $0.1 @ 30 \mathrm{GHz}$ | $26 @ 30 \mathrm{GHz}$ | 14 |
| Ke, H. [24] | 54 | 20.8 | 385 | $<0.5 @ 40 \mathrm{GHz}$ | $34 @ 10 \mathrm{GHz}$ | 21 |
| Fouladi [7] | 23 | 2.1 | 91 | $0.98 @ 20 \mathrm{GHz}$ | $17.9 @ 20 \mathrm{GHz}$ | 82 |
| Liu, Y. [25] | 40 | 7 | 175 | $1.51 @ 40 \mathrm{GHz}$ | $>30 @ 16-36 \mathrm{GHz}$ | 35 |
| Yu, A. [26] | 34.4 | 1.3 | 38 | $<0.4 @ D C-40 \mathrm{GHz}$ | $27 @ 40 \mathrm{GHz}$ | - |
| Ziaei, A. [27] | - | - | 400 | $0.1 @ 10 \mathrm{GHz}$ | $38 @ 10 \mathrm{GHz}$ | $35-40$ |
| Rottenberg [28] | 18 | 7.98 | 459 | $0.06 @ 1-15 \mathrm{GHz}$ | $25 @ 15 \mathrm{GHz}$ | - |
| Rizk, J. [29] | 10 | 0.27 | 27 | 0.25 | $20 @ 80-110 \mathrm{GHz}$ | 30 |
| This paper | 26.5 | 4.3 | 162 | $<0.5 @ 30 \mathrm{GHz}$ | $39.2 @ 31.3 \mathrm{GHz}$ | 15.8 |

## 5. Conclusions

A high capacitance ratio and low actuation voltage RF MEMS switch is designed and fabricated for Ka band RF front-ends application in this paper. A metal-insulator-metal (MIM) capacitor is designed on the signal line to improve the capacitance ratio, which will not degrade the switch reliability. The switch has a $1.6 \mu$ m-thick low spring constant
bending folding beam and bilateral drop-down electrodes, which can greatly reduce the spring constant, and the actuation voltage is as low as 15.8 V . From the fitted results, the up-state capacitance is 6.5 fF , down-state capacitance is 4.3 pF , and capacitance ratios is 162. The measured results indicated that the insertion loss was better than -0.5 dB and the isolation was more than -20 dB from 25 to 35 GHz . This paper has several limitations which should be improved in future research: first, the beam was treated as a rigid body, which is inaccurate for large-size thin plates. Non-linearity of beam elastic coefficient at micro scale should be considered; secondly, the residual stress and warpage were cited as a cause for difference in the measured results and simulation, and they should be taken into account in the theoretical model.

Author Contributions: K.D. and K.H. participated in the design of this study, they are both performed the structure optimization, model simulation, data analysis and manuscript preparation. K.D. carried out designing, data analysis, literature search and chart drawing and manuscript preparation. K.H. collected important background information and drafted the manuscript. F.Y., Y.W. and C.L. provide assistance for data acquisition and manuscript review. All authors have read and agreed to the published version of the manuscript.

Funding: The project was supported by Fund of State Key Laboratory of Information Photonics and Optical Communications (Beijing University of Posts and Telecommunications) (No. IPOC2021ZT20), China.

Institutional Review Board Statement: Not applicable.
Informed Consent Statement: Not applicable.
Conflicts of Interest: The authors declare no conflict of interest.

## References

1. Kale, N.S. Tutorial T4: MEMS: Design, Fabrication, and their applications as chemical and biosenors. In Proceedings of the 28th International Conference on VLSI Design, Bangalore, India, 3-7 January 2015.
2. Mansour, R.R. RF MEMS-CMOS Device Integration: An Overview of the Potential for RF Researchers. IEEE Microw. Mag. 2013, 14, 39-56. [CrossRef]
3. Khushbu, M.; Deepak, B. Improved isolation RF MEMS switch with post release ashing. Microsyst. Technol. 2018, 24, 3863-3886. [CrossRef]
4. Cohn, M.B.; Saechao, K.; Whitlock, M.; Brenman, D.; Tang, W.T.; Proie, R.M. RF MEMS switches for Wide I/O data bus applications. In Proceedings of the 2013 IEEE International Test Conference (ITC), Anaheim, CA, USA, 6-13 September 2013.
5. Ko, C.H.; Ho, K.M.; Rebeiz, G.M. An Electronically-Scanned 1.8-2.1 GHz Base-Station Antenna Using Packaged High-Reliability RF MEMS Phase Shifters. IEEE Trans. Microw. Theory Tech. 2013, 61, 979-985. [CrossRef]
6. Bozler, C.; Drangmeister, R. MEMs microswitch arrays for reconfigurable distributed microwave components. In Proceedings of the IEEE Antennas and Propagation Society International Symposium, Salt Lake City, UT, USA, 1-21 July 2000; pp. 587-591.
7. Fouladi, S.; Mansour, R.R. Capacitive RF MEMS Switches Fabricated in Standard 0.35-CMOS Technology. IEEE Trans. Microw. Theory Tech. 2010, 58, 478-486. [CrossRef]
8. Al-Dahleh, R.; Mansour, R.R. High-Capacitance-Ratio Warped-Beam Capacitive MEMS Switch Designs. J. Microelectromech. Syst. 2010, 19, 538-547. [CrossRef]
9. Angira, M.; Sundaram, G.M.; Rangra, K.; Bansal, D.; Kaur, M. On the investigation of an interdigitated, high capcacitance ratio shunt RF-MEMS switch for X-band applications. Proc. NSTI Nanotechnol. 2013, 2, 189-192.
10. Park, J.Y.; Kim, G.H.; Chung, K.W.; Bu, J.U. Fully intergrated micromachined capacitve switches for RF applications. In Proceedings of the 2000 IEEE MTT-S International Microwave Symposium Digest (Cat. No. 00CH37017), Boston, MA, USA, 11-16 June 2000; pp. 283-286.
11. Park, J.Y.; Kim, G.H.; Chung, K.W.; Bu, J.U. Monolithically integrated micromachined RF-MEMS capcacitive switches. Sens. Actuators 2001, 89, 88-94. [CrossRef]
12. Persano, A.; Quaranta, F.; Cola, A.; Taurino, A.; De Angelis, G.; Marcelli, R.; Siciliano, P. Ta ${ }_{2} \mathrm{O}_{5}$ thin films for capacitive RF-MEMS switched. J. Sens. 2010, 2010, 487061. [CrossRef]
13. Ya, M.L.; Nordin, A.N.; Soin, N. Design and analysis of a low voltage electrostatic actuated RF CMOS-MEMS switch. In Proceedings of the RSM 2013 IEEE Regional Symposium on Micro and Nanoelectronics, Daerah Langkawi, Malaysia, 25-27 September 2013; pp. 41-44.
14. Yongqing, X.; Ying, T.; Binzhen, Z.; Junping, D.; Li, Y. A novel RF MEMS switch on frequency reconfigurable antenna application. Microsyst. Technol. 2018, 24, 3833-3841.
15. Li-Ya, M.; Anis, N.N.; Norhayati, S. Design, optimization and simulation of a low-voltage shunt capactive RF-MEMS switch. Microsyst. Technol. 2016, 22, 537-549.
16. Deng, K.; Yang, F.; Deng, Z.; Wang, X.; Han, K. Analysis and design of a novel high capacitance ratio and low actuation voltage RF MEMS switch. Microsyst. Technol. 2021, 27, 1-11. [CrossRef]
17. Rebeiz, G.M. RF MEMS: Theory, Design, and Technology; Wiley: New York, NY, USA, 2004.
18. Entesari, K.; Rebeiz, G.M. A differential 4-bit 6.5-10-GHz RF MEMS tunable filter. IEEE Trans. Microw. Theory Tech. 2005, 53, 1103-1110. [CrossRef]
19. Pozar, D.M. Microwave Engineering; Wiley: New York, NY, USA, 2009.
20. Persano, A.; Cola, A.; De Angelis, G.; Taurino, A.; Siciliano, P.; Quaranta, F. Capacitive RF MEMS Switches with Tantalum-Based Materials. J. Microelectromech. Syst. 2011, 20, 365-370. [CrossRef]
21. Badia, M.F.; Buitrago, E.; Ionescu, A.M. RF MEMS Shunt Capacitive Switches Using AlN Compared to $\mathrm{Si}_{3} \mathrm{~N}_{4}$ Dielectric. J. Microelectromech. Syst. 2012, 21, 1129-1240. [CrossRef]
22. Fall, M.; Fouladi, S.; Domingue, F.; Dieppedale, C.; Reig, B.; Mansour, R.R. High capacitance ratio RF MEMS dielectric-less switched capacitor. In Proceedings of the 2013 European Microwave Conference, Nuremberg, Germany, 6-8 October 2013; pp. 1327-1330.
23. Bansal, D.; Kumar, A.; Sharma, A.; Kumar, P.; Rangra, K.J. Design of novel compact anti-stiction and low insertion loss RF MEMS switch. Microsyst. Technol. 2014, 20, 337-340. [CrossRef]
24. Han, K.; Guo, X.; Smith, S.; Deng, Z.; Li, W. Novel High-Capacitance-Ratio MEMS Swich: Design, Analysis and Performance Verification. Micromachines 2018, 9, 390. [CrossRef]
25. Liu, Y.; Taylor, T.R.; Speck, J.S.; York, R.A. High-isolation BST-MEMS switches. In Proceedings of the 2002 IEEE MTT-S International Microwave Symposium Digest (Cat. No. 02CH37278), Seattle, WA, USA, 2-7 June 2002.
26. Yu, A.B.; Liu, A.Q.; Zhang, Q.X.; Alphones, A.; Zhu, L.; Shacklock, A.P. Improvement of isolation for MEMS capacitive switch via membrane planarization. Sens. Actuators A Phys. 2005, 119, 206-213. [CrossRef]
27. Ziaei, A.; Dean, T.; Mancuso, Y. Lifetime characterization of capacitive RF MEMS switches. In Proceedings of the 2001 IEEE MTT-S International Microwave Sympsoium Digest (Cat. No. 01CH37157), Phoenix, AZ, USA, 20-24 May 2001.
28. Rottenberg, X.; Jansen, H.; Fiorini, P.; De Raedt, W.; Tilmans, H.A.C. Novel RF-MEMS capacitive switching structure. In Proceedings of the 2002 32nd European Microwave Conference, Milan, Italy, $23-26$ September 2002.
29. Rizk, J.; Tan, G.L.; Muldavin, J.B.; Rebeiz, G.M. High-isolation W-band MEMS switches. IEEE Microw. Wirel. Compon. Lett. 2001, 11, 10-12. [CrossRef]
