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Magnetic Tunnel Junction as an On-Chip Temperature Sensor

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Temperature sensors are becoming an increasingly important component in System-on-Chip (SoC) designs with increasing transistor scaling, power density and associated heating effects. This work explores a compact nanoelectronic temperature sensor based on a Magnetic Tunnel Junction (MTJ) structure. The MTJ switches probabilistically depending on the operating temperature in the presence of thermal noise. Performance evaluation of the proposed MTJ temperature sensor, based on experimentally measured device parameters, reveals that the sensor is able to achieve a conversion rate of 2.5K samples/s with energy consumption of 8.8 nJ per conversion (1–2 orders of magnitude lower than state-of-the-art CMOS sensors) for a linear sensing regime of 200–400 K.

Due to continued device scaling and consequent addition of more components on-chip, which in-turn results in enhanced heat generation, chip temperature monitoring has become a critical issue for ensuring reliable operation. With advanced technology nodes, increased throughput is achieved at the expense of more heat generation. Hence, designing on-chip low-power, low-cost temperature sensors is becoming a crucial requirement^{1–4}. The typical performance metrics for on-chip temperature sensors are the conversion rate and energy consumption per inference. The conversion rate is defined as the number of inference samples that can be produced by the sensor per unit sec which is the inverse of the time required by the sensor to make an inference. The energy consumption per inference is defined as the product of the power consumption of the sensor and the inverse of the conversion rate.

While most of the recent work in the domain of on-chip temperature sensors have been primarily based on CMOS sensors^{1–4}, it is interesting to note that post-CMOS technologies like spintronic devices demonstrate temperature-dependent probabilistic switching due to thermal noise. Although, traditionally the stochastic switching behavior of spin-based devices have been primarily viewed as a disadvantage for on-chip memory applications, recently unconventional computing paradigms like neuromorphic computing^{5–7}, Ising computing^{8,9} and Bayesian inference networks based on stochastic nanomagnets have been proposed that leverage the underlying stochastic device physics. The probabilistic switching of the spintronic device is a function of the input programming current and the operating temperature (assuming a fixed duration of the programming current). However, all these applications abstract the probabilistic switching characteristics of the spintronic device as a function of input current as the external stimulus, at a fixed temperature. This work attempts to explore the stochastic magnet dynamics as a function of temperature and provides an estimation of its performance metrics as an on-chip temperature sensor in comparison to state-of-the-art CMOS based sensors. The potential advantages of such nanomagnetic temperature sensors are compactness, higher conversion rate and lower energy consumption per inference.

MTJ as Temperature-Biased Random Number Generator

An MTJ is a magnetic stack where two ferromagnetic layers are separated by a spacer layer, which is typically a tunneling oxide like MgO¹⁰. The device exhibits two stable resistive states depending on whether the magnetization of the two ferromagnetic layers are in the same (parallel: P orientation) or opposite (anti-parallel: AP orientation) directions. The resistance of the device is higher in the AP state than in the P state. Figure 1(a) shows an MTJ stack. Note that one of the ferromagnetic layers is denoted as the “pinned” layer (PL) since its magnetization direction is pinned to a particular direction (usually by coupling to an anti-ferromagnetic layer) while the magnetization of the “free” layer (FL) can be manipulated. The MTJ state can be changed from the P to the AP state by passing current through the MTJ from the “pinned” layer to the “free” layer and vice versa due to spin-transfer torque effect¹¹. Recent experiments on ferromagnet-heavy metal (FM-HM) bilayers have revealed an alternative energy-efficient mechanism of magnetization reversal due to spin-orbit torque^{12–15}. As shown in Fig. 1(a), flow of charge current in the x -direction through the heavy-metal layer results in y -axis polarized spins to be injected on

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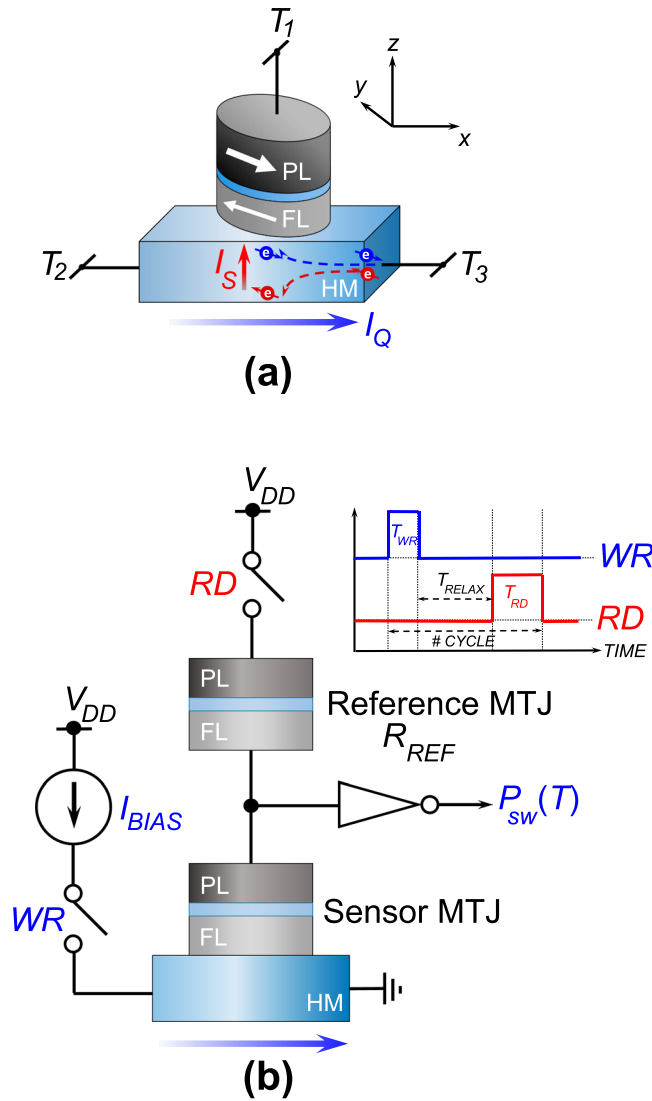


Figure 1. (a) The Magnetic Tunnel Junction (MTJ): “free” layer, FL, separated from “pinned” layer, PL, by a tunneling oxide) based temperature sensor is a three-terminal device structure where a charge current, I_Q , flowing between terminals T_2 and T_3 of a heavy metal (HM) underlayer results in the injection of an in-plane polarized spin current, I_S , on the FL lying on top. “Read” current flowing between terminals T_1 and T_3 is used to “read” the device state, (b) The Sensor MTJ is interfaced with a Reference MTJ (R_{REF}) to form a voltage divider circuit (driven by supply voltage V_{DD}) that drives an inverter at the output to determine the switching probability (P_{sw}) at an operating temperature T . WR and RD are control signals that activate the “write” and “read” current paths of the MTJ respectively. During the “write” phase (WR activated), a bias current (I_{BIAS}) probabilistically switches the magnet depending on the temperature. After a subsequent “relaxation” phase, T_{RELAX} , the “read” phase (RD activated) is used to determine the final state of the MTJ due to the corresponding “write” phase.

the ferromagnet lying on top of the HM layer¹⁶. This input in-plane polarized spin current in the z direction can be utilized to switch the ferromagnetic layer of an MTJ with in-plane magnetic anisotropy. The energy-efficiency of spin-orbit torque driven magnetization switching stems from the fact that input electrons scatter repeatedly at the magnet-heavy metal interface, thereby transferring multiple units of spin-angular momentum to the magnet lying on top. The input spin current density (J_S) is related to the charge current density (J_Q) flowing through the HM underlayer by the relationship, $J_S = \theta_{SH} J_Q \Rightarrow I_S = \theta_{SH} \left(\frac{A_{MTJ}}{A_{HM}} \right) I_Q$, where I_S and I_Q are the input spin current and charge current magnitudes, θ_{SH} is the spin-Hall angle¹⁶ and, A_{MTJ} and A_{HM} are the MTJ and HM cross-sectional areas, respectively. In this work, we propose spin-orbit torque driven MTJ switching in magnet-heavy metal heterostructures as the underlying physical phenomena for realizing an on-chip temperature sensor. The device structure is depicted in Fig. 1(a), where “write” current flowing through the heavy metal between terminals T_2 and T_3 programs the device state. Subsequently, the MTJ state is “read” between terminals T_1 and T_3 .

Parameters	Value
Free Layer Area	$\frac{\pi}{4} \times 100 \times 40 \text{nm}^2$
Free Layer Thickness	1.2nm
Heavy-Metal Thickness	2nm
Saturation Magnetization, M_S	1000 KA/m
Spin-Hall Angle, θ_{SH}	0.3 ¹³
Gilbert's Damping Factor, α	0.0122 ¹³
Energy Barrier, E_B	20 $k_B T$ at $T = 300K$
MgO Thickness	2nm
Resistivity of HM	200 $\mu\Omega.cm$
"Write" Phase Duration, T_{WR}	0.5ns
"Relaxation" Duration, T_{RELAX}	2ns
"Read" Phase Duration, T_{RD}	1ns
Design Temperature	200–400K

Table 1. MTJ Device Parameters.

At non-zero temperatures, the MTJ switching phenomena is stochastic and the degree of stochasticity is governed by the operating temperature. The probabilistic switching characteristics of the MTJ can be analyzed by Landau-Lifshitz-Gilbert (LLG) equation with additional term to account for spin-orbit torque generated by the HM underlayer,

$$\frac{d\hat{m}}{dt} = -\gamma(\hat{m} \times \mathbf{H}_{eff}) + \alpha(\hat{m} \times \frac{d\hat{m}}{dt}) + \frac{1}{qN_s}(\hat{m} \times \mathbf{I}_s \times \hat{m}) \quad (1)$$

where, \hat{m} is the unit vector of FL magnetization, $\gamma = \frac{2\mu_B\mu_0}{\hbar}$ is the gyromagnetic ratio for electron, α is Gilbert's damping ratio, \mathbf{H}_{eff} is the effective magnetic field including the shape anisotropy field for elliptic disks, $N_s = \frac{M_S V}{\mu_B}$ is the number of spins in free layer of volume V (M_S is saturation magnetization and μ_B is Bohr magneton), and \mathbf{I}_s is the spin current generated by the HM underlayer. Thermal noise is included by an additional thermal field¹⁷, $\mathbf{H}_{thermal} = \sqrt{\frac{\alpha}{1+\alpha^2} \frac{2k_B T}{\gamma\mu_0 M_S V \delta_t}} G_{0,1}$, where $G_{0,1}$ is a Gaussian distribution with zero mean and unit standard deviation, k_B is Boltzmann constant, T is the temperature and δ_t is the simulation time-step. The first term represents the precession torque, the second term represents the damping-like torque while the third term denotes the spin-torque term. Interested readers are referred to Ref.¹⁸ for a detailed derivation of the thermal noise term.

The operation of the device as a temperature-biased random number generator has been explained in Fig. 1(b). A particular temperature inference takes place over a number of "write"- "read"- "reset" cycles. The timing waveform for a particular cycle has been shown in the figure. During the "write" cycle, the MTJ is driven by a current source which passes an input charge current through the heavy metal underlayer. Depending on the operating temperature, the MTJ switches with a given probability. Consecutively, during the "read" phase, the MTJ state is determined using the resistive divider circuit shown in Fig. 1(b). The reference resistor, R_{REF} , is an MTJ whose state is fixed in the AP state. The read current is maintained to sufficiently low values such that the MTJ states are not disturbed. Note that the "write" and "read" phases are separated by a "relaxation" period, T_{RELAX} , in order to stabilize the magnetization directions to either of the two stable states after the "write" phase. The magnet is "reset" to the initial AP state for the next cycle in case a switching event takes place by passing a large enough magnitude of current through the heavy metal in the opposite direction to ensure approximately deterministic switching. The switching probability is determined from multiple such measurement cycles and the operating temperature is determined from the measured switching probability.

The device parameters have been mentioned in Table 1. The parameters are based on experimental measurements reported in Ref.¹³. Figure 2 depicts two typical independent temporal profiles of the stochastic MTJ magnetization dynamics when subjected to an input current stimulus of magnitude 80 μA and duration 0.5 ns. While the MTJ switches in the first instance, it stabilizes to the initial state during the "relaxation" period in the other instance.

Sensor Performance Metrics

Figure 3(a) represents the switching probability characteristics of the MTJ (as a function of "write" current through the HM) with varying temperature. The dispersion in switching probability characteristics between 200 K and 400 K is maximized at the central region of the switching probability characteristics (Fig. 3(b)). Specifically, we note that for our design pulse width duration of 0.5 ns, the optimal design current is $\sim 70 \mu A$ and the probability dispersion (absolute difference in the MTJ switching probabilities at 200 K and 400 K) is $\sim 24\%$.

Figure 4 denotes the MTJ switching probability at the optimal bias current of 70 μA as a function of temperature. Although the switching characteristic becomes non-linear and tends to saturate at very high temperatures,

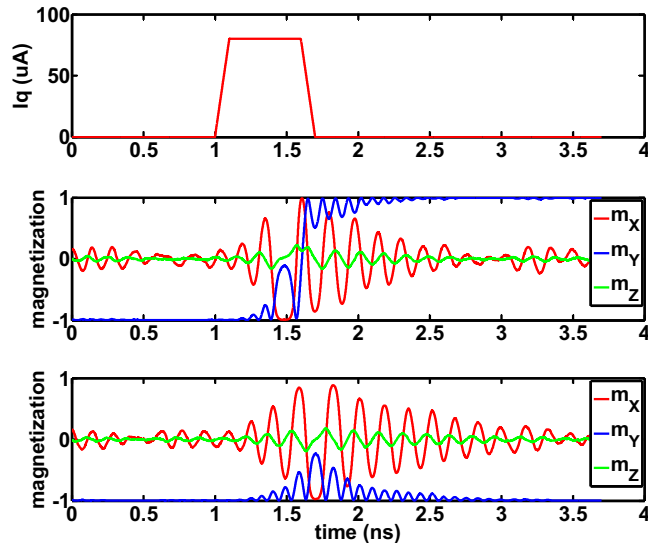


Figure 2. An input current magnitude of $80 \mu\text{A}$ and duration 0.5 ns is passed through the HM layer of the MTJ. Two independent stochastic LLG simulations of the MTJ are shown. The MTJ structure is an elliptic disk of volume $\frac{\pi}{4} \times 100 \times 40 \times 1.2 \text{ nm}^3$ with saturation magnetization of $M_s = 1000 \text{ KA/m}$ and damping factor, $\alpha = 0.0122$. While the MTJ switches in one instance, switching does not occur in the other instance. m_x, m_y and m_z are the X, Y and Z components of magnetization respectively where m_y is the magnetization component being switched. Please refer to Fig. 1 for axes directions.

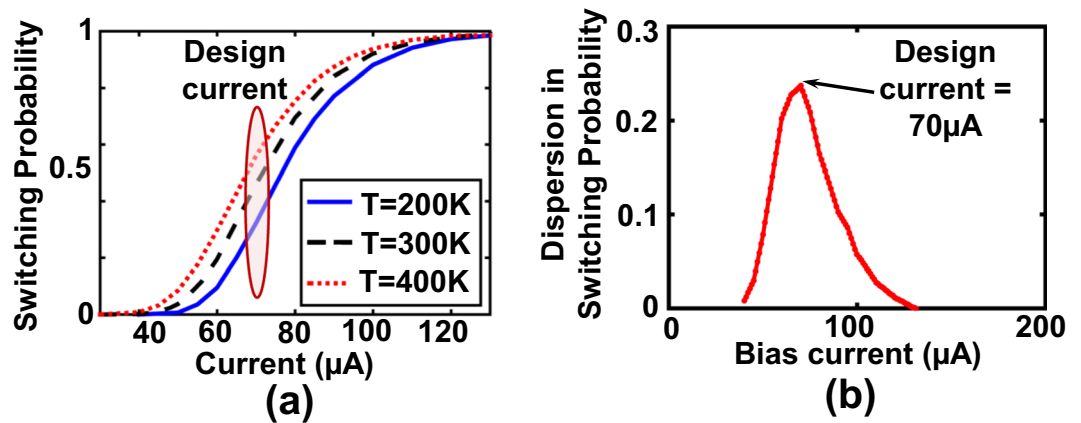


Figure 3. (a) MTJ switching probability characteristics with varying temperature in the range $200\text{--}400\text{ K}$. (b) The dispersion in switching probability between 200 K and 400 K is maximized for a design bias current $70 \mu\text{A}$ (central region of the switching probability characteristics).

the characteristic is approximately linear in the range of $200\text{ K--}400\text{ K}$. The resolution of the sensor linearity is $\sim 0.37\%/1^\circ\text{C}$.

A single switching event of the MTJ can be considered to be a stochastic process with the probability of switching being determined by the temperature. Consequently, the precision of temperature sensing is expected to increase as the number of switching events (“write”-“read”-“reset” cycles) for the temperature inference process is increased. Figure 5 shows that the average sensing error in the range $200\text{ K--}400\text{ K}$ is reduced to $\sim 1^\circ\text{C}$ as the number of samples is increased to 100,000. Considering each cycle to be of duration 4 ns (0.5 ns for “write” phase, 2 ns for “relaxation” phase, 1 ns for “read” phase and 0.5 ns for “reset” phase), the resultant time required for one inference is $4 \times 10^{-4} \text{ s}$ (with an error tolerance of $\sim 1^\circ\text{C}$). The corresponding conversion rate is 2500 samples/s.

The energy consumption of the MTJ based sensor can be estimated by considering the energy consumed during the “write”, “read” and “reset” phases of operation in one cycle. Considering the bias current of $70 \mu\text{A}$ is provided by a 1 V supply, the total “write” energy consumption is estimated to be 35 fJ (VIT_{WR} energy consumption, where $V = 1\text{ V}$, $I = 70 \mu\text{A}$ and $T_{RESET} = 0.5 \text{ ns}$). Assuming a design temperature sensing range of $200\text{ K--}400\text{ K}$, the device exhibits a switching probability of $P_{RESET} = 46\%$ at the mean temperature of 300 K . Since, the MTJ needs to be reset for every switching event by passing a $140 \mu\text{A}$ charge current in the opposite direction through the HM layer (to ensure deterministic switching: see Fig. 3(a)), the “reset” energy consumption is estimated to be $\sim 32 \text{ fJ}$

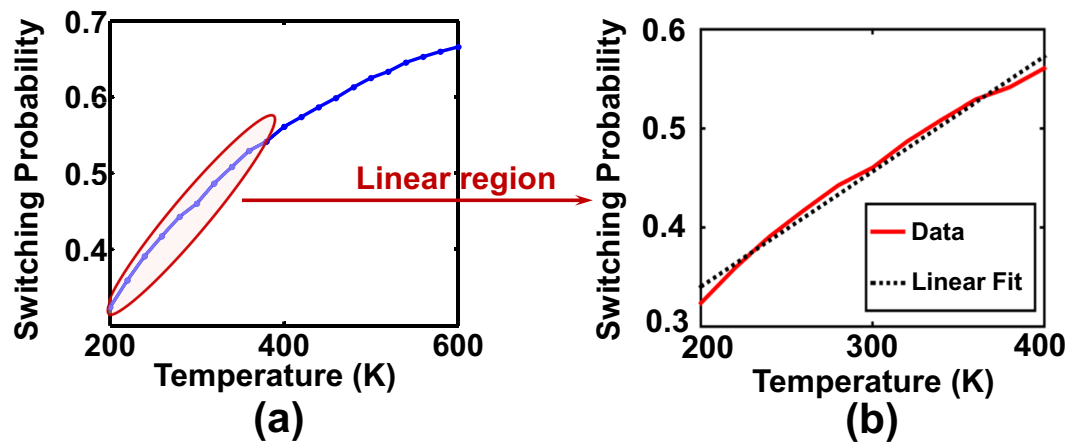


Figure 4. The switching probability of the MTJ subjected to a bias current of magnitude $70\mu A$ and duration $0.5ns$ as a function of temperature. Although the characteristics increase non-linearly, it is approximately linear in the design temperature range of 200–400K.

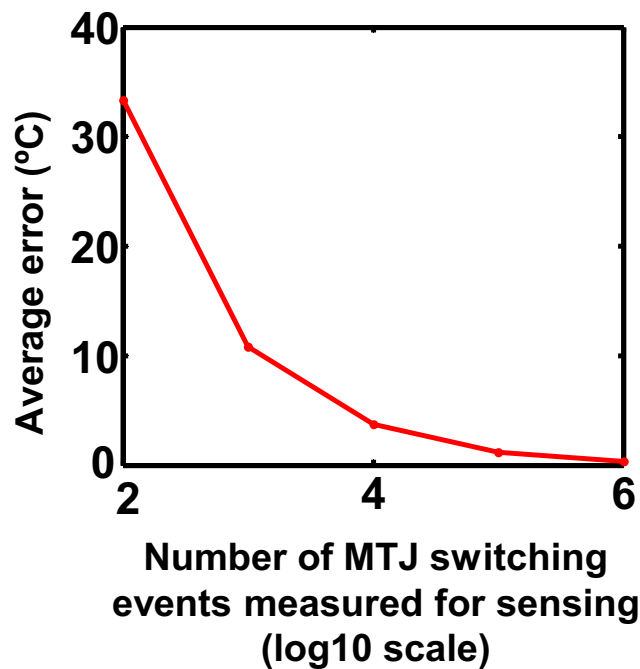


Figure 5. Inaccuracy of the MTJ based temperature sensor as a function of the number of switching events (“write”-“read”-“reset” cycles) used for inferring the switching probability and operating temperature. The average error reduces to $\sim 1^\circ C$ as the number of samples is increased to 100,000.

($P_{RESET} V_{IT} T_{RESET}$ energy consumption where, $V = 1V$, $I = 140\mu A$ and $T_{RESET} = 0.5ns$). The “read” energy consumption was estimated by SPICE simulations of the MTJ based voltage divider driving an inverter stage (as shown in Fig. 1(b)). Non-Equilibrium Green’s Function (NEGF) based transport simulation framework was used to model the MTJ resistance¹⁹. The total “read” energy consumption was estimated to be $\sim 21fJ$ (including the energy consumption of the latch being driven by the inverter stage). Considering the total number of cycles per inference to be 100,000, the total energy consumption of the MTJ based temperature sensor per conversion is given by the product of the resultant energy consumption per cycle and the number of cycles required per inference, and is equivalent to $\sim 8.8nJ$. Comparison of the MTJ based temperature sensor in terms of conversion rate and energy/conversion with other recent proposals of CMOS based temperature sensors are summarized in Table 2.

Scaling to the Super-Paramagnetic Regime

The discussion so far has been based on magnet dimensions exhibiting a barrier height of $\sim 20k_B T$ (at the nominal temperature $T = 300K$). However, as the magnet dimensions are aggressively scaled down to the

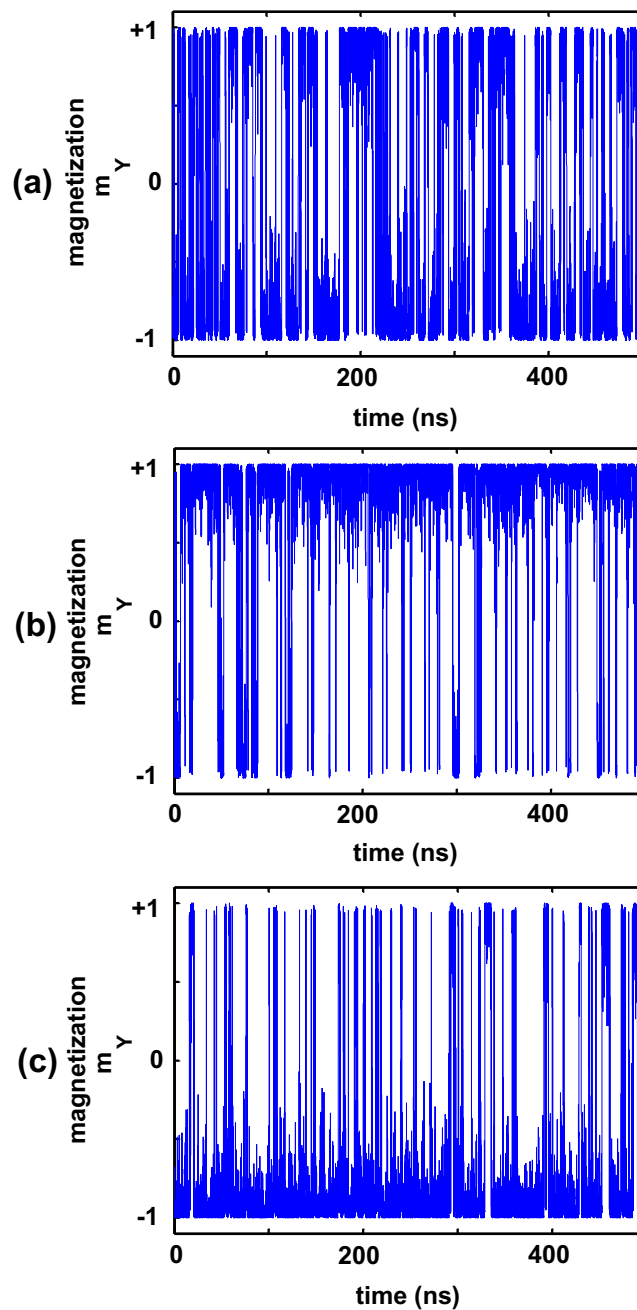


Figure 6. Switching characteristics of a magnet with $1k_B T$ barrier height: (a) When the current flowing through the HM layer is zero, the magnet is equally likely to be in the $m_y = +1$ or $m_y = -1$ magnetization state, (b) When $1.5\mu A$ is flowing through the HM layer, the magnet is more likely to be in the $m_y = +1$ state, (c) When $-1.5\mu A$ is flowing through the HM layer, the MTJ is more likely to be in the $m_y = -1$ state.

super-paramagnetic regime ($1k_B T$ barrier height), the magnet exhibits random telegraphic switching between the two extreme states. As shown in Fig. 6(a), the average dwell time in each state is $\sim 50\%$, and the average in-plane magnetization over a duration of $500ns$ is approximately zero. The dwell time in either of the two extreme states can be biased by the magnitude of the input current stimulus (flowing through the underlying HM layer) as well as the operating temperature. Figure 6(b,c) depicts the temporal dynamics of the magnetization under the influence of an external current stimulus of magnitude $\pm 1.5\mu A$ at $T = 300K$. The average in-plane magnetization is clearly impacted by magnitude of the external current stimulus. Figure 7(a) represents the average in-plane magnetization as a function of the “write” current flowing through the HM layer at the nominal temperature $T = 300K$. For a design bias current of $1\mu A$, the MTJ exhibits linear variation of average magnetization profile with sensing temperature (Fig. 7(b)).

Due to the low barrier height, the magnet essentially operates as a volatile device. Consequently, the circuit peripherals have to be operated in an asynchronous fashion (in contrast to the synchronous “write”-“read”-“reset”

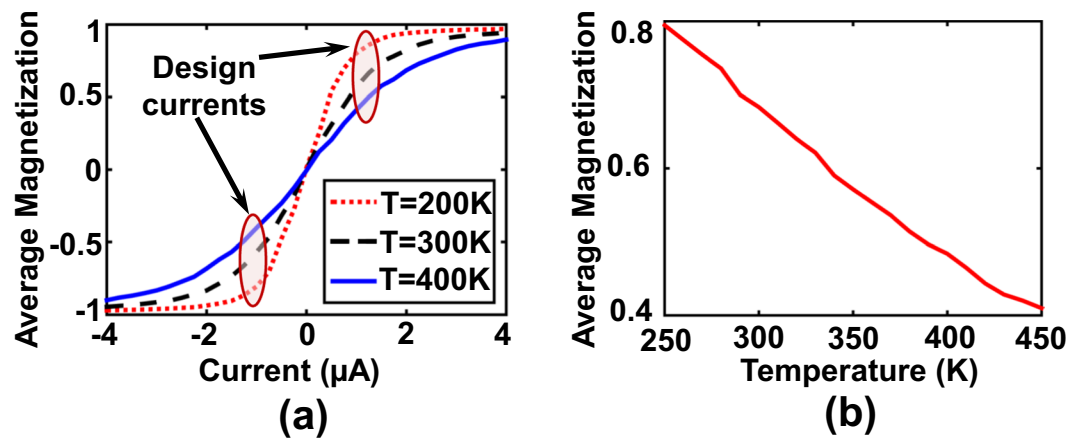


Figure 7. (a) Variation of the average in-plane magnetization with magnitude of the “write” current for $T=200\text{K}–400\text{K}$, (b) For a design bias current of $1\mu\text{A}$, the average magnetization varies approximately linearly with the operating temperature. The time-window used for the averaging operation is $100,000\text{ns}$.

Sensor Type	Temperature Range (°C)	Inaccuracy (°C)	Conversion Rate (samples/s)	Energy/Conversion (nJ)	Technology
CMOS ¹	0–100	−0.8– + 1	10	150	$0.35\mu\text{m}$
CMOS ²	0–100	−0.851 ~ 0.524	1.6K	98.13	$0.18\mu\text{m}$
CMOS ³	−40–85	±1	1K	66.5	$0.18\mu\text{m}$
CMOS ⁴	20–50	±0.1	10	1600	$0.18\mu\text{m}$
MTJ (This Work)	−73–127	±1	2.5K	8.8	—

Table 2. Comparison With Other Proposed Temperature Sensors.

mode of operation discussed for high barrier height magnets). The “write” and “read” current paths have to be activated simultaneously and the “read” circuit has to be optimized to ensure that the “read” current has minimal impact on the switching of the magnet. Circuit-level simulations indicate that the “read” current can be maintained to values below 100nA , thereby having negligible influence on the switching probability characteristics of the magnet.

The potential benefits of such super-paramagnetic sensors lies in the conversion rate and energy consumption per inference. Since telegraphic switching occurs in the $\sim\text{ps}$ time scale, the time window per inference can be greatly reduced. Further, the “write” bias current magnitude is reduced by almost an order of magnitude, thereby reducing the “write” power consumption. Additionally, no “reset” operation is required (due to telegraphic magnet switching), leading to reduction in both the power consumption and the delay involved in the “reset” operation.

Conclusions

In conclusion, we proposed a compact nanoelectronic temperature sensor that is able to provide a higher throughput and lower energy consumption in comparison to state-of-the-art CMOS temperature sensors. A key point that enables the usage of stochastic switching behavior of MTJs for temperature sensing applications (in comparison to stochastic switching behavior of other resistive memory technologies) is that the causal element for the device stochasticity is thermal noise. Instead of considering the underlying device stochasticity to be disadvantageous, this work can potentially pave the way for MTJ-enabled on-chip temperature sensors that exploit the probabilistic switching characteristics of nanomagnets at non-zero temperatures.

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Author Contributions

A. Sengupta, B. Jung and K. Roy conceived the research study. A. Sengupta wrote the paper, developed the simulation framework and performed the simulations. C.M. Liyanagedera performed the simulations for MTJs in the superparamagnetic regime. All authors helped with the writing of the paper, developing the concepts and discussing the results.

Additional Information

Competing Interests: The authors declare that they have no competing interests.

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