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Engineering integrated photonics OPENfor heralded quantum gates

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Scaling up linear-optics quantum computing will require multi-photon gates which are compact, phasestable, exhibit excellent quantum interference, and have success heralded by the detection of ancillary photons. We investigate the design, fabrication and characterisation of the optimal known gate scheme which meets these requirements: the Knill controlled-*Z* **gate, implemented in integrated laser-written waveguide arrays. We show device performance to be less sensitive to phase variations in the circuit than to small deviations in the coupler reflectivity, which are expected given the tolerance values of the fabrication method. The mode fidelity is also shown to be less sensitive to reflectivity and phase errors than the process fidelity. Our best device achieves a fidelity of 0.931±0.001 with the ideal 4×4 unitary circuit and a process fidelity of 0.680±0.005 with the ideal computational-basis process.**

Recent advances in on-chip integration of efficient photon sources¹⁻⁴ and detectors⁵⁻⁷ hold promise for significant progress in the linear optics architecture for quantum information processing and simulation. This architecture relies on the insight that entangling quantum gates can be realised probabilistically by interacting photonic qubits using effective optical nonlinearities induced by measurement^{[8](#page-6-2)}. However, efficient scaling in this architecture necessitates entangling gates which are both logically and physically scalable. Logical scalability requires successful operation to be heralded nondestructively, typically by the detection of additional 'ancilla' photons. Physical scalability, meanwhile, requires a compact and phase-stable architecture. Integrated, non-heralded entangling gates have been demonstrated^{[9,](#page-6-3)10} as have heralded gates in bulk optics^{[11](#page-6-5),12}. However, gates which are scalable in both the logical and physical senses have not been implemented to date due to their geometric complexity as well as the need for low circuit losses.

The simplest heralded entangling two-qubit photonic gate design, with the highest known success probability, was found by Knill¹³ and implements a controlled-*Z* operation with probability $\frac{2}{27}$. This heralded cz design, henceforth called the *h*cz, relies on pairwise non-classical interference of four indistinguishable photons in a circuit with four particular beamsplitters (BSs), as shown in [Fig. 1\(a\),](#page-1-0) as well as a stable phase shift of precisely *π* between the first and second BS pairs.

Integrated arrays of coupled waveguides could enable compact, phase-stable circuits with the requisite splittings and phase for Knill's design. However, integrated *h*cz circuits require a physical swapping of neighboring modes; such crossovers are difficult to achieve lithographically. Laser-written waveguides have recently been used to demonstrate a wide variety of quantum photonics circuitry^{[14](#page-6-8),[15](#page-6-9)}. The femtosecond-laser direct-write (FLDW) technique in particular allows 3D waveguide arrays, simplifying waveguide crossovers, along with demonstrated high mode indistinguishability¹⁴, and has recently been employed for multiport and arbitrary-phase directional couplers^{16–19}, all-optical routers²⁰, circuits for small-scale quantum simulations^{21–23}, quantum walks²⁴, and non-heralded quantum gates¹⁰.

Here we investigate implementation of the *h*cz gate both theoretically and experimentally using the direct-write technique, with a particular focus on its action in the presence of deviations from optimal phase

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Figure 1. (**a**) The circuit for a *h*cz gate showing paths for ancillary photons *A* and *B* as well as the computational qubits; the control (target) photon is encoded across spatial paths $C_0(T_0)$ representing $|0\rangle$ and *C* (*T*) representing |1〉. Modes are labeled C (Control), T (Target), A (Ancilla) and B (Ancilla). The |0〉-modes do not interact in the gate; the four remaining modes undergo four beamsplitting operations with reflectivities $R(\theta_n) = \cos^2(\theta_n)$ as described in Eq. (1). The light-coloured side indicates the surface yielding a relative π phase change upon reflection. (**b**) The four interacting circuit modes modelled as a waveguide array, showing the crossover and optimal reflectivities for the BSs implemented using evanescent coupling. The waveguides are separated by 127*μ*m at the device end facets; fan-in and fan-out regions are not shown. The input mode labeling is reversed compared to (**a**) due to the reflectance of the couplers being defined as the proportion of input light which couples from one waveguide to the other.

and reflectivity parameters. We derive the variation in gate performance as quantified by two metrics—the optical-circuit mode-fidelity and the computational-basis process-fidelity—with respect to such deviations. We further detail the fabrication of 12 prototype circuits including a novel and simple method for achieving the requisite internal phase, and their full characterisation using coherent techniques as well as quantum interference, which confirms their excellent mode indistinguishability and suitability for the single-photon regime.

Materials and Methods

The optical circuit for the *h*cz gate is shown in [Fig. 1\(a\)](#page-1-0). The *control* and *target* qubits are each encoded as single photons across a pair of modes, labelled *C* and C_0 for the control, and *T* and T_0 for the target. C_0 and T_0 , which encode $|0\rangle$ for their respective qubits, interact with neither the $|1\rangle$ -modes nor the ancillas; our fabricated circuits contain only the four interacting modes as shown in [Fig. 1\(b\).](#page-1-0) Modulo local phases on the input and output modes, this circuit implements a heralded cz operation: conditioned on the detection of one photon in each ancilla mode it flips the sign of the |11〉-term of an arbitrary two-qubit input state α_{00} | 00 \rangle + α_{10} | 10 \rangle + α_{01} | 01 \rangle + α_{11} | 11 \rangle .

Circuit Modelling and Design. Demonstrations of entangling linear optics quantum gates relying on *post-selection* of the computational photons go back more than a decade and include both free-space^{[25,](#page-7-1)26} and integrated implementations^{[9](#page-6-3),[10](#page-6-4)}. In such gates, success relies on detecting the photonic qubits in particular output modes, precluding their use in subsequent multi-qubit operations^{[8](#page-6-2)}. In contrast heralded gates—where success is signalled by detection of *ancillary* photons without measuring the output qubits—can be incorporated as modules in complex quantum computations. The effects of fabrication imperfections on post-selected gates have been modelled[27,](#page-7-3)[28.](#page-7-4) However such gates can effectively be simplified to include only a single instance of two-photon interference at one beamsplitter^{[26](#page-7-2)}; by contrast the *h*cz, despite its simplicity relative to other heralded gates, requires four distinct two-photon interference events at four beamsplitters. Here we quantify the effects of device imperfections on such a heralded gate.

We first model imperfect *h*cz device operation as follows. We assume four single-mode waveguides coupled by BSs as in [Fig. 1\(b\)](#page-1-0). The quantum state of the light is described by four bosonic creation operators $a_C^{\dagger}, a_T^{\dagger}, a_A^{\dagger}$, and a_B^{\dagger} which create a photon in the control, target, and two ancillary modes respectively. We employ the symmetric BS convention so that two modes a_1 and a_2 transform as

$$
\text{BS}(\theta): \quad a_1^\dagger \to a_1^\dagger \cos \theta + i a_2^\dagger \sin \theta, \qquad a_2^\dagger \to a_2^\dagger \cos \theta + i a_1^\dagger \sin \theta. \tag{1}
$$

We first consider a photon or coherent state in a superposition of the input modes injected into a circuit of the form in [Fig. 1\(b\)](#page-1-0). Our model neglects intrinsic loss, as in our fabricated circuits such loss is nearly constant across the waveguides and therefore results only in a reduction in the output state amplitude and, thus, the gate success probability. The circuit then maps the input creation operators a^{\dagger} to the outputs b^{\dagger} via the transformation $\overline{b}_k^{\dagger} = \sum_j U_{ik}^{\text{circ}} a_i^{\dagger}$, where U^{circ} is a unitary matrix, and the mode indices *k* and *j* are ordered {*C*, *T*, *A*, *B*}. We allow for arbitrary splitting parameter angles θ_n , $n = \{1, \ldots, 4\}$. The requisite internal phase shift is implemented by an additional phase of π on BS3, equivalent to $\theta_3 \to -\theta_3$. Using Eq. (1) and allowing for additional undesired phase shifts $(a_n^{\dagger} \rightarrow e^{i\phi_n} a_n^{\dagger})$ between the BS pairs, we find that all these unwanted internal phases can be collected into a single net phase shift $\phi_N = \phi_c + \phi_a - \phi_b - \phi_t$. For the total circuit action, modulo external local phases, we thus find:

$$
U^{\rm circ} = \begin{bmatrix} \cos \theta_1 \cos \theta_3 & \cos \theta_2 \sin \theta_3 & \cos \theta_1 \sin \theta_3 & \sin \theta_2 \sin \theta_3 \\ \cos \theta_1 \sin \theta_3 & -\cos \theta_2 \cos \theta_3 & \sin \theta_1 \sin \theta_3 & -\cos \theta_3 \sin \theta_2 \\ \cos \theta_4 \sin \theta_1 & e^{i\phi_N} \sin \theta_2 \sin \theta_4 & -\cos \theta_1 \cos \theta_4 & -e^{i\phi_N} \cos \theta_2 \sin \theta_4 \\ \sin \theta_1 \sin \theta_4 & -e^{i\phi_N} \cos \theta_4 \sin \theta_2 & -\cos \theta_1 \sin \theta_4 & e^{i\phi_N} \cos \theta_2 \cos \theta_4 \end{bmatrix} . \tag{2}
$$

Up to external phases and in the absence of net phase *φN*, the ideal matrix *Uh*cz given by Knill[13](#page-6-7) is achieved by the target angles of $\theta_1 = \theta_2 = \theta_3 = \arccos{\sqrt{1/3}}$, and $\theta_4 = \arccos{\sqrt{\frac{1}{2}}} + \frac{1}{2}$ 2 $rac{1}{\sqrt{6}}$.

We employ two metrics to assess the design accuracy of a physical circuit for a *h*cz gate. The *mode fidelity Fm* directly compares the 4 × 4 circuit mapping matrix *U*^{dici} to the ideal unitary matrix *U^{hcz}*, and is given by the normalized Hilbert-Schmidt inner product²⁹: $F_m = \text{Tr}\{U^{hczt}U^{circ}\}^2/N^2$, where $N = 4$ is the number of modes. This metric most closely captures the differences between the manufactured integrated device and the ideal target device, but only partially captures how the device would function with qubits since it ignores the effect of measurement heralding. For instance if the ancilla modes were swapped prior to detection, F_m would decrease without any operational effect on the function of the heralded gate on the qubits, as it is irrelevant which detector detects which ancilla. The second metric directly assesses the effect of the measurement-induced optical nonlinearity in the space of the qubits, and thus will be independent of such irrelevant changes. A quantum process $\mathcal E$ can be represented abstractly as a quantum state *ρ* via the *Jamiolkowski isomorphism*[30](#page-7-6), and the natural figure of merit for gate quality is then the *process fidelity*: $F_p = Tr \{\rho_{CZ}^{\dagger} \rho_{\varepsilon}\}\$ which simply compares the state representing the implemented process and the state *ρ*cz representing the process for an ideal cz gate^{31,32}. While we would expect the two measures to be roughly similar, particularly for small imperfections in *e.g.* splitting ratios, there is in general no simple relationship between them, as they are differently sensitive to imperfections.

The most direct way to calculate ρ_c is to consider a maximally-entangled state $|\phi_{\text{max}}\rangle$ between the Hilbert space on which the process acts, and another fictitious space of the same dimension. The process acts on one half of the entangled state, and the resulting total state is exactly *ρ*. As our computational input is two qubits, the entangled state is

$$
\phi_{\text{max}} = (|0000\rangle + |0101\rangle + |1010\rangle + |1111\rangle)/2. \tag{3}
$$

After a cz operation on the first two qubits the result is

$$
|\phi_{\text{CZ}}\rangle = (|0000\rangle + |0101\rangle + |1010\rangle - |1111\rangle)/2, \tag{4}
$$

and the corresponding final state representing the process is $CZ = |\phi_{CZ}\rangle \langle \phi_{CZ}|$.

Given that each qubit comprises a photon in two modes, $|\phi_{\text{max}}\rangle$ involves four photons encoded across eight modes, where the fictitious additional control (target) mode has creation operator $a_{C2}^{\dagger}(a_{T2}^{\dagger})$. With the addition of the two ancillary modes, the entangled input state is thus represented using boson creation operators as $(1 + a_T^{\dagger}a_{T2}^{\dagger} + a_C^{\dagger}a_{C2}^{\dagger} + a_C^{\dagger}a_{C2}^{\dagger}a_T^{\dagger}a_T^{\dagger})a_A^{\dagger}a_B^{\dagger}|\mathbf{0}\rangle$ where $|\mathbf{0}\rangle$ is a multimode bosonic vacuum, and creation operators for the non-interacting logical $|0\rangle$ modes are again omitted. The circuit transforms $a_C^{\dagger}, a_T^{\dagger}, a_A^{\dagger}$, and a_B^{\dagger} according to *U*circ, and gate success is heralded by measuring exactly one photon in each ancillary mode. This measurement removes these modes and induces a cz on the remaining modes.

A subtle problem arises when the photonic gate is not perfectly balanced. There is then a non-zero amplitude for the states proportional to $(a_C^{\dagger})^2$ and $(a_T^{\dagger})^2$, which lie outside the qubit space and represent errors. In characterising circuit performance, we account for these errors by calculating the process fidelity against a version of *ρ*cz which is extended to include these two states but with zero support, and thus any weight on these terms will always reduce F_p .

[Figure 2](#page-3-0) shows the variation of the process and mode fidelity due to a deviation in a single BS reflectivity or the net internal phase. Both fidelity metrics are much less sensitive to small deviations in phase than in splitting ratios. [Figure 2](#page-3-0) also shows two fidelity distributions each resulting from 2000 randomly-chosen gate simulations with simultaneous deviations in all five reflectivity and phase parameters. Perhaps unsurprisingly the mode fidelity is far less sensitive to errors overall.

Device Fabrication. The circuits were fabricated using the FLDW technique wherein a tightly focused femtosecond laser generates a localised refractive index contrast in a glass substrate. By translating the glass in (*x, y, z*) with respect to the incident laser, arbitrary 3D regions of net-positive refractive index change can be produced. Our fabrication employed a Ti:Sapphire oscillator (800 nm centre wavelength, <50 fs pulse duration) with 5.1 MHz repetition rate^{33,34}. A telescope was used to overfill the input pupil of a $100 \times$ oil immersion objective which focused the laser into the boro-aluminosilicate sample (Corning Eagle 2000) for writing with 66 nJ pulses at a translation speed of 1200mm/min. The sample was subsequently annealed to obtain a more symmetric and Gaussian refractive index profile³⁵. This significantly improves waveguide throughput efficiency, as shown in (ref. [36](#page-7-12)). This process yields a mode field diameter of 5*μ*m which has excellent overlap with an 800nm single-mode optical fibre, and gives rise to fibre-to-fibre coupling loss of only 1.8dB for straight waveguides of length 40mm. Inputand output-coupling accounts for the majority of this loss, while intrinsic propagation loss is below 0.2 dB/cm^{36} , and importantly intrinsic losses were found to be constant across the four waveguides to within measurement error.

The splitting ratios of the waveguide BSs—or directional couplers—can be adjusted by changing their coupling lengths. Using a symmetric phase convention, the amplitudes in an ideal directional coupler of total length *L* with

uniform coupling constant *C* vary sinusoidally with propagation length *z* as $a_i^{\dagger} \rightarrow a_i^{\dagger} \cos(Cz) + ia_i^{\dagger} \sin(Cz)$, where *j*, $k = \{1, 2\}$ and $0 \le z \le L$. Since the waveguides have nominally identical profiles, the reflectivity takes the simple form $R = \cos^2 \gamma$, where $\gamma = \int_0^L C(z) dz$ and we allow for variation in the coupling strength $C(z)$ along the waveguide³⁷.

While specific, arbitrary phase shifts are difficult to realise precisely without active elements using FLDW^{[19,](#page-6-13)23}, adjustments in coupler length also allowed us to achieve the requisite internal phase shift of *π*. Extending *L* such that γ goes from θ to $(2\pi - \theta)$ changes the action of the splitter to $a_j^{\dagger} \to a_j^{\dagger}$ $cos(-\theta) + ia_k^{\dagger} sin(-\theta) = a_i^{\dagger} cos(\theta) - ia_k^{\dagger} sin(\theta)$. Exploiting this identity, we implemented the requisite phase shift by lengthening BS3 from $\gamma = \arccos \sqrt{1/3}$ to $\gamma = (2\pi - \arccos \sqrt{1/3})$. For ideal couplers the relative phase is limited to $\pm \pi/2$ and the application of this technique on BS3 yields no undesired internal phase ϕ_N , even for slight errors in *L*. In practice $\phi_N \neq 0$ can occur due to slight variations in local waveguide profile resulting from laser power fluctuations in fabrication, as well as from small internal path length variations. All arms of the device are however designed to have the same path length.

An extensive parameter study of directional couplers was completed in order to determine the optimal laser characteristics, writing algorithm, and coupling lengths for achieving the desired reflectivities and internal phase. However, the performance of couplers written according to a particular algorithm will nevertheless vary from sample to sample, depending on the precise substrate and laser characteristics at the time of fabrication. In particular, slight refractive index differences between the two waveguides constitute a significant source of deviations from intended reflectivities. Such differences yield phase mismatch which prevents full power transfer between the waveguides, an effect which becomes more pronounced as coupling length increases. Twelve separate candidate circuits were thus fabricated, both to increase the likelihood of achieving near-optimal phase and reflectivity parameters in one or more circuits, and in order to experimentally investigate the sensitivity of device operation to parameter variations. In our directional coupler parameter scan the waveguide separation was maintained at 7*μ*m while we varied the coupling length. We fixed the length of BS4 (2.5 mm) and tested all combinations of three values for BS1 & BS2 (0.25, 0.50, and 0.75 mm) and four different values of BS3 (5.50, 5.75, 6.00, and 6.25mm). The necessity for testing different parameter combinations results from variability of the writing laser, which in turn ultimately arises from its passive mode locking and the length of the cavity necessary to achieve a balance of sufficiently high pulse energy $(>100 \text{ nJ})$ and repetition rates (5 MHz). This cavity is 30 m long, and any perturbation can have a substantial knock-on effect on the resultant pulse energy at short (sub second) time scales. Small fluctuations in the pulse energy result in fluctuations in the refractive index of the waveguides, to which the directional couplers are extremely sensitive (our laser fluctuates by up to 5% during processing). In

Figure 3. Results from coherent circuit characterisation. Error bars are too small to see and are thus absent. Top: Mode and process fidelity of the measured circuit mappings with the ideal *h*cz circuit unitary, optimised over local external phases. The mean uncertainty in F_m and F_p are 0.002 and 0.006 respectively; these uncertainties were determined via Monte Carlo methods using the measured uncertainties in phases and moduli. Centre: Net undesired internal phase *φN*. The displayed value is the mean of the four values determined from the four occurrences of ϕ_N in comparing *U*^{meas} for each circuit to Eq. 2. For all 12 candidate circuits these differ by a maximum of 0.07. The variation between these four values dominated that between our many phase measurement trials for each circuit, and their standard deviation is thus taken to be our uncertainty; the mean resulting value over all 12 circuits is 0.0015. Bottom: Deviations Δ*θ* from the ideal reflectivity parameters for the four BSs. For all values of Δ*θ* the mean uncertainty—determined from repeated measurement trials—is 0.0026. In all 3 panels, the measured circuits are ordered by decreasing process fidelity.

principle these laser fluctuations could be improved by temperature stabilisation of the cavity, or potentially operation *in vacuo*.

Results and Discussion

Coherent Device Characterisation. We characterised the fabricated candidate circuits using a recent-ly-demonstrated technique^{[38,](#page-7-14)39} which yields $U^{meas}=r^{meas}\exp(i\phi^{meas})$ using only single- and two-mode bright coherent states and output intensity measurements. The moduli r_{jk}^{meas} result from intensity measurement at each output *k* for an input at mode *j*. The phases ϕ_{jk}^{meas} are obtained as follows: a two-mode coherent state is injected into two inputs, and a relative phase between the modes is induced via continuous path-length variation in one mode using motorised translation. The output interference fringes are recorded with fast photodiodes and an oscilloscope, and the phases ϕ_{jk}^{meas} are simply the phase differences between the pairs of resulting periodic output intensity signals {*Ij* (*t*)}. However, due to experimental noise and slight variations in the phase-setting translation velocity, it was more accurate in practice to determine the unknown phases ϕ_{jk}^{meas} by subtracting the discrete Fourier transforms of the output signals. Gate inputs must be identically polarized for optimal interference; we tested the circuits with both horizontal- and vertical-polarized inputs and found the differences to be insignificant, indicating little birefringence in the waveguides. The data presented is for the horizontal polarization.

The resulting 12 measured maps *U^{meas}* are nearly unitary within error: over all 12 circuits the maximum value of $D_{jk} = |U_{jk}^{\text{meas}} U_{jk}^{\text{meas}\dagger} - 1_{jk}|$ was 0.050, with a mean of 0.010, and on average D_{jk} differed from zero by just 1.6 standard deviations, as determined through Monte Carlo analysis using our uncertainties in r_{jk}^{meas} and ϕ_{jk}^{meas} . Those uncertainties were derived directly from the measured variance in output power ratios and relative phase respectively, taken over multiple trials. Comparison of the measured matrices *U*meas to *U*circ in Eq. 2 allows nearly direct determination of the net phase ϕ_N ; notably the values of ϕ_{jk}^{meas} are consistent with U^{circ} to within error. The splitting parameters θ_n , $n \in \{1, ..., 4\}$ can be determined from r^{meas} using numerical optimisation. The results for F_p , F_m , ϕ_N , and θ_n for all 12 measured candidate circuits are shown in [Fig. 3.](#page-4-0) Notably the fidelity values shown were calculated directly from the measured matrices *U*meas, but agree to within error with the values calculated from the

Figure 4. (**a**) Setup for measuring two-photon interference. Degenerate photon pairs at 820nm are created via spontaneous parametric down-conversion (SPDC) in a nonlinear *β*-barium-borate (BBO) crystal pumped by a 410nm frequency-doubled Ti:Sapphire laser. Manual polarisation controllers (POL) enable alignment of SPDC polarisation with the axes of polarisation-maintaining fibers coupled to the test device. We detect photons in coincidence using avalanche photo diodes (APDs). (**b**) Representative observed non-classical interference patterns, showing anti-coalescent and coalescent interference for two different output mode combinations as well as fits to the data with Gaussian and sinc components. The error bars shown are calculated from Poissonian counting statistics. (**c**) Two photon interference results for the best *h*cz circuit. We compare predictions (left) from the coherently-characterised circuit against measured two-photon interference visibilities (centre). The right panel shows visibilities for the ideal circuit *Uh*cz; most of the difference between this panel and the other two is due to the deviation in the best circuit from the ideal reflectivity for BS4 and unwanted net phase *φN*, as shown in [Fig. 3.](#page-4-0) For the residuals after subtracting the measured visibilities from the predictions the mean and standard deviation are −0.002 and 0.061, while after subtracting the measured values from the ideal values they are −0.022 and 0.282 respectively.

measured phase and reflectivity deviations in the manner depicted in [Fig. 2](#page-3-0). The relatively higher variance in θ_3 is due largely to the increased sensitivity of longer couplers to slight index mismatches, as explained in Device Fabrication; BS3 has a coupling region almost six times the length of BS1 and BS2 in order to achieve the required internal phase shift.

For the best measured device, the mode and process fidelities determined were $F_m(U^{\text{meas}}, U^{hcx}) = 0.931 \pm 0.001$ and $F_p = 0.680 \pm 0.005$ respectively; the net internal phase found was $\phi_N = -0.346 \pm 0.013$, and the splitting parameter deviations determined were $\Delta \theta_1 = 0.087 \pm 0.002$, $\Delta \theta_2 = 0.083 \pm 0.003$, $\Delta \theta_3 = -0.065 \pm 0.003$, and ∆ $\theta_4 = 0.337 \pm 0.002$. As an illustration these parameter deviations are also depicted in [Fig. 2](#page-3-0) along with their individual effects on the fidelities. For all 12 devices BS4 was erroneously fabricated with a reflectivity near 60% rather than the ideal value of 90.8%. However we note that if Δ*θ*4 had been approximately the mean of the other splitting deviations achieved, with a value of 0.08, the process fidelity calculated according to those parameter errors would have been F_p = 0.882 and the mode fidelity F_m = 0.984.

Verification Using Two-Photon Interference. Full operation of these circuits as gates using spontaneous parametric down-conversion (SPDC)—the current state-of-the-art in generating multiple single photons requires a six-photon output state where two serve as triggers. This is to avoid heralding false positives due to the probabilistic nature of SPDC. Given the $2/27$ gate success probability and our loss of at least 1.8 dB per waveguide, this would result in a success probability of less than 0.015 per six-photon input. With current six-photon SPDC generation capabilities⁴⁰ we would thus expect a success rate of less than 3 mHz, necessitating prohibitively long integration times and low signal-to-noise for *e.g.* quantum process tomography, thereby limiting conclusions regarding actual gate fidelity.

However, despite requiring four input photons for full operation, the *h*cz circuit relies only on fourth-order interference effects (in field), *i.e.* two-photon quantum interference; any higher-order interference effects between the four input photons can only result in error terms where the control and target qubits along with the two ancillary modes do not output exactly one photon each. In order both to confirm the ability of the circuits to support high-visibility quantum interference and to verify the results of their coherent characterisation, we therefore measured the visibility of two-photon quantum interference in the best-performing circuit for all possible input-output mode combinations. These measured visibilities are compared against both predictions from our classical characterisation and the ideal *h*cz circuit visibilities in [Fig. 4](#page-5-0).

The apparatus for measuring the quantum interference effects is depicted schematically in [Fig. 4\(a\).](#page-5-0) The ratio of two-fold coincidences to those resulting from higher order terms may be calculated according to Broome *et al.*[41](#page-7-17). We estimate the percentage of 4-folds to 2-folds of 0.02% resulting in a maximum 95.6% two-photon interference visibility. We measured two-photon quantum interference visibility for all $\binom{4}{2}^2 = 36$ combinations of two input and two output ports. The interference visibility *V* is calculated as $V = (C_{\text{max}} - C_{\text{min}})/C_{\text{max}}$, where *C* is the rate of coincident photon detection events as a function of the temporal delay between the input photons, and C_{max} and C_{min} are calculated from a fit to the data as shown in [Fig. 4\(b\).](#page-5-0)

The measured visibilities are shown in [Fig. 4\(c\)](#page-5-0), along with those predicted from *U*meas as determined via coherent characterisation, and the visibilities for an ideal *h*cz circuit. The mean of the residuals after subtracting the measured visibilities from the predictions is only −0.002 with a standard deviation of 0.061. Perhaps a better comparison is achieved by numerically calculating the unitary *U*vis which would yield the minimum root-mean-square difference from the measured visibilities; this unitary has mode fidelities of *Fm*(*U*meas, U^{vis} = 0.983 and $F_m(U^{hcz}, U^{\text{vis}})$ = 0.931 with the measured circuit and the ideal *hcz* respectively. The small differences between predicted and measured visibilities can be attributed largely to three factors: polarisation non-degeneracy between the interfering photons in the FLDW circuit; the slightly differing spectra of the SPDC photons and the laser diode used for the coherent characterisation; and the effects of higher-order SPDC terms.

Conclusions

Along with further improvements in photon sources and detection, heralding will be required to concatenate multiple entangling linear optics quantum gates, and thus enable more complex quantum computations and simulations. We have demonstrated that integrated waveguide arrays, particularly using femtosecond laser-writing, are capable of generating the required multimode interference circuits with both high fidelity and excellent quantum interference, and allow simple implementation of mode crossover elements and *π* phase shifts. However further careful engineering will be required to precisely achieve the desired beamsplitter ratios and to avoid undesired phase accumulation. This study has outlined the challenges both experimentally and theoretically in achieving waveguide circuits with high operational fidelities.

The quantum process fidelity of candidate circuits can be calculated from known fabrication tolerances or classical characterisation results using the Jamiolkowski isomorphism, and this metric has proven to be more sensitive and useful than mode fidelity for assessing such circuits. However any circuit imbalance will lead to error terms outside the computational subspace wherein two photons exit in either the control or target mode. The precise effects of such coherent error terms when multiple gates are concatenated, as well as possibilities for their correction or mitigation, could be a fruitful avenue for future investigation.

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Author Contributions

T.M. fabricated device with guidance from M.J.W., M.J.S. and G.D.M. D.N.B. characterised devices with input from T.M., A.G.W., M.A.B. and A.F. M.D. and A.G. were responsible for theoretical performance calculations. All authors contributed to the manuscript.

Additional Information

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