

Article

A Model of BGA Thermal Fatigue Life Prediction Considering Load Sequence Effects

Weiwei Hu ^{1,2,†}, Yaqiu Li ^{1,*,†}, Yufeng Sun ¹ and Ali Mosleh ²

¹ Reliability and System Engineering School, Beihang University, Haidian District, Beijing 100191, China; hww@buaa.edu.cn (W.H.); syf@buaa.edu.cn (Y.S.)

² B. John Garrick Institute for the Risk Sciences, University of California, Los Angeles, CA 90095, USA; mosleh@umd.edu

* Correspondence: ermao13@163.com; Fax: +86-10-8231-5729

† These authors contributed equally to this work.

Academic Editor: Yoshiharu Mutoh

Received: 20 August 2016; Accepted: 13 October 2016; Published: 24 October 2016

Abstract: Accurate testing history data is necessary for all fatigue life prediction approaches, but such data is always deficient especially for the microelectronic devices. Additionally, the sequence of the individual load cycle plays an important role in physical fatigue damage. However, most of the existing models based on the linear damage accumulation rule ignore the sequence effects. This paper proposes a thermal fatigue life prediction model for ball grid array (BGA) packages to take into consideration the load sequence effects. For the purpose of improving the availability and accessibility of testing data, a new failure criterion is discussed and verified by simulation and experimentation. The consequences for the fatigue underlying sequence load conditions are shown.

Keywords: BGA; life prediction; load sequence effects; crack growth; damage accumulation; resistance strain

1. Introduction

Solder joint interconnects serve as electrical connections and mechanical bonds between components and the substrate [1]. Small package size trends dictate leadless solder ball grid arrays (BGA) becoming the mainstream, as a result BGA package reliability has been put at the forefront of research issue.

Fatigue life prediction models have been investigated based on a diverse range of solder joints, all the models require some specific geometry and material related information to establish constitutive equations [2]. Some experts concentrate on the damage mechanisms and conditions, for example: electronic products may experience thermal cycle, power, shock, and vibration during the whole life cycle. Literature [3] shows three quarters of BGA fatigue failures are the result of thermal and vibration load, and researchers developed models by associating the stress-strain data under certain load with damage situation. For now, most of the models applied in engineering follow the Miner linear damage accumulation rule for its convenience. However, some researchers believe that the variation of load conditions also has an effect on the BGA products failure time [4], study [5] indicates that the linear cumulative damage law cannot conform to the experiments well. So they focus on the sequence influence of individual cycles in physical fatigue damage accumulation. A study by Okuyama et al. [6] demonstrates that the load conditions of thermal to vibration are harsher than in reverse. Fatemi and Yang [7] provided an overview on cumulative fatigue damage and life prediction, but these models do not have good practicability.

This paper tries to propose a life prediction model for BGA packages that takes load sequence influence into consideration that is considered more appropriate for application. Actually, it is

extremely difficult to monitor and track the initiation and propagation of fatigue cracks of BGA packages, consequently we define electrical resistance strain, whose value is equal to ratio of the resistance variation to initial electrical resistance, and can reflect the quantitative relationship between the extent of solder joint resistance variation and crack expansion as an alternative failure criteria for measuring the physical fatigue damage.

2. Thermal Fatigue Life Prediction Model for BGA

2.1. Model Assumption

Failure occurs when the fatigue crack of BGA solders reaches a critical length a_C ; the fatigue failure process of BGA solder joints can be divided into initial crack stage and crack propagation stage, the fatigue life of solder joints can be described as Equation (1) shows,

$$N_f = N_o + N_p, \quad (1)$$

where N_f means the whole fatigue lifetime of solder joints, N_o represents the lifetime of initial cracks, namely the cycles to crack initiation, and N_p represents the lifetime of the propagation stage, respectively [8]; assuming that cyclic thermal load conditions in the initial crack stage may affect the life of solder joints.

Several studies [9,10] indicate that cyclic thermal load conditions result in material hardening or softening, which may change the material hardness of solder joints. Bao [11,12] points out that when the recovery resistance of material is the same, the elastic modulus is proportional to the square root of the material hardness, therefore the elastic modulus of solder joints may be influenced and subsequently the stress situation is affected. In addition, loads with different amplitudes have different effects on inner defect of solder joints [13], Figure 1 shows that low loading amplitude can help release the inner stress of defects and extend solder joints fatigue life. On the contrary, high loading conditions may promote the expansion of defects and shorten their lifetime. L_1, L_2, L_3 in this figure represent three loading conditions and N_1, N_2, N_3 are their corresponding fatigue cycles, respectively.

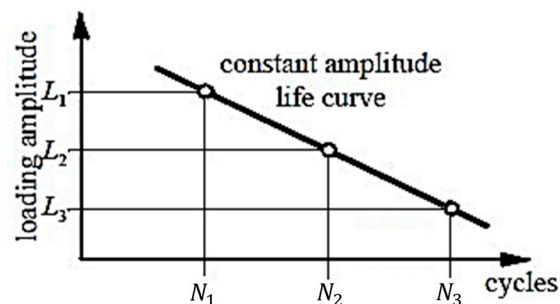


Figure 1. Scheme of different loading amplitude versus solder lifetime cycles.

Assuming that the load condition effects only exist in initial crack stage.

Abundant tests [14] show that cyclic softening or hardening situation will be stable within a short time, and the lifetime of the initial crack stage is much shorter than that of propagation stage (nearly 10%–20%). Therefore, we suppose that the load condition effects only exist in initial crack stage to simplify the model.

2.2. Base Model for BGA Fatigue Life

According to the Darveaux model [15], the relation between crack growth and final failure time can be described as follow,

$$N_o = k_1 (\Delta W_{ave})^{k_2}, \quad (2)$$

$$\frac{da}{dN} = k_3(\Delta W_{ave})^{k_4}, \tag{3}$$

$$N_f = N_0 + \frac{aC}{da/dN}, \tag{4}$$

in which ΔW_{ave} is the incremental viscoplastic strain energy density, $\frac{da}{dN}$ is the crack growth rate and $k_1 \sim k_4$ are correlation coefficients of material properties.

The Darveaux model has been proven to be available in engineering applications, Equation (3) indicates this model suppose that the crack growth rate is constant, which can be linear fitted by test data. However, research [16] demonstrates that crack growth rate increases with the increasing number of cycles in propagation stage, and the increasing trend is similar to exponential growth, as Figure 2 shows.

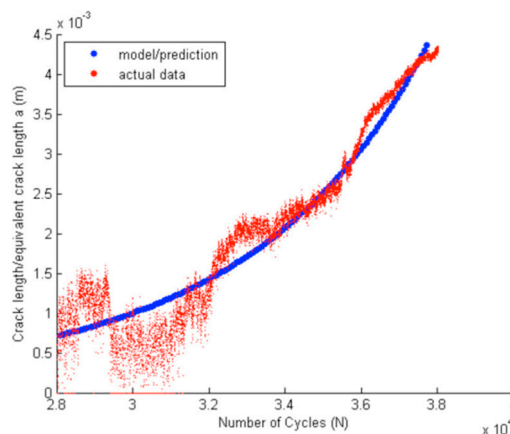


Figure 2. Relationship between the crack length and cycle number in BGA shearing test.

Hence we improve the Darveaux model with the Paris formula, which is widely used in describing the crack propagation stage, as Equation (5) shows.

$$\frac{da}{dN} = C (\Delta K)^n; \Delta K = \Delta\sigma Y \sqrt{\pi a}. \tag{5}$$

where C and n are material constants, ΔK means amplitude of stress intensity factor, $\Delta\sigma$ represents the differences between the maximum stress value and minimum stress value under certain load conditions, Y means the geometry influence function whose value is related to the sample size and load form, in our model we set $Y = 1$ for simplification [17].

Transforming Equation (5) and integrating from an initial crack length to a variable crack length gives ($N = 0, a_0 = 0$)

$$a^{-\frac{n}{2}} da = C \Delta\sigma^n \pi^{\frac{n}{2}} dN \Rightarrow a = \left(\frac{2-n}{2} C \pi^{\frac{n}{2}} \right)^{\frac{2}{2-n}} \Delta\sigma^{\frac{2n}{2-n}} N^{\frac{2}{2-n}}, \tag{6}$$

take a derivative with respect to cycle number,

$$\frac{da}{dN} = \frac{2}{2-n} \left(\frac{2-n}{2} C \pi^{\frac{n}{2}} \right)^{\frac{2}{2-n}} \Delta\sigma^{\frac{2n}{2-n}} N^{\frac{n}{2-n}}, \tag{7}$$

let $\alpha = \frac{2}{2-n} \left(\frac{2-n}{2} C \pi^{\frac{n}{2}} \right)^{\frac{2}{2-n}} \Delta\sigma^{\frac{2n}{2-n}}$, $\beta = \frac{n}{2-n}$ and we get,

$$\frac{da}{dN} = \alpha N^\beta, \tag{8}$$

where α is related to thermal cyclic stress condition $\Delta\sigma$ in crack propagation stage, thus it can be called in-time load factor. β is denoted as the exponential increasing trend of crack growth ratio, according to the assumption 4 the value of β is related to the stress condition in initial crack stage. When $\beta = 0$ and $da/dN = \alpha$ is constant, our model coincides with the Darveaux model.

Integrating Equation (8) from zero to critical crack length a_C gives equation of N_p ,

$$N_p = \left(\frac{\beta + 1}{\alpha} \cdot a_C \right)^{1/(\beta+1)}, \tag{9}$$

thus N_f can be refined as

$$N_f = N_o + N_p = k_1 (\Delta W_{ave})^{k_2} + \left(\frac{\beta + 1}{\alpha} \cdot a_C \right)^{1/(\beta+1)}. \tag{10}$$

2.3. Load Sequence Effects in Model

In daily engineering practice, load sequence effects are often ignored and variable amplitude fatigue life are calculated using the linear damage accumulation rule, but the results rarely coincide with experimentally determined data [18]. With the help of α and β in the base model, we are able to consider the load sequence effects in thermal fatigue life prediction of BGA solder joints.

There are two kinds of thermal cyclic load conditions: Ω_1 and Ω_2 , based on Equation (7) the instantaneous crack growth rates of the sample are,

$$\Omega_i : da_i/dN_i = \alpha_i N_i^{\beta_i}, i = 1, 2, \tag{11}$$

when the crack length $a = a_1 = a_2$, integrating Equation (11) gives

$$a_i = \frac{\alpha_i}{\beta_i + 1} N_i^{\beta_i + 1}, i = 1, 2, \tag{12}$$

combine these two equations together, relation formula between N_1 and N_2 is given

$$N_1 = \left[\frac{\alpha_2(\beta_1 + 1)}{\alpha_1(\beta_2 + 1)} \right]^{\frac{1}{\beta_1 + 1}} \cdot N_2^{\frac{\beta_2 + 1}{\beta_1 + 1}}. \tag{13}$$

Assuming that the BGA solder joint sample has experienced $N_1 = n_1$ cycles since passed initial crack stage in Ω_1 , then the thermal cyclic condition is transformed rapidly into Ω_2 , the instantaneous crack growth rate gives

$$\Omega_{1 \rightarrow 2} : da_2/dN_2 = \alpha_2 N_1^{\beta_1} = \alpha_2 \left[\frac{\alpha_2(\beta_1 + 1)}{\alpha_1(\beta_2 + 1)} \right]^{\frac{\beta_1}{\beta_1 + 1}} \cdot N_2^{\frac{\beta_1(\beta_2 + 1)}{\beta_1 + 1}}, \tag{14}$$

let $A = \alpha_2 \left[\frac{\alpha_1(\beta_1 + 1)}{\alpha_2(\beta_2 + 1)} \right]^{\frac{\beta_1}{\beta_1 + 1}}$, $B = \frac{\beta_1(\beta_2 + 1)}{\beta_1 + 1}$ and the modified crack growth equation of BGA sample can be deduced as follows,

$$\int_0^{a_C - a_1} da_2 = \int_0^{N_{p1 \rightarrow 2} - N_1} A \cdot N_2^B dN_2 \Rightarrow \Delta a = a_C - a_1 = \frac{A}{B + 1} (N_p - N_1)^{B + 1}, \tag{15}$$

$$a_C = \frac{A}{B + 1} (N_{p1 \rightarrow 2} - N_1)^{B + 1} + \frac{\alpha_1}{\beta_1 + 1} N_1^{\beta_1 + 1} \Rightarrow N_{p1 \rightarrow 2} = \left[\frac{B + 1}{A} \left(a_C - \frac{\alpha_1}{\beta_1 + 1} N_1^{\beta_1 + 1} \right) \right]^{\frac{1}{B + 1}} + N_1,$$

finally, we work out the thermal fatigue lifetime prediction model:

$$N_{f_{1 \rightarrow 2}} = N_{o_1} + N_{p_{1 \rightarrow 2}} = k_1 (\Delta W_{ave1})^{k_2} + N_1 + \left[\frac{B+1}{A} \left(a_C - \frac{\alpha_1}{\beta_1+1} N_1^{\beta_1+1} \right) \right]^{\frac{1}{\beta_1+1}} \quad (16)$$

2.4. Parameters Determination Based on Historical Crack Length Data

It is convenient that determining the values of α and β if the crack length data of a specimen in each load cycle is available. Take the logarithm of Equation (10), a relation between crack length and cycle number can be derived,

$$\ln a = (\beta + 1) \ln N + \ln \frac{\alpha}{\beta + 1} \quad (17)$$

we can work out the least square estimates of α and β by

$$\left\{ \begin{aligned} \hat{\beta} &= \frac{\sum_{i=1}^p \ln N_i \ln a_i - p \overline{\ln N} \overline{\ln a}}{\sum_{i=1}^p (\ln N_i)^2 - p (\overline{\ln N})^2} - 1 = \frac{\sum_{i=1}^p (\ln N_i - \overline{\ln N})(\ln a_i - \overline{\ln a})}{\sum_{i=1}^p (\ln N_i - \overline{\ln N})^2} - 1 \\ \hat{\alpha} &= (\hat{\beta} + 1) \left(\prod_{i=1}^p \frac{a_i}{n_i^{\hat{\beta}+1}} \right)^{1/p} \end{aligned} \right. \quad (18)$$

However, these history data are deficient because these micro defects like crack and void are unobservable without destructive physical analysis (DPA) method, not to mention that this method cannot take sequence effects into consideration. Therefore, improved methods are urgently needed.

3. Discussion of Numerical Computation Method for Model Parameter Determination

In this paper, we propose a numerical computation method for determining parameters of a BGA life prediction model considering load sequence effects. This method is available without keeping a record of crack length, as long as the cycle numbers in several conditions are provided. It will be more convenient for forecasting the lifetime of BGA packaged products.

3.1. Parameters Determining Equations

In order to reflect the load sequence effects, more relations between load condition and life cycle need to be established. At least four equations are needed to determine all the parameters in Ω_1 and Ω_2 as Equation (19) shows.

$$\left\{ \begin{aligned} \Omega_1 : N_{f_1} &= N_{o_1} + N_{p_1} = k_1 (\Delta W_{ave1})^{k_2} + \left(\frac{\beta_1+1}{\alpha_1} \cdot a_C \right)^{1/(\beta_1+1)} \\ \Omega_2 : N_{f_2} &= N_{o_2} + N_{p_2} = k_1 (\Delta W_{ave2})^{k_2} + \left(\frac{\beta_2+1}{\alpha_2} \cdot a_C \right)^{1/(\beta_2+1)} \\ \Omega_{1 \rightarrow 2} : N_{f_{1 \rightarrow 2}} &= N_{o_1} + N_{p_{1 \rightarrow 2}} = k_1 (\Delta W_{ave1})^{k_2} + N_1 + \left[\frac{B_1+1}{A_1} \left(a - \frac{\alpha_1}{\beta_1+1} n_1^{\beta_1+1} \right) \right]^{\frac{1}{\beta_1+1}} \\ \Omega_{2 \rightarrow 1} : N_{f_{2 \rightarrow 1}} &= N_{o_2} + N_{p_{2 \rightarrow 1}} = k_1 (\Delta W_{ave2})^{k_2} + N_2 + \left[\frac{B_2+1}{A_2} \left(a - \frac{\alpha_2}{\beta_2+1} n_2^{\beta_2+1} \right) \right]^{\frac{1}{\beta_2+1}} \end{aligned} \right. \quad (19)$$

where $A_1 = \alpha_2 \left[\frac{\alpha_1(\beta_1+1)}{\alpha_2(\beta_2+1)} \right]^{\frac{\beta_1}{\beta_1+1}}$, $B_1 = \frac{\beta_1(\beta_2+1)}{\beta_1+1}$, $A_2 = \alpha_1 \left[\frac{\alpha_2(\beta_2+1)}{\alpha_1(\beta_1+1)} \right]^{\frac{\beta_2}{\beta_2+1}}$, $B_2 = \frac{\beta_2(\beta_1+1)}{\beta_2+1}$, the simplification of these simultaneous equations gives relations between β_1 and β_2 , as Equation (20) shows,

$$\left\{ \begin{aligned} \left[\frac{1+\beta_1+\beta_1(\beta_2+1)}{(\beta_2+1)(\beta_1+1)} \left(\frac{\beta_2+1}{\beta_1+1} \right)^{\frac{2\beta_1}{\beta_1+1}} \left(1 - \left(\frac{n_1}{N_{p_1}} \right)^{\beta_1+1} \right) N_{p_1}^{\beta_1} N_{p_2}^{-\frac{\beta_1(\beta_2+1)}{\beta_1+1}} \right]^{\frac{1+\beta_1}{1+\beta_1+\beta_1(\beta_2+1)}} &= N_{f_{1 \rightarrow 2}} - N_{o_1} - n_1 \\ \left[\frac{1+\beta_2+\beta_2(\beta_1+1)}{(\beta_1+1)(\beta_2+1)} \left(\frac{\beta_1+1}{\beta_2+1} \right)^{\frac{2\beta_2}{\beta_2+1}} \left(1 - \left(\frac{n_2}{N_{p_2}} \right)^{\beta_2+1} \right) N_{p_2}^{\beta_2} N_{p_1}^{-\frac{\beta_2(\beta_1+1)}{\beta_2+1}} \right]^{\frac{1+\beta_2}{1+\beta_2+\beta_2(\beta_1+1)}} &= N_{f_{2 \rightarrow 1}} - N_{o_2} - n_2 \end{aligned} \right. \quad (20)$$

the approximate values of β_1 and β_2 can be worked out by Matlab software [19] with the test records of cycle numbers in different conditions. Afterwards, α_1 and α_2 can be calculated with Equation (21).

$$\alpha_i = \frac{a(\beta_i + 1)}{N_i^{\beta_i + 1}}, \tag{21}$$

3.2. Resistance Strain Theory

By nature of metal conductor resistance, according to solid fracture theory, the quantitative relationship can be revealed for the solder joint resistance strain and the crack expansion. The electrical resistivity of metal material increases as fatigue load cycles. Resistivity changes slightly in the initial crack stage, in the crack propagation stage resistivity varies relatively faster, when the material fails, fatigue damage mutates significantly as well as the resistivity [20].

Based on the Griffith fracture theory, the resistance strain of solder joints can be solved by Equation (22),

$$\varepsilon_R = \frac{R_t - R_0}{R_0}, \tag{22}$$

where R_0 is the solder joints resistances at time point t_0 , and R_t is the solder joints resistances at time point t .

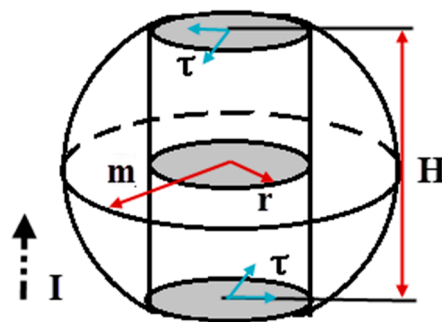


Figure 3. Schematic diagram of BGA solder joint and equivalent crack.

The geometry model of BGA solder joint is shown as Figure 3, I represents current direction passing the solder joint, τ represents the stress in the welding points. Supposing that only part of the damage crack defects, which are perpendicular to the current direction, have effects on the specimen resistance. When the solder joint is undamaged, its resistance can be solved by

$$R_0 = \rho \frac{H}{S}, \tag{23}$$

ρ is material resistivity, H is solder joint thickness, S is the cross-sectional area that perpendicular to the current, and $S = \pi r^2$, r is the radius of welding points between solder point and PCB. When crack defects grow under load the resistance changes by

$$R = \rho \frac{H}{S - S(a)} = \rho \frac{H}{\pi r^2 - S(a)}, \tag{24}$$

$S(a)$ is a function of crack length, which represents the equivalent area of crack defects. Equation (24) indicates that when $S(a) = 0$, the solder joint is undamaged and $R = R_0$. When $S(a) \ll S$, means that crack is in initial stage, the resistance increases slowly and linearly dependent on the crack growth. The solder joint fractures when $S(a)$ reaches the maximum value and resistance value tends to infinity nonlinearly ($R \rightarrow \infty$).

Differentiating Equation (24) with respect to all variables we obtain,

$$dR = \frac{d\rho \cdot H}{\pi r^2 - S(a)} + \rho \cdot \frac{dH}{\pi r^2 - S(a)} + \rho H \cdot \frac{S'(a)da}{[\pi r^2 - S(a)]^2} - \rho H \cdot \frac{2\pi r \cdot dr}{[\pi r^2 - S(a)]^2}, \tag{25}$$

then

$$\frac{dR}{R} = \frac{d\rho}{\rho} + \frac{dH}{H} + \frac{S'(a)da}{\pi r^2 - S(a)} - \frac{2\pi r \cdot dr}{\pi r^2 - S(a)}. \tag{26}$$

In this paper, it is the thermal load conditions that dominate electrical resistance changes, and the influence of dr and dH can be neglected. So Equation (25) can be transformed into

$$\varepsilon_R \approx \frac{\Delta R}{R} = \frac{\Delta\rho}{\rho} + \frac{S'(a)\Delta a}{\pi r^2 - S(a)} = \frac{\Delta\rho}{\rho} + \frac{\Delta S}{\pi r^2 - S(a_t) - \Delta S} + o(\Delta\rho\Delta S). \tag{27}$$

Assume that the equivalent area is a circle with radius a , thus $S(a_t) = \pi a_t^2$, a_t is the crack length at time point t , $S'(a) = 2\pi a$ and Δa is the increment of crack length in Δt . [21] points out that $\Delta\rho = \rho_0 \times k \times \Delta T$, ρ_0 is the resistivity of material at a certain ambient temperature, k is the temperature coefficient of resistivity, ΔT is temperature range. Thus we have

$$\varepsilon_R = \frac{\rho_0 k \Delta T}{\rho_0(1 + T_1)} + \frac{2\Delta a^2}{r^2 - a_t^2 - 2\Delta a^2} = \frac{k\Delta T}{(1 + T_1)} + \frac{2\Delta a^2}{r^2 - a_t^2 - 2\Delta a^2}, a < r, \tag{28}$$

where T_1 is the initial temperature of the thermal cycle load.

Crack growth rate [22] can be expressed by $\frac{da}{dt} = m \times \omega^\gamma$, in which $m = 8.34 \times 10^{-8}$, $\gamma = 1.08$, ω is related to the thermal stress inside of solder joint, in this paper we assume $\omega = 0.003$ which also follows the reference because of similar geometry and thermal conditions of solder joints. Combined with Equation (5) we have

$$\begin{cases} a_N = a_0 + C (\Delta\sigma Y \sqrt{\pi a_{t-1}})^n \times N \\ a_t = a_N + m \times \omega^\gamma \times rem(t, t_c) \end{cases}, \tag{29}$$

With $rem(t, t_c)$ means taking reminders of t/t_c , t_c is the time that a single cycle may costs. Besides, parameters in the equation that $C = 2.6 \times 10^{-13}$, $n = 2.1$, $Y = 1$, and $\Delta\sigma$ can be given by simulation which will be shown in following Section 3.3. As a result, the electrical resistance of solder joint in the $(N + 1)$ th cycle is

$$\varepsilon_R = \frac{k\Delta T}{(1 + T_1)} + \frac{2(m \times \omega^\gamma \times \Delta t)^2}{r^2 - [a_0 + C (\Delta\sigma Y \sqrt{\pi a})^n \times N + m \times \omega^\gamma \times rem(t, t_c)]^2 - 2(m \times \omega^\gamma \times \Delta t)^2}, \tag{30}$$

let t_r represents the time of heating-cooling process, t_s represents the holding time, T_L represents the minimum temperature while T_H means the maximum temperature, and the temperature changing rate is b . Equation (30) can be refined into

$$\begin{cases} Heating : \varepsilon_R = \frac{kb(t - N \times t_c)}{(1 + kT_L)} + v \\ holding_high : \varepsilon_R = \frac{k(T_H - T_L)}{(1 + kT_L)} + v \\ cooling : \varepsilon_R = \frac{kb[(N + 1) \times t_c - t_s - t]}{(1 + kT_L)} + v \\ holding_low : \varepsilon_R = v \end{cases}, \tag{31}$$

in which

$$v = \frac{2(m \times \omega^\gamma \times \Delta t)^2}{r^2 - [a_0 + C (\Delta\sigma Y \sqrt{\pi a})^n \times N + m \times \omega^\gamma \times rem(t, t_c)]^2 - 2(m \times \omega^\gamma \times \Delta t)^2} \tag{32}$$

3.3. Stress Range Simulation

Mechanical behavior of solder joints under certain load profile can be computed by ANSYS software [23]. In order to analyze the stress and strain situation of BGA under different loading conditions and obtain the equivalent stress range mentioned in the previous section, simulation models of BGA specimens are constructed in software. To ensure the accuracy of simulation results, simulation models need to be in accord with the real specimen.

3.3.1. Geometry Construction

In this paper MROM TF-BGA48 is selected as the research sample, the structure of which is as Figure 4 shows, detail size information is listed in Table 1.

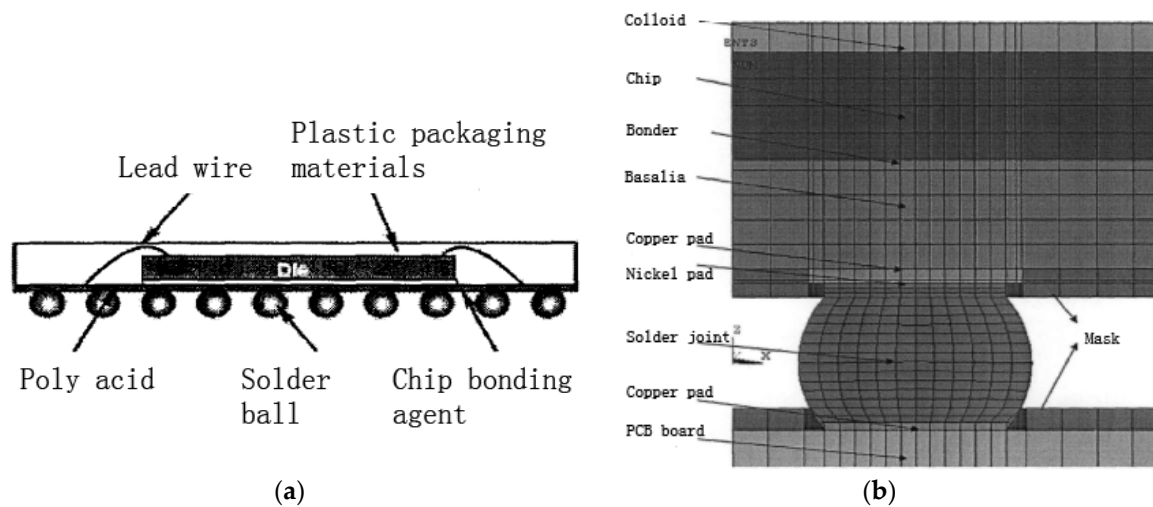


Figure 4. Structure diagram of MROM TF-BGA48: (a) Chip level structure; (b) Micro-structure of BGA solder joint.

Table 1. Information of MROM TF-BGA48.

Model Components	Whole Package	PCB	Upper Copper Backing	Lower Copper Backing	Nickel Pad
(mm)	6 × 8 × 1.3	40 × 40 × 0.5	φ0.45 × 0.027	φ0.514 × 0.018	φ0.45 × 0.005
(mm)	Welded-Spot pitch 0.8	Chip 4 × 5.6 × 0.254	Substrate 6 × 8 × 0.2366	Binder 6 × 8 × 0.0254	Colloid 6 × 8 × 0.53
(mm)	diameter (φ) 0.514	total height 0.322	Solder joint upper cylinder height 0.034		material 63Sn37Pb

According to the size information, a simulation sample can be constructed in software, as Figure 5 shows.

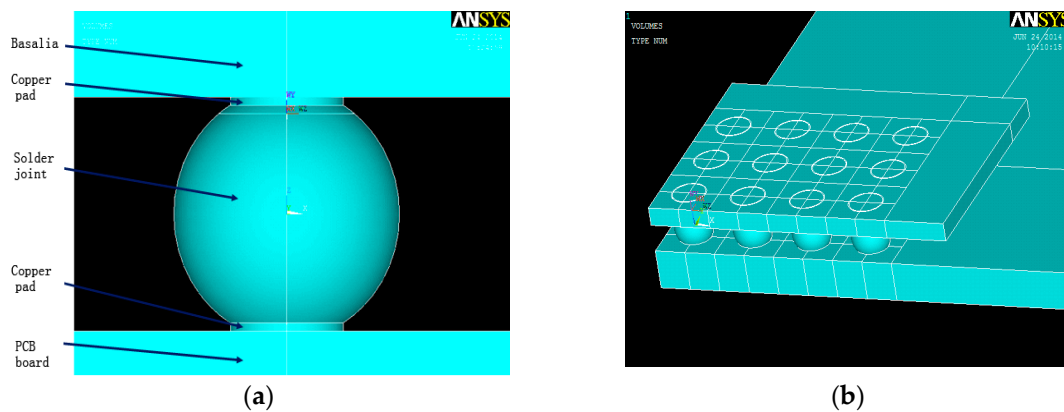


Figure 5. Simulation model of MROM TF-BGA48: (a) Single solder joint simulation model; (b) Chip level simulation model.

3.3.2. Initial Simulation Setting

For the purpose of describing the relationship between stress tensor and strain tensor, we can directly invoke a classical Anand constitutive model, which has been embedded into ANSYS. Furthermore, we chose Solid185 instead of Visoco107 for material unit in ANSYS, since the Solid185 can be used in simulating the deformation of more complex materials, such as incompressible elastic-plastic material or incompressible hyperelastic material, other material settings in the simulation are listed in Table 2. The full transient dynamic analysis method in software is used for large displacement analysis and the time increment of load substep values 10, other simulation settings like calculating times and convergence condition remain at defaults to ensure the convergence and accuracy of results.

Table 2. Material attribution setting.

Component Category	Elasticity Modulus (GPa)	Poisson's Ratio	CTE ($10^{-6}/^{\circ}\text{C}$)
Solder joint	30	0.35	21
PCB (FR4)	48.2	0.3	19.17
Substrate	16.85	0.3	16
Copper backing	129	0.38	16.9
Specimen plate	17.4	0.15	15

3.3.3. Mesh and Boundary Condition

Both the quantity and quality of mesh are significant to guarantee the accuracy of simulating results, especially the meshes on the welded ball of the specimen. Firstly, we divided a single welded ball model into quarters along two mutually perpendicular planes. Then we trisected the radius of a quarter of a welded ball, simultaneously quartering the remaining ball arc. After that, we executed the 'sweep' command to generate sufficient meshes, repeating the operation until the whole welded ball is meshed, as the Figure 6a shows.

To reflect the actual boundary condition of BGA specimens, symmetry plane constraint command is attached to the edge surfaces for prohibiting their displacements and rotary movements in simulation. Additionally, we set the PCB bottom point's DOF (degree of freedom) as zero to avoid rigid body motion as Figure 6b shows.

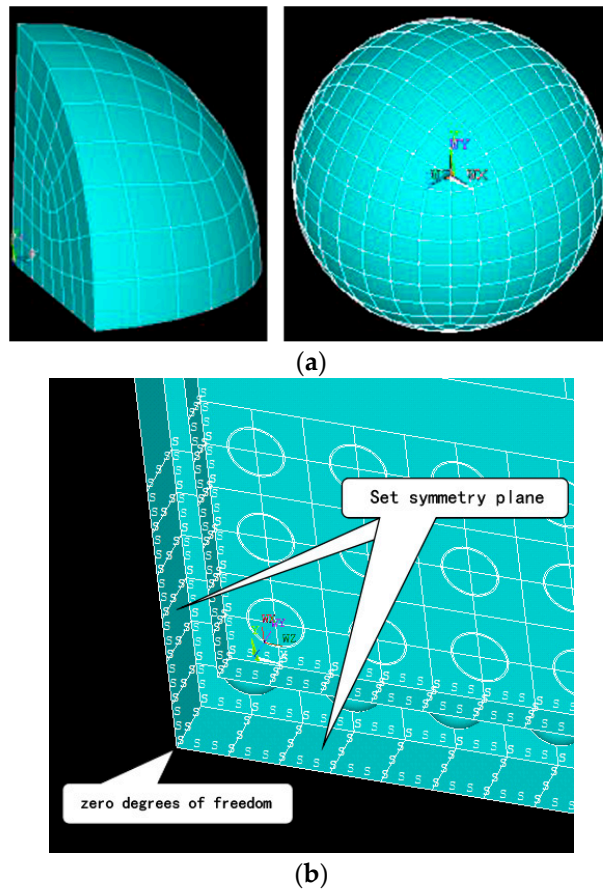


Figure 6. Mesh and boundary set in simulation model: (a) Meshes on welded ball of specimen; (b) Boundary conditions of symmetry plane and zero DOF.

3.3.4. Thermal Load Profile

The simulation thermal load profiles are set as Figure 7 shows. In Ω_1 , the maximum temperature is 100 °C and the minimum temperature is -60 °C, holding times are 10 min, the rate of temperature change is 1 °C/min. In Ω_2 , the maximum temperature is 80 °C and the minimum temperature is -40 °C, holding time and temperature changing rate are the same.

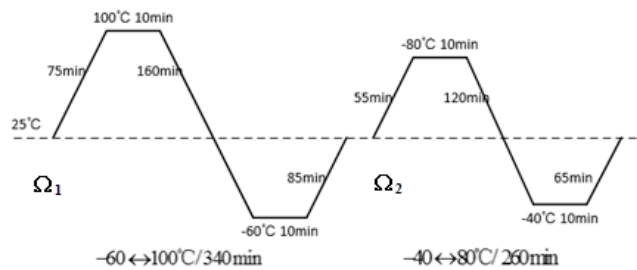


Figure 7. Thermal load profiles of Ω_1 and Ω_2 .

3.3.5. Equivalent Stress Range

Figure 8 is the deformation nephogram of solder joints in BGA package structure under one of our thermal load conditions. It indicates the solder joints that are closer to the edge of package the larger plastic strain they will suffer, which coincides with the experiment results in [24]. Table 3 shows the simulation results of equivalent stress (Von Mises stress) range $\Delta\sigma$, plastic shear strain range $\Delta\gamma$,

and fatigue life N_f . Default Coffin-Manson model is selected in ANSYS to give a preliminary fatigue life in Ω_1 and Ω_2 , respectively.

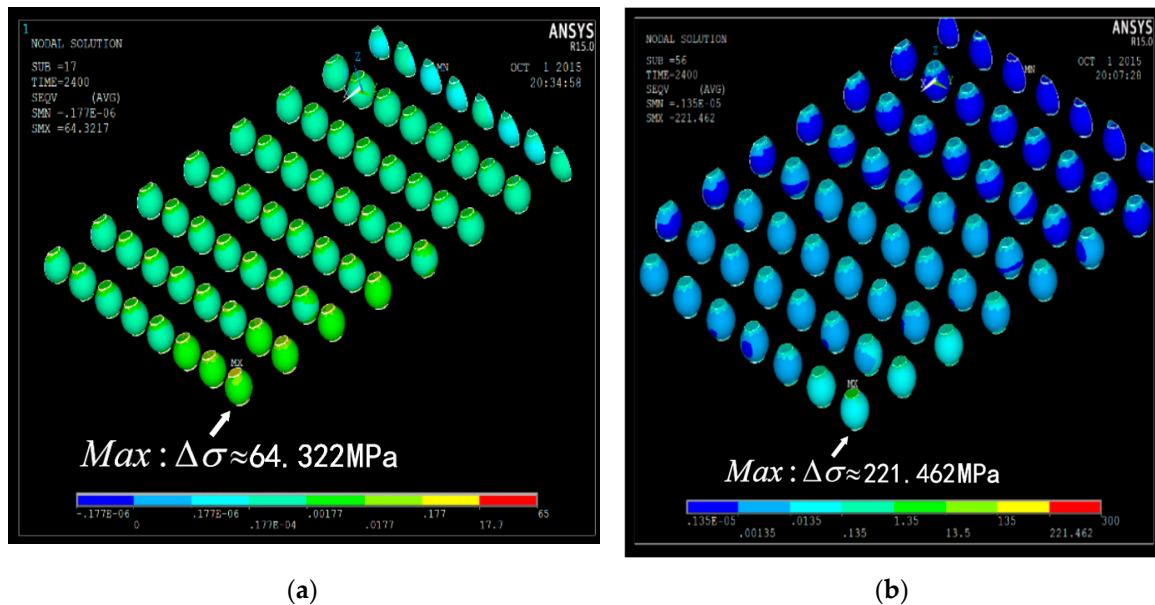


Figure 8. Distribution of stress and strain contours: (a) In Ω_1 load profile; (b) In Ω_2 load profile.

Table 3. Simulation results under thermal load profiles.

Model Variables	Ω_1	Ω_2
Equivalent stress range $\Delta\sigma$ (MPa)	221.462	64.322
Plastic shear strain range $\Delta\gamma$	0.03068	0.02334
Fatigue life N_f	1198	2045

3.4. Thermal Fatigue Life Computation

According to Equation (31), resistance strain curve under certain load conditions can be graphed as Figure 9 shows. The figure indicates that the variation of resistance strain is reversible, it increases or decreases as the temperature rises or falls during the thermal cycle. The amplitude of resistance strain variation depends on the temperature differences, the values of resistance strain increment are 0.335 and 0.243 in Ω_1 and Ω_2 , respectively.

The relationship between resistance strain and number of cycles can be obtained by extracting the information of resistance strain at certain temperature in each cycle. The relation curve under different thermal load conditions Ω_1 , Ω_2 , $\Omega_{1\rightarrow 2}$, $\Omega_{2\rightarrow 1}$ are graphed by programming in Matlab according to the theory described in Section 3.2 and the result are shown in Figure 10.

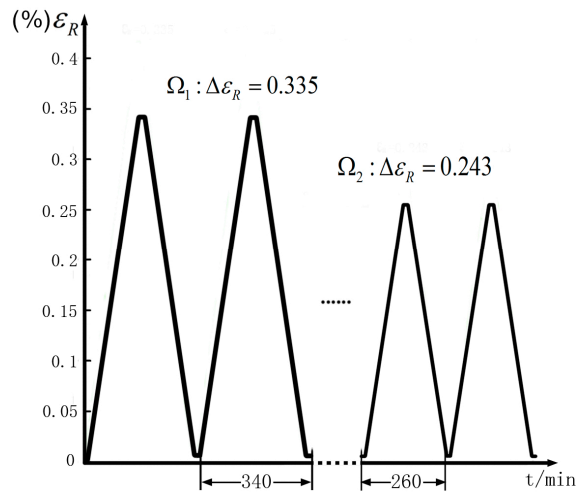


Figure 9. Resistance strain curve in certain thermal load conditions.

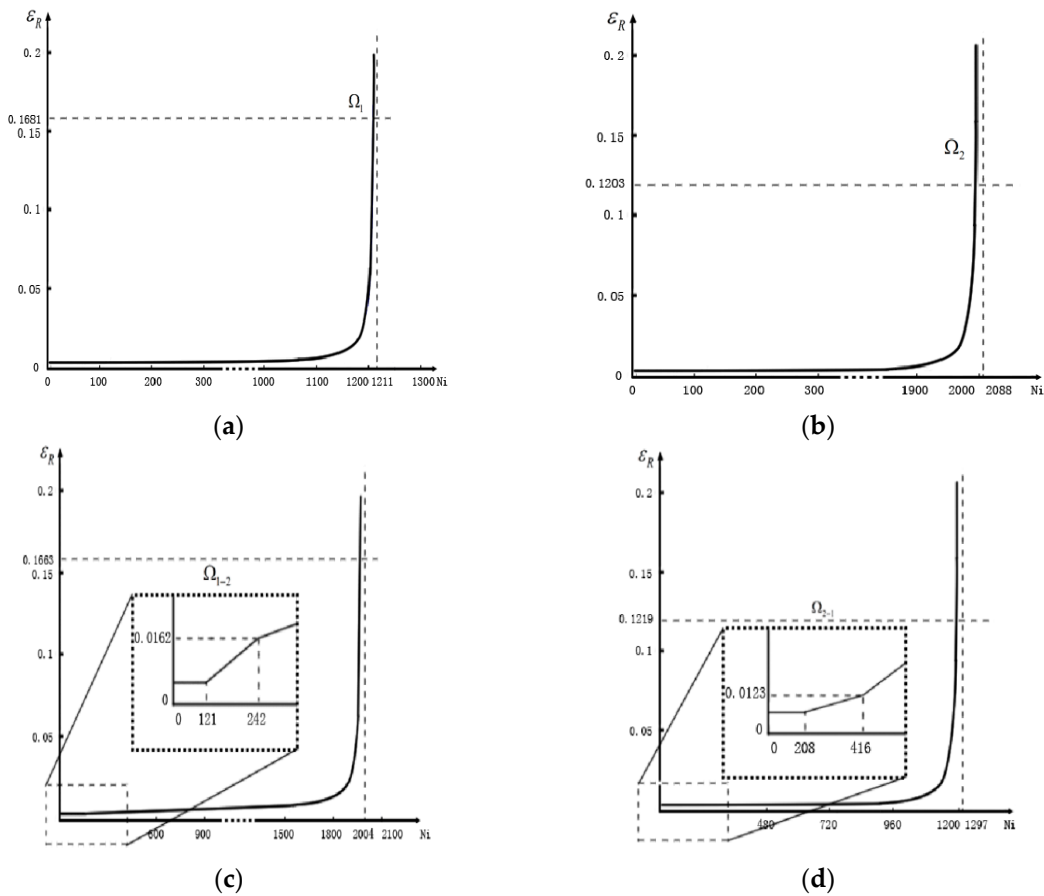


Figure 10. Relation between resistance strain and cycle numbers at $T = 298\text{ K}$ under different load conditions: (a) Ω_1 ; (b) Ω_2 ; (c) $\Omega_{1 \rightarrow 2}$; (d) $\Omega_{2 \rightarrow 1}$.

As (a) and (b) describe that there are some irreversible resistance strains during thermal cycle, this can be the results of irrecoverable defects like crack and void. The irrecoverable defects are negligibly small with respect to the recoverable defects in Figure 10, however, when these kinds of defects accumulate to some extent, a surge may occur in resistance strain. It turns out to be the consequence of crack length reaches the critical length a_c (radius of solder points) that surge occurs at the same time.

The simulation of thermal load sequence conditions $\Omega_{1 \rightarrow 2}$ and $\Omega_{2 \rightarrow 1}$ are based on the previous work. Solder joints spend their initial crack phase, which occupies 10% of the whole fatigue lifetime according to the fourth model assumption, under the first condition. We hypothesize that the specimen remains in the first condition until the next 1/10 of fatigue lifetime has passed, the load condition turns into the other condition afterwards. The simulation results showed in (c) and (d) indicate that a crack grows linearly before the surges. Moreover, the crack growth rate varies with the thermal load condition.

According to the simulation analysis, all information of fatigue life needed for calculating the life prediction model parameters are shown in Table 4.

Table 4. Fatigue life information.

Fatigue Variables	Ω_1	Ω_2	$\Omega_{1 \rightarrow 2}$	$\Omega_{2 \rightarrow 1}$
N_{f_i}	1211	2088	2004	1297
N_{o_i}	121	208	121	208
N_{p_i}	1090	1880	1762	881
n_i	/	/	121	208

Plug these fatigue data into Equation (19), the values of β_1 and β_2 can be solved by Matlab as Figure 11 shows, $\beta_1 = 1.136$ and $\beta_2 = 1.052$. So we can work out $\alpha_1 = 7.8192 \times 10^{-9}$ and $\alpha_2 = 4.4132 \times 10^{-9}$.

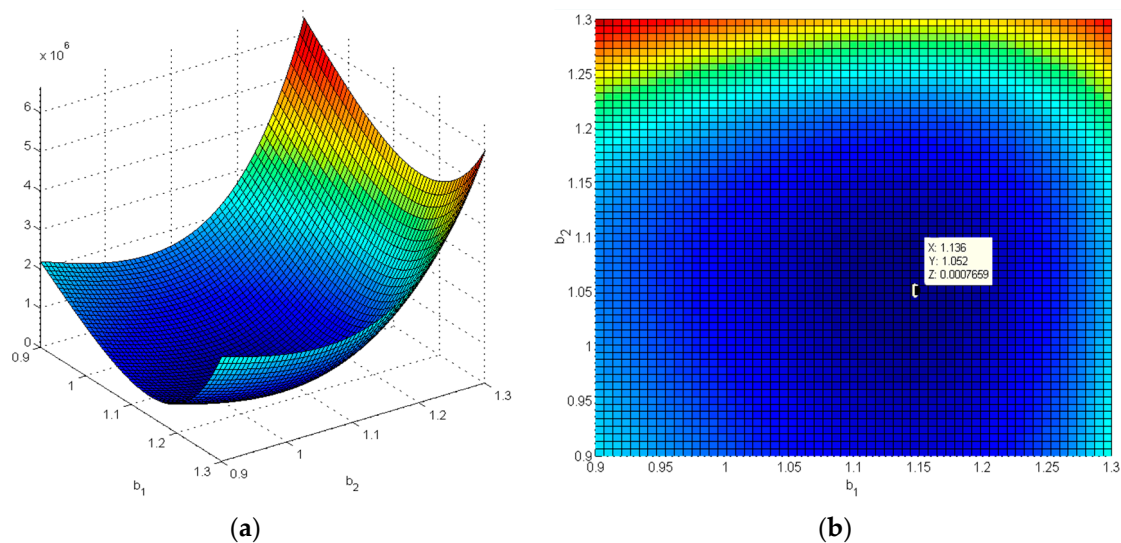


Figure 11. Simulation result of MATLAB: (a) 2-D parameter surface of β_1 and β_2 ; (b) Minimum point of β_1 and β_2 .

4. Experimental Verification

Studies [25–27] demonstrate that daisy chain structure is an effective way to reduce the difficulty of monitoring a batch of solder joints. In daisy chain structure, severe damage in one single solder joint, especially the corners, often leads to the destruction of the entire package. The BGA package sample is designed as Figure 12 shows.

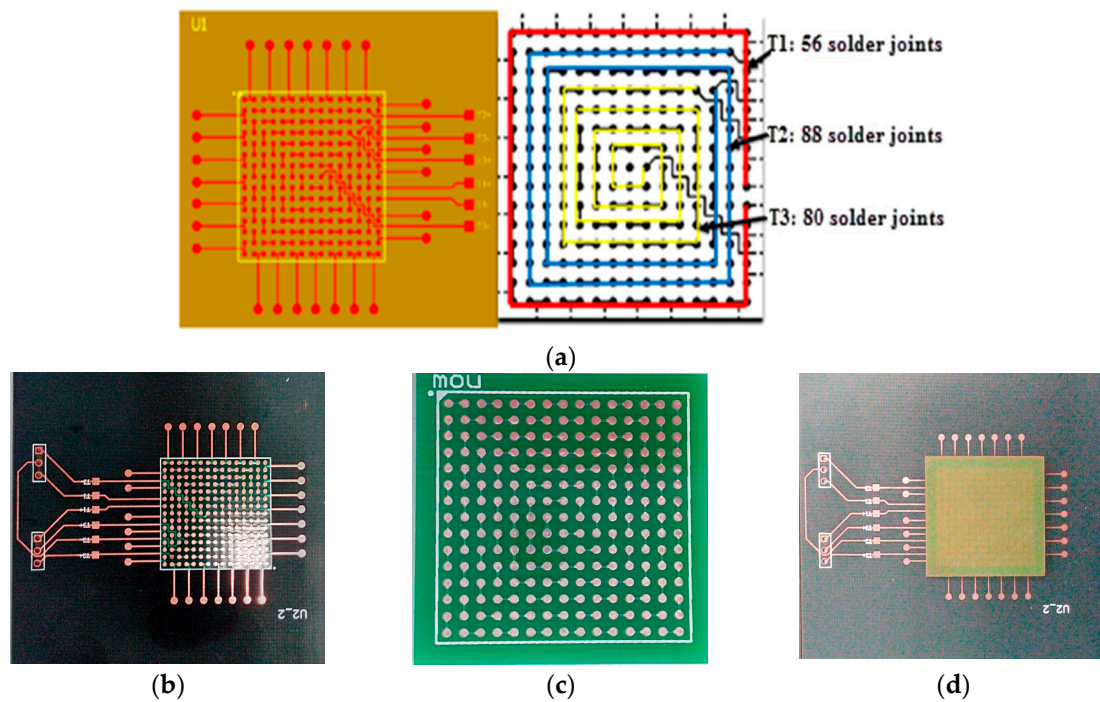


Figure 12. BGA package sample used in experiment: (a) Designed daisy chain structure; (b) Printed circuit board; (c) Chip substrate; (d) Welded sample.

Temperature cycle test is conducted following the $\Omega_{1 \rightarrow 2}$ and $\Omega_{2 \rightarrow 1}$ conditions, we record the resistances of solder joints in daisy chain at $T = 298$ K every five cycles. The relation curve between resistance strain and cycle numbers can be fitted by the records as shown in Figure 13a,b. After thermal cycle test, the SEM (scanning electron microscope) is used for location the crack deflection region, and we observed that cracks arose a lot, especially at the corner solder joints as reference mentioned, as shown in Figure 13c.

According to Figure 13, we can see that in the early stage each condition the resistance strain grows slowly and linearly. The values of resistance strain at the condition changing time is similar to the simulation results, and the tendencies of ε_R approximate to the results in Figure 10. In real experimental environment, fatigue is not the only factor that leads to damage, creep and other mechanisms may also exist. As a result, the experimental value of ε_R is basically bigger than simulation results, and experiment life is shorter.

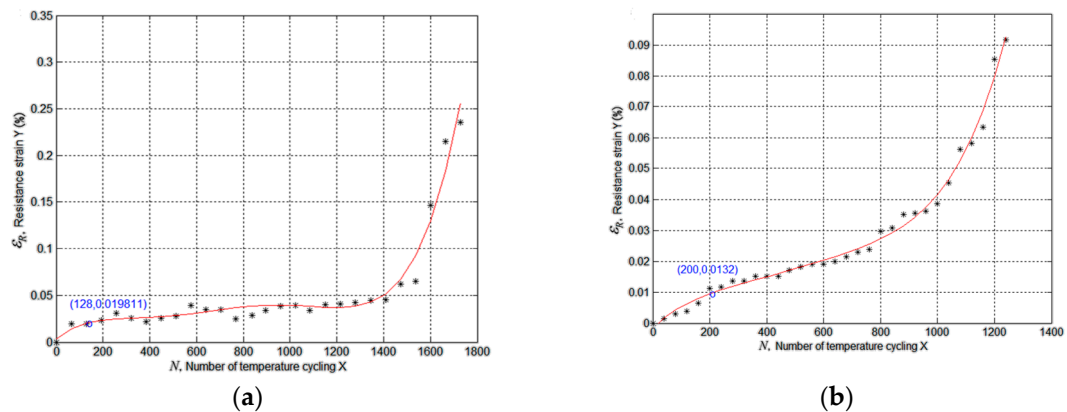


Figure 13. Cont.

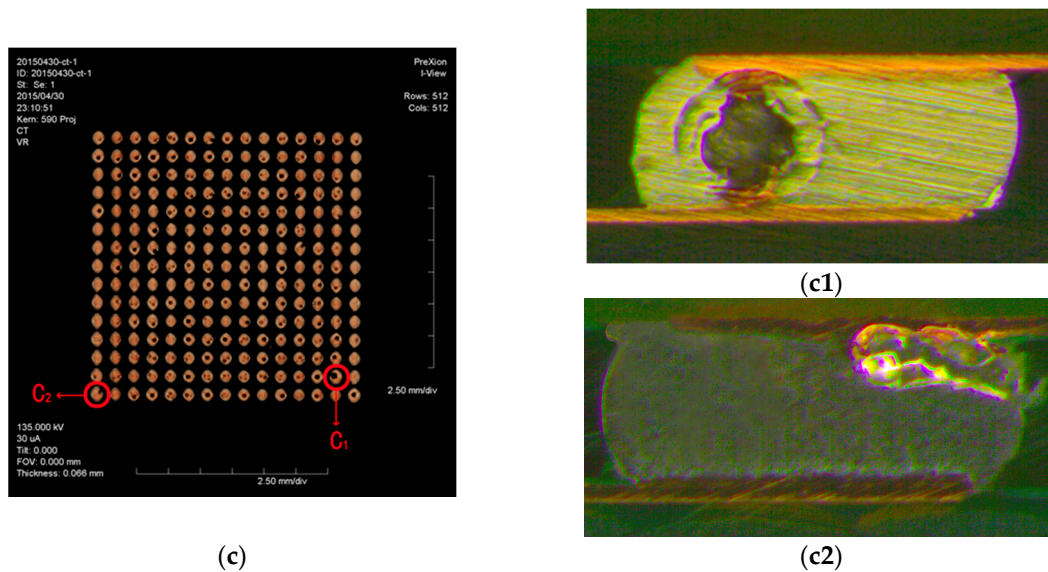


Figure 13. Relation curve between resistance strain and cycle numbers under: (a) $\Omega_{1 \rightarrow 2}$ and (b) $\Omega_{2 \rightarrow 1}$ condition of temperature cycle test; (c) Different crack appearances in solder joints at corner areas of the sample after test: (c1) crack penetrating into the solder joint; (c2) crack at the welded fringe.

5. Conclusions

Traditional fatigue prediction approaches are based on isothermal fatigue test data, but basically they cannot be appropriately used for engineering applications. Actually, most of the approaches ignore the load sequence influence and fatigue test data for mini-sized devices is hard to obtain, as a result of that, errors may be introduced if the data is not accurate.

In this article, we develop a new BGA life prediction model considering load sequence effects. Specifically, the analytic mathematic model is derived based on Darveaux model and Paris formula, whose parameters are determined by numerical simulation approaches. A resistance strain theory is proposed to reflect the crack growth progress by more convenient resistance data from fatigue test, and conducted by Mat lab software; the stress and strain situation that is needed for the theory is analyzed by modeling the BGA in ANSYS software. This work can help decrease the difficulty of getting fatigue test data effectively. Moreover, the resistance strain is discussed as microstructure damage related failure criteria and verified by experiment. However, the infinite element simulation of our model in ANSYS was able to analyze the stress and strain situation under different loading conditions, but cannot take the crack influence into consideration. Further research will be focused on improving the model, and our life prediction model may be a promising tool for future research in electronic device monitoring and life prediction.

Author Contributions: Weiwei Hu and Yaqiu Li conceived and designed theory and experiments, then performed the experiments; Yaqiu Li analyzed the data; Yufeng Sun contributed experiment and material tools, Ali Mosleh provided feedback on the described experiments and the writing of the manuscript; Yaqiu Li wrote the paper.

Conflicts of Interest: The authors declare no conflict of interest.

Nomenclature

N_f	the whole fatigue lifetime of solder joints, also the stress cycles of a specified character that a specimen sustains before failure of a specified nature occurs
N_{f_i}	the total fatigue life cycles of ball grid arrays under the i th thermal load condition
$N_{f_{i \rightarrow j}}$	the total fatigue life of ball grid arrays consists of certain fatigue cycles under the i th thermal condition and the rest cycles under the j th condition
N_0	the initial crack lifetime of solder joints, also the cycles to crack initiation

N_{0_i}	the initial crack lifetime of solder joints under the i th thermal condition
N_p	the crack propagation or growth lifetime of solder joints
N_{p_i}	the crack propagation lifetime of solder joints under the i th condition
$N_{p_{i \rightarrow j}}$	the crack propagation lifetime of ball grid arrays consists of certain propagation cycles under the i th thermal condition and the rest cycles under the j th condition
N_i	the actual cycles under the i th thermal condition, in which we assume that initial crack lifetime under i th thermal condition is included
a, a_C, a_t, a_N	crack length, critical crack length, crack length at certain time point t , and the crack length at certain cycle times N
da_i/dN_i	crack growth rate under the i th thermal load condition
ΔW_{avei}	the incremental viscoplastic strain energy density under the i th thermal load condition
α_i	constant related to thermal cyclic stress range $\Delta\sigma$ in crack propagation stage under the i th thermal load condition
β_i	constant related the exponential increasing trend of crack growth ratio under the i th thermal load condition
k_1, k_2, k_3, k_4	the correlation coefficients of material properties in Darveaux model
C, n	material constants of the Darveaux model in the Paris formula
ΔK	the amplitude of stress intensity factor
$\Delta\sigma$	the differences between the maximum stress value and minimum stress value under certain load conditions
Y	the geometry influence function whose value is related to the sample size and load form
A_1, A_2, B_1, B_2	model parameters related with α_i and β_i
Ω_i	the unchangeable load profile under i th thermal condition
$\Omega_{i \rightarrow j}$	the changeable load profile under i th condition then change to j th condition
ε_R	the resistance strain of solder joints
ρ, ρ_0	the material resistivity and the resistivity of material at certain ambient temperatures
H	the thickness of solder joint
S	the cross-sectional area that perpendicular to the current, and $S = \pi r^2$
r	the radius of welding points between solder point and PCB
t_r, t_s	the time of heating-cooling process and the holding time
T_H, T_L	the minimum temperature and the maximum temperature

References

1. Lee, W.W.; Nguyen, L.T.; Selvaduray, G.S. Solder joint fatigue models: Review and applicability to chip scale packages. *Microelectron. Reliab.* **2000**, *40*, 231–244. [[CrossRef](#)]
2. Pang, H.L.; Low, T.H.; Xiong, B.S. Lead-free 95.5Sn-3.8Ag-0.7Cu solder joint reliability analysis for Micro-BGA assembly. In Proceedings of the 50th Annual electronics components and technology conference, Las Vegas, NV, USA, 31 May–3 June 2005; pp. 131–136.
3. Luan, J.E.; Tee, T.E.; Pek, E. Advanced numerical and experimental techniques for analysis of dynamic responses and solder joint reliability during drop impact. *IEEE Trans. Compon. Packaging Tech.* **2006**, *29*, 449–456. [[CrossRef](#)]
4. Yang, L.; Yin, L.; Arafei, B.; Roggeman, B.; Borgesen, P. On the assessment of the life of SnAgCu solder joints in cycling with varying amplitudes. *IEEE Trans. Compon. Packaging Tech.* **2013**, *3*, 430–440. [[CrossRef](#)]
5. Tang, H.; Basaran, C. Experimental characterization of material degradation of solder joint under fatigue loading. In Proceedings of the Eighth Intersociety Conference on IEEE, San Diego, CA, USA, 30 May–1 June 2002; pp. 896–902.
6. Okuyama, S.; Yu, Q.; Akutsu, T. Reliable evaluation method for solder joints in vehicle electronics devices considering the actual use conditions. In Proceedings of the 13th IEEE Intersociety Conference on IEEE, San Diego, CA, USA, 30 May–1 June 2012; pp. 654–658.

7. Fatemi, A.; Yang, L. Cumulative fatigue damage and life prediction theories: A survey of the state of the art for homogeneous materials. *Int. J. Fatigue* **1998**, *20*, 9–34. [[CrossRef](#)]
8. Robert, D.; Sabira, E.; Corey, R.; Christopher, J.B.; Nabeel, Z. Crack initiation and growth in WLCSP solder joints. In Proceedings of the 2011 IEEE 61st Electronic Components and Technology Conference (ECTC), IEEE, Lake Buena Vista, FL, USA, 29 May–1 June 2011; pp. 940–953.
9. Manson, S.S.; Halford, G.R. Treatment of Multiaxial Creep-Fatigue by Strain Range Partitioning. In Proceedings of the Winter Annual Meeting of the Am. Soc. of Mechanical Engineers, NASA Lewis Research Center, New York, NY, USA, 6–10 December 1976.
10. Yang, L.; Liang, Y.; Roggeman, B.; Borgesen, P. Effects of microstructure evolution on damage accumulation in lead-free solder joints. In Proceedings of the 60th Electronic Components and Technology Conference (ECTC), Las Vegas, NV, USA, 1–4 June 2010; pp. 1518–1523.
11. Bao, Y.W.; Li, W.H.; Qiu, Y. Investigation in biaxial-stress effect on mode I fracture resistance of brittle materials by FEM simulation and K–G–J– δ relationship. *Mater. Lett.* **2005**, *59*, 439–445. [[CrossRef](#)]
12. Bao, Y.W.; Liu, L.Z.; Zhou, Y.C. Assessing the elastic parameters and energy-dissipation capacity of solid materials: A residual indent may tell all. *Acta Mater.* **2005**, *53*, 4857–4862. [[CrossRef](#)]
13. Lin, C.K.; Teng, H.Y. Creep properties of Sn-3.5 Ag-0.5 Cu lead-free solder under step-loading. *J. Mater. Sci. Mater. Electron.* **2006**, *17*, 577–586. [[CrossRef](#)]
14. Lall, P.; Vaidya, R.; More, V.; Goebel, K. Assessment of Accrued Damage and Remaining Useful Life in Leadfree Electronics Subjected to Multiple Thermal Environments of Thermal Aging and Thermal Cycling. *IEEE Trans. Compon. Packaging Tech.* **2012**, *2*, 634–649. [[CrossRef](#)]
15. Darveaux, R.; Banerji, K.; Mawer, A.; Dody, G. Reliability of plastic ball grid array assembly. *Ball Grid Array Technol.* **1995**, 379–442.
16. Fiedler, B.; Fine, M.; Kao, K.; Keer, L. Monitoring Fatigue Cracking in Interconnects in a Ball Grid Array by Measuring Electrical Resistance. *J. Electron. Packaging* **2012**, *134*, 2346–2350. [[CrossRef](#)]
17. Vormwald, M. Classification of Load Sequence Effects in Metallic Structures. *Procedia Eng.* **2015**, *101*, 534–542. [[CrossRef](#)]
18. Vormwald, M. Fatigue crack propagation under large cyclic plastic strain conditions. *Procedia Mater. Sci.* **2014**, *3*, 301–306. [[CrossRef](#)]
19. Lindfield, G.; John, P. *Numerical Methods: Using MATLAB*; Academic Press: Pittsburgh, PA, USA, 2012.
20. Jiang, L.; Yang, K.; Zhou, J.; Xiang, K.; Wang, W. Quantification of creep strain in small lead-free solder joints with the in situ micro-electronic resistance measurement. *Microelectro. Reliab.* **2008**, *48*, 438–444. [[CrossRef](#)]
21. Suresh, S. *Fatigue of Materials*; Cambridge University Press: Cambridge, UK, 1998.
22. Zhao, J.; Mutoh, Y.; Miyashita, Y.; Wang, L. Fatigue crack growth behavior of Sn–Pb and Sn-based lead-free solders. *Eng. Fracture Mech.* **2003**, *70*, 2187–2197. [[CrossRef](#)]
23. *ANSYS Structural Analysis Guide*; ANSYS, Inc.: Canonsburg, PA, USA, 2013.
24. Rodgers, B.; Punch, J.; Jarvis, J. Finite element modelling of a BGA package subjected to thermal and power cycling. In Proceedings of the The Eighth Intersociety Conference on Thermal and Thermomechanical Phenomena in Electronic Systems, 2002, IThERM 2002, San Diego, CA, USA, 30 May–1 June 2002; pp. 993–1000.
25. Kim, Y.G.; Han, B.; Choi, S.; Kim, S.K. Bottom Ledged Plastic (BLP) package: A new design with enhanced solder joint reliability. In Proceedings of the Electronic Components and Technology Conference, Orlando, FL, USA, 28–31 May 1996; pp. 448–452.
26. Lau, J.H.; Lee, S.W.R.; Lee, R.S. *Chip Scale Package (CSP): Design, Materials, Process, Reliability & Applications*; McGraw-Hill Professional: New York, NY, USA, 1999.
27. Ghaffarian, R. Effect of Area Array Package Types on Assembly Reliability and Comments on IPC-9701A. *APEX* **2016**, in press.

