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Low-cost uncooled MWIR PbSe quantum dots photodiodes

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A mid-wave infrared (MWIR) uncooled PbSe-QDs/CdS p–n heterojunction photodiode has been fabricated using a wet-chemical synthesis route. This offers a low-cost alternative to traditional monocrystalline photodiodes relying on molecular beam epitaxy (MBE) technology. It was demonstrated that the post-annealing is critical to tailor the photoresponse wavelength and to improve the performance of photodiodes. After annealing at 673 K in air for 0.5 h, the ligand-free PbSe-QDs/CdS photodiode exhibits a MWIR spectral photoresponse with a cutoff wavelength of 4.2 μm at room temperature. Under zero-bias photovoltaic mode, the peak responsivity and specific detectivity at room temperature are $0.36 \pm 0.04 \text{ A W}^{-1}$ and $(8.5 \pm 1) \times 10^8 \text{ cm Hz}^{1/2} \text{ W}^{-1}$, respectively. Temperature-dependent spectral response shows an abnormal intensity variation at temperatures lower than 200 K. This phenomenon is attributed to the band alignment transition from type II to type I, resulting from the positive temperature coefficient of PbSe. In addition, it was proved that In doped CdSe (CdSe:In) films could be used as a promising new candidate of infrared transparent conductive electrodes, paving the way for monolithic integration of uncooled low-cost MWIR photodiodes on Si readout circuitry.

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1 Introduction

Lead chalcogenide (PbS, PbSe, and PbTe) quantum dots (QDs) have achieved rapid progress in various research fields over the past 10 years.^{1–6} Their affinities for solution-based processing,⁴ large bandgap tunability,^{7–9} and enhanced multiple exciton generation (MEG)¹⁰ or carrier multiplication (CM),¹¹ have made them promising candidates for next-generation low-cost solution-processed photo-electric technologies. Among the lead chalcogenide family, lead selenide (PbSe) QDs have received significant scientific and technological attention not only in visible light-emitting diodes,^{12,13} solar cells^{14,15} and gas sensor^{16,17} but also in infrared photodiodes.^{18,19} Being a narrow bandgap semiconductor (0.27 eV), PbSe possesses an extremely large bulk Bohr exciton radii of 46 nm, which creates a strong quantum confinement in nanocrystals, allowing its bandgap and absorption edge to be tailored from NWIR (around 0.7 μm for 2 nm QD) to MWIR (3–4.5 μm for QD size around 20 nm to micro-sized crystals).^{9,20} More importantly, the large bandgap tunability of more than 1.5 eV is easily achieved by controlling QD size by using various scalable, low-cost methods such as hydrothermal synthesis,²¹ thermal decomposition,²² electron beam irradiation,²³ chemical bath deposition (CBD) method,²⁴ *etc.* In addition, these PbSe QDs can show high infrared-responsivities even at room temperature due to their low

Auger recombination and low intra-gap defect density.²⁵ This becomes highly favourable for the realization of small size, light weight and low power (SWaP) infrared detection, particularly for high-operating temperature, low-cost, high-yield detector arrays required for imaging applications. Yang reported solution-processed PbSe QD infrared photodetectors with photo-responsivity greater than 2.93 A W^{-1} for 980 nm illumination in 2016.²⁶ Subsequently, Dang demonstrated high-performance solution-processed PbSe QD detectors working at mid-infrared 2.8 μm . The maximum responsivity, detectivity and external quantum efficiency are 0.96 A W^{-1} , 8.13×10^9 Jones and 78% at 5 V bias respectively at room temperature.²⁷ However, there is no report on PbSe QD photodiodes operating beyond 3 μm at room temperature. This is caused by the following three reasons. First, it is well known that the introduction of organic or metal halide ligands during the QD synthesis process is essential for high-performance operation.^{6,28} The ligands can effectively reduce the trap states by surface passivation. Unfortunately, the introduction of ligands degrades the carrier mobility of PbSe CQD films, resulting in a heavy carrier transport loss, which hinders the PbSe CQDs potential advantages in MWIR detection.²⁹ Second, due to a low deposition rate and an inert atmosphere environment associated with spin-coating (layer-by-layer deposition) technology, it is still a challenge to obtain thicker CQD films of more than 1 μm . This thickness is necessary for a sufficient absorption of MWIR radiation. Moreover, the state-of-the-art MWIR transparent conductive electrodes, including lower concentration tin-doped indium oxide,³⁰ carbon nanotubes,³¹ cadmium

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oxide,³² ultra-thin metal films,³³ copper aluminium oxide³⁴ and Sn-doped CuScO₂ thin films,³⁵ can't simultaneously satisfy the conditions for high transmittance, high conductivity and excellent chemical stability,^{30–35} limiting the device structural designs.

Herein, we report a high-performance PbSe QD photodetector with a broad spectral range up to 4.2 μm at room temperature for the first time. First, we developed a novel ligand-free chemical bath deposition (CBD) method to grow thick lead-salt QD films directly onto a substrate at ambient atmosphere.²⁴ Due to the film being ligand-free, high-temperature post-annealing, rather than control of the growth temperature, was employed to adjust the QD size and spectral response region, as well as to efficiently passivate the trap states and to improve the QDs crystalline quality. Second, our recent results indicated that indium doped cadmium selenide (CdSe:In) films can simultaneously exhibit a low sheet resistance and a high optical transmission over a broad spectral range, from visible to MWIR (9 μm). Such broadband transparent and conductive properties combined with excellent chemical stability make the CdSe:In a promising candidate for MWIR transparent conductive electrodes. Furthermore, the 4.95 eV electron affinity (EA) of CdSe readily allows for CdSe:In to form a good ohmic contact with most of the n-type semiconductors, such as CdS.³⁶ In addition, low-cost thermal co-evaporation could be employed to grow CdSe:In on any suitable substrates such as calcium fluoride (CaF₂), quartz, silicon wafer *etc.*, since it allows for an accurate control of the film composition and thickness, facilitating the activation of dopants.

The synthesized PbSe-QD films and CdSe:In electrodes were studied by High-Resolution Scanning Electron Microscopy (HRSEM), Fourier Transform Infrared Spectroscopy (FTIR) and Hall effect measurement systems. PbSe-QDs/CdS heterojunction photodetectors were grown on MWIR transparent conductive CdSe:In/silicon substrates. Temperature-dependent spectral response and blackbody radiometric measurements were also performed. The maximum room-temperature peak responsivity and specific detectivity of $0.36 \pm 0.04 \text{ A W}^{-1}$ and $(8.5 \pm 1) \times 10^8 \text{ cm Hz}^{1/2} \text{ W}^{-1}$ with a 4.2 μm cutoff were achieved after the photodiode was annealed at 673 K for 0.5 h in air. In addition, temperature-dependent MWIR spectral response showed an abnormal performance decrease below 200 K, which suggests that band alignment transition could be a possible cause for this phenomenon.

2 Experimental details

All chemicals used in the experiment were analytic grade reagents without further purification. A structural scheme for PbSe-QDs/CdS heterojunction photodiode is shown in Fig. 1.

2.1 Materials growth

CdSe:In infrared transparent conductive films were synthesized on cleaned Si substrates by co-evaporation of CdSe, indium and selenium in an ultrahigh vacuum thermal evaporation chamber

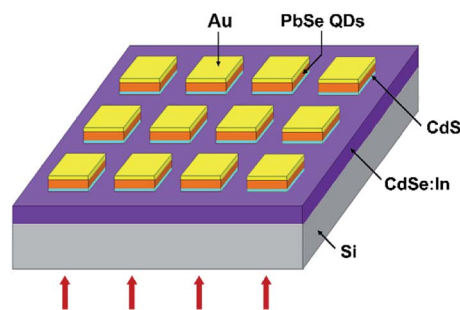


Fig. 1 Structural scheme for a PbSe-QDs/CdS/CdSe:In/Si heterojunction photodiode.

at 2×10^{-6} Pa. The CdSe:In films of 1.2 μm thicknesses were deposited at 623 K substrate temperature with a constant In/CdSe flux ratio of 0.6% by maintaining temperatures of respective cells at 1143 K and 883 K for 1.5 hours respectively.

CdS films were grown on CdSe:In covered Si substrates by chemical bath deposition (CBD). An aqueous precursor was prepared by mixing 8 mM cadmium acetate ($\text{Cd}(\text{CH}_3\text{CO}_2)_2$), 8 mM ammonium acetate ($\text{NH}_4\text{CH}_3\text{CO}_2$), 12 mM thiourea ($\text{CH}_4\text{N}_2\text{S}$), and 2 M ammonium hydroxide ($\text{NH}_3 \cdot \text{H}_2\text{O}$, 28%). The CdSe:In/Si substrates were immersed into the precursor with the CdSe:In surfaces facing down. The solution was then heated up and stabilized in a 353 K water bath for 1 h to obtain 300 nm thickness.

PbSe QD films were deposited from the precursor mixed with 3.0 M sodium hydroxide (NaOH), 0.25 M lead acetate ($\text{Pb}(\text{C}_2\text{H}_3\text{O}_2)_2$) and 0.25 M selenosulfate (Na_2SeSO_3) with a concentration ratio of 12 : 1 : 1. Then, the CdS/CdSe:In/Si substrates and glass substrates (as reference) were placed with the CdS surfaces facing down in the solution, mounted on a custom-designed Teflon stage at an angle of 70° with respect to the surface. The growth was carried out at 276 K for 240 h for 1.2 μm thickness.

2.2 Heterojunction photodiode fabrication

A photoresist (AZ5241E) layer was coated onto the PbSe-QDs/CdS/CdSe:In/Si film by spin-coating at 2500 rpm for 60 s followed by a soft bake at 368 K for 90 s. Then, a quadrate photoresist array with $200 \times 200 \mu\text{m}$ was patterned by using the MJB3 mask aligner UV lithography (275 W) with an exposure time of 10 s, followed by a development time of 45 s. Subsequently, the PbSe QDs/CdS array on CdSe:In layer was obtained by a wet-chemical etching and acetone lift-off. The etchant consists of equal parts of 80% H_3PO_4 , 29% HCl, and H_2O . The array was then treated by a post-annealing at 473, 573 and 673 K in air for 0.5 h, accompanied by the PbSe QDs grown on the glass substrates. The electrode pattern was then shaped by a second lithographic process, with a spin-coating at 2500 rpm for 60 s, a soft bake at 368 K for 90 s, an exposure for 4 s, a hard bake at 398 K for 90 s, UV exposure for 50 s and a development for 60 s. Then, a thick 100 nm Au electrode was deposited by thermal evaporation at room temperature for 20 min in 2×10^{-4} Pa. It should be noted that a 20 nm indium film was

deposited over CdSe:In prior to Au deposition for an ohmic contact. Finally, a set of photodiodes with Au/PbSe-QDs/CdS/CdSe:In/Si structures were fabricated after lift-off with acetone for 300 s.

2.3 Structure and morphology, optoelectronic and device characterizations

The morphologies of the synthesized thin films were characterized by a Zeiss Neon-40 EsB high-resolution field-emission scanning electron microscope (FESEM). The size and size distribution were calculated from the FESEM image by using the Nanomeasurer 1.2 software. Hall effect measurement was conducted in van der Pauw four-point probe configuration, using fresh indium contacts, in an automated EGK HEM-2000, with a magnetic induction of 0.37 T. For the MWIR PL measurement, PbSe samples were optically pumped by a Q-switched Nd:YAG pulse laser of 1064 nm wavelength. The input power was tuned and calibrated to be at 10 mW on average. The excited spontaneous photon emission from the samples were then collected by a parabolic mirror and redirected into a Bruker IFS-66v Fourier transform infrared spectroscopy. A cryogenic-cooled InSb detector was then used to detect and resolve the frequency dependent signal information. The transmission spectra of the samples were measured by the FTIR system with Si detector from 350–1500 nm, and deuterated triglycine sulfate (DTGS) detector from 1–10 μm . The dark current density–voltage (J – V) behaviour was measured at room temperature by applying bias from –1 to 1 V (Keithley 2400 source meter).

The detector performance at room temperature was evaluated by a home-made detectivity measurement system, consisting of an IR-508/301 blackbody (Infrared System Development), an IR-762 modulator (Infrared System Development), a SR830 Lock-in amplifier (Stanford Research System) and a ST-500 probe station (Janis Research). The blackbody temperature was set at 500 K during the measurement, with an integrated radiation intensity of $\sim 0.355 \text{ W cm}^{-2}$. A mechanical chopper was used to modulate the input signal at a frequency of 750 Hz to avoid environmental and frequency-dependent noises. SR830 lock-in amplifier was used to collect the current signal produced by the detector under zero-bias photovoltaic mode.

Temperature-dependent MWIR spectrum-resolved photo-responses were measured by Bruker IFS-66v Fourier Transform Infrared Spectroscopy system. The PbSe-QDs/CdS hetero-junction detector was mounted in a Janis VPF-100 vacuum liquid nitrogen cryostat during measurement in the range from 298 K to 160 K.

3 Results and discussion

The as-grown CBD ligand-free PbSe-QDs film synthesized at 276 K appears to be highly adhesive, light-brown in color, smooth surface, uniform and reflecting. The side-view FESEM image of the as-grown film shown in Fig. 2(a) reveals that a uniform, crack-free, glassy close-packed PbSe-QDs film with 1.2 μm

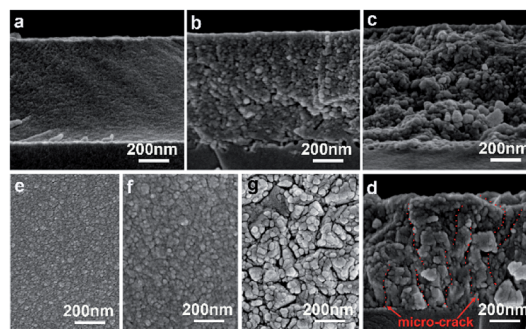


Fig. 2 Top- and cross-side view FESEM images of (a) as-grown, (b) and (e) 473 K, (c) and (f) 573 K, (d) and (g) 673 K annealed ligand-free PbSe-QDs synthesized from low-temperature CBD method on glass substrates. Red dotted lines indicate micro-cracks.

thickness, was obtained at a 5 nm h^{-1} growth rate. The film is made up of the accumulation of individual spherical dots of 6–7 nm in diameter without any voids, and the dot size is constant throughout the thickness of the film. With increasing post-annealing temperatures, the color of PbSe-QDs films gradually deepens to brown, then brownish-black, and finally to grey. At the same time, the transparency quickly declines at annealing temperatures higher than 473 K. Top- and side-view FESEM images shown in Fig. 2(b)–(g) display the morphological evolution of ligand-free PbSe-QDs films with increasing annealing temperatures. Based on the size and distribution calculated from the Nanomeasurer 1.2 software, it is clear the QDs size continuously increases from $6 \pm 1 \text{ nm}$ to $12 \pm 5 \text{ nm}$, $17 \pm 4 \text{ nm}$ and $25 \pm 2 \text{ nm}$, respectively, and the size distribution becomes narrow, as shown in Fig. 2(b)–(d). Meanwhile, PbSe-QDs begin forming multi-QDs aggregates with increasing annealing temperature, resulting in an increased surface roughness. Furthermore, the boundaries of the multi-QDs aggregates become more obvious with the increase of annealing temperature, and eventually develop into micro-cracks (Fig. 2(d) and (g)) when the annealing temperature reaches 673 K. Annealing temperatures beyond 673 K result in increased micro-crack formation, introducing a large number of defect states and negatively impacting device performance. For this reason, 673 K was found to be the upper limit of suitable annealing conditions for our samples. Additionally, a slight increase in thickness with increasing temperature is observed from side-view images.

During annealing, besides evaporation of excess Se atoms (Se-rich, p-type), heat activates Pb and Se atomic migration by providing enough energy to break Pb–Se bonds, subsequently resulting in grain growth and recrystallization.^{37–39} As a thermodynamic spontaneous process, the grain growth occurs to reduce the total Gibbs free energy. Due to being ligand-free, the oriented attachment (OA) mechanism dominates the grain growth at relatively low annealing temperature, where adjacent small QDs with common crystallographic orientations combine to form larger ones.^{37,38} With increasing temperature, Ostwald ripening (OR) becomes the significant grain growth pathway, in which larger QDs are formed by an interface-reaction-limited

mass transfer from smaller particles.³⁹ It is the mixed (OA + OR) that results in the increased QDs size, the narrow size distribution, micro-crack formation and coarsening effect.

In addition, thinner surface passivation layers readily form in an oxygen-rich annealing environment due to the diffused oxygen atom's affinity for occupying Se vacancies (V_{Se}'').⁴⁰ Oxygen atoms may also react with Pb to form PbO or PbSeO₃, especially at high annealing temperatures.⁴¹ A recent study has also found the formation of ternary PbO_xSe_{1-x} structure, which may form as a result of oxygen atom incorporation.⁴² This surface oxygen passivation, which is always employed to enhance the photo-responsivity of lead-salt detectors,⁴³ is another reason for the coarsening of the film's surface when annealed at 673 K.

Room temperature photoluminescence (PL) spectra for PbSe-QD films annealed at various temperatures were shown in Fig. 3. No PL emission was detected for the as-grown film along with films annealed at temperatures lower than 373 K, indicating poor QD crystallinity. Poor crystallinity is the result of high defect densities and dislocations introduced by defects, especially concerning selenium interstitials (Se_i') and lead vacancies (V_{Pb}''). These defects will act as the radiative recombination centers, quenching the PL emission. After annealing, PL emission is clearly observed from every QDs film annealed at high temperature, indicating that the heat treatment could significantly eliminate the density of intrinsic defects and dislocations by heat-driven atom diffusion (recrystallization and grain growth), improving the PbSe-QDs' crystallinities. With increasing annealing temperature, the PL emission shows an obvious red-shift from 3.2, to 3.5 and 3.70 μm , respectively. It is well known that this red-shift results from the increased QDs size with increasing annealing temperature. The observed size from FESEM images are consistent with Hollingsworth's experimental result,⁹ in which the QD size corresponding to PL emission peak at 3.2, 3.5 and 3.7 μm should be around 12 ± 0.9 nm, 17 ± 1.9 nm and 23 ± 3 nm, respectively. The sizes generally agree with the corresponding sizes of our annealed PbSe QDs observed from FESEM images.

The PbSe-QDs films proved to be p-type conductors by Hall measurement. The as-grown PbSe QD film's Hall mobility is $0.03 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, which is nearly the same as that of hydro-thermal colloidal PbSe QD films with 6–8 nm from spin-coating

method. As the annealing temperature increases, the carrier concentration doesn't alter dramatically. However, the mobility shows a rapid increase with annealing temperature, especially at temperatures ranging from 473 to 573 K, as showing in Fig. 4.

The carrier mobility has the following relation:⁴⁴

$$\mu_p = \frac{q\tau_p}{m_p}$$

where μ_m is the hole mobility, q is the electronic quantity, τ_p is the relaxation time, and m_p is the effective mass. Because the carrier effective mass is almost constant, it is deduced that the increase of the mobility is due to the increase of the relaxation time, which is inversely related to the scattering probability. The presence of many defects existing in as-grown PbSe-QDs dramatically increases carrier scattering, leading to lower mobility. In addition, large number of boundaries between QDs result in a large ratio of interface/volume, which further exacerbates carrier scattering, worsening the mobility. After annealing, the number of defects and boundaries reduces significantly by the grain growth and recrystallization under heat-driven diffusion, leading to a reduction in carrier scattering, resulting in an elevated carrier mobility.

Fig. 5 shows the transmittance spectrum of CdSe:In film deposited on double side polished (DSP, 380 μm) Si substrate using a co-vapour deposition method. Deposition parameters, such as indium concentration and substrate temperature, are optimized to obtain the maximum conductivity as well as keep high transparency in the broad infrared region (1–10 μm). After optimizing In/CdSe beam flux (0.6%), the minimum sheet resistance $40 \Omega \text{ sq}^{-1}$ is achieved with an electron density of $7.6 \times 10^{18} \text{ cm}^{-3}$ and a mobility of $204 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$. The average transmittance of the film is 57% in the MWIR (3.0–5.0 μm) region, and more than 40% in the long-infrared (8–10 μm), as shown in Fig. 5. The 57% transmittance of CdSe:In is approximately equal to that of DSP Si substrate at the MWIR range, indicating a coupling of high infrared transparency and high conductivity simultaneously. A lower sheet resistance could be achieved by further increasing the carrier concentration to $5 \times 10^{19} \text{ cm}^{-3}$ through increasing the In/CdSe beam flux ratio. However, higher electron concentrations result in higher plasma frequencies,³⁶ limiting the optical transparency of incident light in the MWIR range. The dependency of plasma

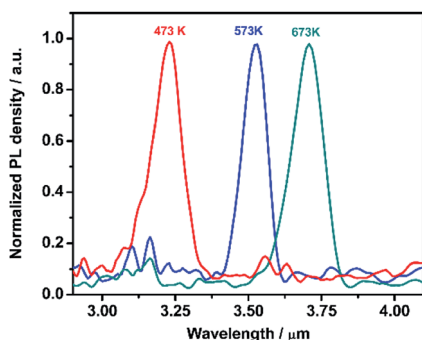


Fig. 3 Room temperature PL spectra of PbSe-QDs synthesized from CBD method annealed at different temperatures.

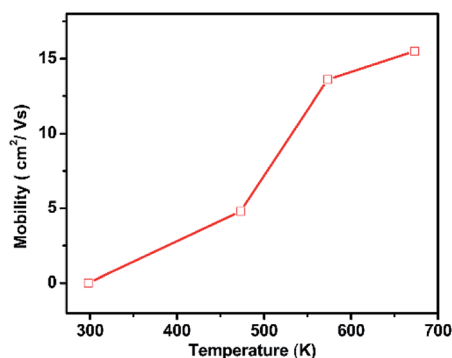


Fig. 4 Mobility of PbSe QDs annealed at different temperatures.

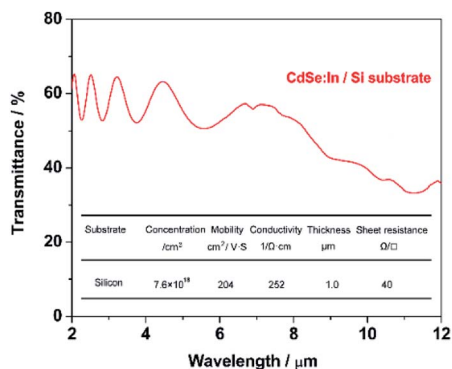


Fig. 5 Transmittance of CdSe:In on Si substrate synthesized from co-deposition method.

frequency on carrier concentration is provided by the following equation:⁴⁵

$$\omega_{\text{pe}} = \sqrt{\frac{n_e e^2}{m^* \epsilon_0}}$$

where n_e is the carrier concentration, e is the charge of the carriers, m^* is the effective mass of the carriers, and ϵ_0 is the permittivity of free space.

The dark current density–voltage (J - V) curves of the PbSe-QDs/CdS photodiodes were measured, and shown in Fig. 6. No major changes were observed in the reverse current density and rectifying ratio until annealing temperature reached 673 K. With increasing the annealing temperature to 673 K, the dark current at -0.3 V decreases 3 orders of magnitude from 6–8 mA cm⁻² to 3.5×10^{-3} mA cm⁻², resulting in a more than 144-fold increase in the rectifying ratio measured at ± 0.3 V from 1.5 (≤ 575 K) to 217.

The forward J - V characteristic of a p-n junction can be described by the exponential relation:⁴⁶

$$J \propto \exp(qV/\eta TK_b)$$

where $\exp(qV/\eta TK_b) \gg 1$, V is the bias voltage, and η is the ideality factor. Using the data from 0.10 to 0.30 V, η is determined by curve fitting to be 1.9, indicating that trap-assisted

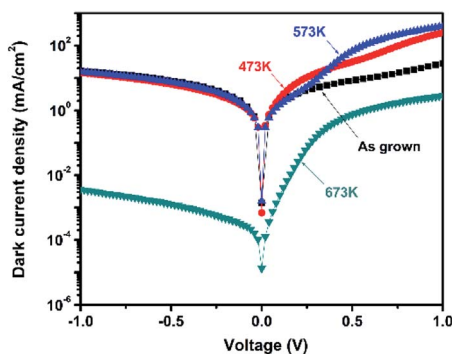


Fig. 6 Dark current density–voltage (J - V) characteristics of PbSe-QDs/CdS/CdSe:In/Si photodiodes annealed at various temperatures.

recombination dominates current, based on the Sah–Noyce–Shockley theory.⁴⁷ The trap-assisted recombination is further confirmed by the fact that dark current increases with increasing reverse bias voltage. Based on the trap-assisted recombination model, the dark current level of heterojunction device is governed by the defect density existing in PbSe-QDs films and the interfaces between PbSe and CdS. Therefore, the reduced dark current density with increasing temperature shows that higher annealing temperature helps to improve diode behaviour by significantly decreasing the defect density in the PbSe/CdS interfaces.

The responsivity R and specific detectivity D^* were obtained as described in ref. 48 by using the definition as shown below:⁴⁰

$$D^* = R \times \frac{\sqrt{A \times \Delta f}}{I_n} \text{ (cm Hz}^{1/2} \text{ W}^{-1}\text{)}$$

$$R = \frac{I_s}{P_i} \text{ (A W}^{-1}\text{)}$$

where I_s and I_n are the measured detector output signal and noise currents, A is the device detection area, Δf is the noise bandwidth, and P_i is the incident radiant power. Peak responsivity R and detectivity D^* were then calculated through a conversion factor defined in ref. 49.

Under 750 Hz chopping frequency modulation, the detector assembled with PbSe-QDs annealed at 673 K demonstrated the best R_{peak} of 0.36 ± 0.04 A W⁻¹ and peak D^* of $(8.5 \pm 1) \times 10^8$ cm Hz^{1/2} W⁻¹ at a cutoff wavelength of 4.2 μm at room temperature based on 57% MWIR transmittance, which is more than one order of magnitude larger than that of devices constructed by low-temperature annealed PbSe-QDs at their corresponding cutoff wavelengths. Measured results from the spectral response shown in Fig. 7 provides an illustration of this comparison.

Fig. 7 shows the spectral response curves for PbSe-QDs/CdS photodiodes annealed at various temperatures. It is clear that spectral response peak and cutoff wavelength move gradually to longer wavelength with increasing annealing temperature, and

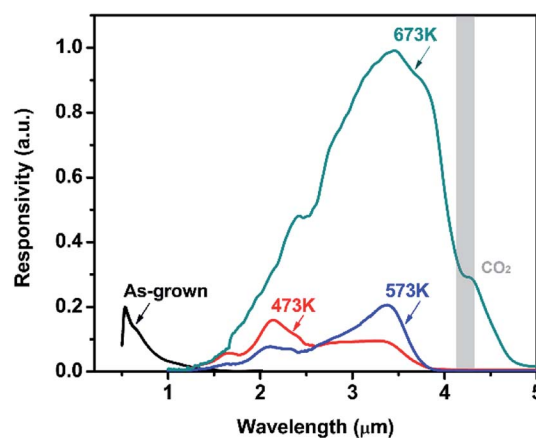


Fig. 7 Room-temperature spectral response curves of the PbSe-QDs/CdS/CdSe:In/Si photovoltaics annealed at various temperatures.

photoresponse intensity dramatically enhances after annealing temperature reaches 673 K. Only a sharp spectral response peak centered at 650 nm is observed from the photodiode without annealing, implying its potential application in QDs solar cells. As annealing at a lower temperature (≤ 573 K), the spectral response curve is composed of a sharp and broad band, covering a range from 1 to 2.5 μm (NIR) and 2.5 to 4.5 (MWIR) μm , respectively. This characteristic with obvious divisibility represents two kinds of QDs sizes in low-temperature annealed PbSe-QDs layers. Furthermore, the response peak at the NIR range dramatically decreases with increasing temperature and vanishes as the temperature reaches 673 K, suggesting that higher temperature annealing (≥ 673 K) is helpful to form larger PbSe-QDs with a uniform size distribution. At the same time, the spectral response peak at the MWIR range is in close agreement with the corresponding PL emission peak, indicating that the red-shift in spectra response curve is a result of the increased size. These results coincide with the results observed from FESEM images and PL spectra. Noticeably, the spectra response center of 3.5 μm and the cutoff of 4.2 μm of CBD PbSe/CdS are smaller than that of single-crystal bulk PbSe/CdS photodiode fabricate by MBE method,⁵⁰ indicating the MWIR quantum dot photodiode was obtained even after annealing at 673 K.

It should be noted that there is a discrepancy between PL peak (3.75 μm) and the detection cut-off (4.2 μm) for the same sample annealed at 673 K. This discrepancy was always observed from CBD-photoconductive PbSe detectors fabricated in our group,⁵⁰ which is attributed to the Moss–Burstein shift effect.⁵¹ The carrier concentration level of the prepared thin films is below 10^{19} cm^{-3} , which means they are all mild-doped semiconductor films whose Fermi level lies between the conduction and valence bands. Therefore, with no extra excitation, the measured absorption edge reflects the true energy band gap of the material. However, in terms of the photoluminescence which was measured under the high power pulsed pumping Nd-YAG laser, it will cause significant increase of the electron concentration in the conduction band under the high-energy pulse pumping. This temporal increase of electrons within the conduction band pushes the Fermi level to higher energy which may lie inside the conduction band. In this case, electrons from the top of the valence band can only be excited into the conduction band states higher than the Fermi level since all the states below the Fermi level are occupied states. Considering the parabolic distribution of density of states in the conduction band, most of those excited electrons stay in the states level higher than the conduction band edge. Thus, in the PL measurement, we observed a blue-shift in the peak position compared with the absorption edge measured under the normal FTIR transmission setup.

Although the performance of CdS/PbSe photovoltaic detectors is now 10–30 times lower than the performance (typical $1 \times 10^{10} \text{ cm Hz}^{1/2} \text{ W}^{-1}$) of our reported PbSe photoconductors (PC),⁴³ there is a strong market demand for uncooled MIR photovoltaic (PV) detectors because cameras built with a photodiode structure are cheaper, faster, smaller, and lower-power compared to photoconductive cameras. First,

photovoltaic CdS/PbSe structure can monolithically integrate on commercially available readout integrated circuit (ROIC), reducing the fabrication cost. Second, the electrode layout at photovoltaic detectors can significantly decrease the pixel size and pitch, achieving a high fill factor and a high resolution. Third, photovoltaic detectors need smaller offset voltage in comparison with its PC counterpart, reducing the weight and power. Since its performance is far below the theoretical limit of Auger recombination,⁵² there is large room to improve the performance of CdS/PbSe photovoltaic detectors.

Fig. 8 presents temperature-dependent responsivity of the photodiode with PbSe-QDs annealed at 673 K. As shown in Fig. 8, the cutoff wavelength increases at lower temperatures, however, the responsivity intensity starts to drop below 200 K. This same phenomenon was observed in our previous work about MBE-PbSe/CdS MWIR photovoltaic detector.⁵⁰

Our previous experiment suggested that the two materials of bulk PbSe and CdS could create a type II band alignment heterojunction at room temperature. The band gap energy (E_g) increases with reducing the size based on the Brus equation shown as follows⁵³

$$E_{g(\text{QD})} = E_{g(\text{bulk})} + \frac{\hbar^2 \pi^2}{8R^2} \left(\frac{1}{m_e^*} + \frac{1}{m_h^*} \right) - \frac{1.786e^2}{4\pi\epsilon_0\epsilon_r R^2}$$

where $E_{g(\text{QD})}$ is the band gap of QDs, $E_{g(\text{bulk})}$ is the band gap of bulk semiconductor, \hbar is Planck's constant, R is the radius of the QD, and m_e^* and m_h^* are the effective masses of electrons and holes, ϵ_0 and ϵ_r are permittivity of vacuum and relative permittivity.

Subsequently, PbSe-QDs have higher LUMO compared with the bottom of the conduction band for bulk PbSe. The alignment of PbSe-QDs/CdS p–n junction must be type II as shown in Fig. 9, so that the photo-excited electrons generated in PbSe-QDs can efficiently inject into CdS film. It is well known that the photo-generated carriers in PbSe films, especially at lower temperatures, is dominated by the diffusion length and absorption coefficient.⁴¹ Since both the diffusion length and absorption coefficient increase with decreasing temperature

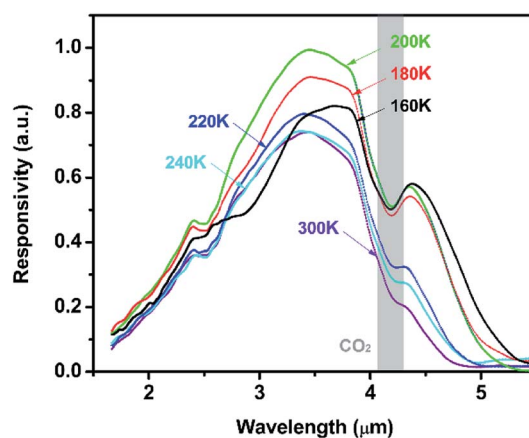


Fig. 8 Temperature dependent responsivity of the PbSe-QDs/CdS/CdSe:In/Si photovoltaics annealed at 673 K.

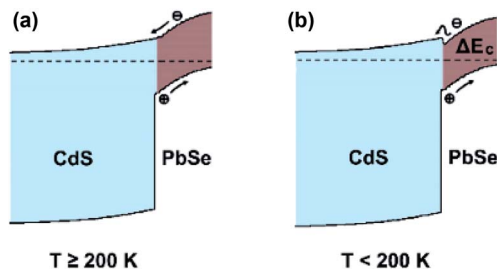


Fig. 9 Band diagram of the 673 K annealed PbSe-QDs/CdS heterojunction at (a) above 200 K and (b) below 200 K.

based on the multiband-model theory, lowering temperature increase the generated carrier density, leading to an enhanced photoresponse intensity.

At the same time, as a material with a positive temperature coefficient, the band gap of PbSe QDs (≥ 5 nm) reduces with falling temperature, which can be approximated by Preier's expression:³⁴

$$E_g(T) = 125 + \sqrt{400 + 0.25T^2}$$

This causes a redshift in cutoff wavelength at low temperatures. This is also proved by a steady red shift of cutoff wavelength from 4.2 to 4.75 μm with the temperature dropping from 300 to 160 K. However, CdS has a negative temperature coefficient which can be expressed by:³⁵

$$E_g(T) = 2.5825 - 3.06 \times 10^{-3} \times T^2 / (2156 + T)$$

Therefore, we suggest that the opposite temperature coefficient between PbSe and CdS triggers the band alignment transformation from type-II to type-I as temperature low than 200 K, as shown in Fig. 9.

Consequently, the barrier introduced by the change of conduction band offset could gradually hinder the diffusion of photo-generated electrons by a factor of $e^{-\frac{q\Delta E_c}{TK_b}}$, which is larger than the increased intensity resulting from the increase of L_e at lower temperature. This would result in photoresponse intensity dropping as temperature reaches below 200 K.

4 Conclusions

In summary, we've demonstrated a new all-solution synthetic procedure to assemble low-cost, uncooled PbSe-QDs/CdS p-n heterojunction photodiodes. A series of annealing-temperature control experiments suggest the photoresponse wavelength of the ligand-free QDs photodiodes could be tailored from visible to MWIR with increasing temperature. The photoresponse redshift is attributed to the increased QDs size, resulting from grain growth and recrystallization triggered by high temperature. At the same time, higher annealing temperature is helpful to enhance the performance of photodiodes by improving the QDs crystallinity and decreasing the interface defect density

between PbSe-QDs and CdS, which can significantly suppress the dark current and improve the photocurrent. The maximum peak responsivity R_{peak} of $0.36 \pm 0.04 \text{ A W}^{-1}$ and specific detectivity D^* of $(8.5 \pm 1) \times 10^8 \text{ cm Hz}^{1/2} \text{ W}^{-1}$ with a cutoff wavelength of 4.2 μm were achieved after annealing at 673 K for 0.5 h. In addition, an abnormal performance decrease was observed when the measurement temperature is lower than 200 K, which may be caused by a band alignment transition from type-II to type-I, as a result of a reverse temperature coefficients of the two materials. It is believed that the performance could be significantly improved through future optimizations in both structure design and material quality improvement. Furthermore, CdSe:In films fabricated by co-vapor deposition proved promising as a high infrared transparent conductive electrode, paving the way for furthering structural design diversity of next-generation MIR photodiodes.

Conflicts of interest

There are no conflicts to declare.

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