



# Article Influence of Annealing Atmosphere on the Characteristics of Ga<sub>2</sub>O<sub>3</sub>/4H-SiC n-n Heterojunction Diodes

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**Abstract:**  $Ga_2O_3/4H$ -SiC n-n isotype heterojunction diodes were fabricated by depositing  $Ga_2O_3$  thin films by RF magnetron sputtering. The influence of annealing atmosphere on the film quality and electrical properties of  $Ga_2O_3$  layers was investigated. X-ray diffraction (XRD) analysis showed a significant increase in the peak intensities of different faces of  $\beta$ -Ga<sub>2</sub>O<sub>3</sub> {(-201), (-401) and (002)}. X-ray photoelectron spectroscopy (XPS) measurement showed that the atomic ratio of oxygen increases under high-temperature annealing. Moreover, an N<sub>2</sub>-annealed diode exhibited a greater rectifying ratio and a lower thermal activation energy owing to the decrease in oxygen-related traps and vacancies on the Ga<sub>2</sub>O<sub>3</sub> film and Ga<sub>2</sub>O<sub>3</sub>-metal interface.

Keywords: gallium oxide; silicon carbide; heterojunction diodes; thermal activation energy

# 1. Introduction

Wide bandgap (WBG) semiconductors find applications in high-power transistors and light detectors. Among the more promising WBG materials, gallium oxide (Ga<sub>2</sub>O<sub>3</sub>) is uniquely transparent to visible and ultraviolet light [1–3]. It has a bandgap ranging from ~4.6 to ~4.9 eV, resulting in a high electric breakdown field strength of ~8 MV/cm. The Baliga's figure of merit (BFOM) of Ga<sub>2</sub>O<sub>3</sub> is 3400, which is roughly four times higher than that of gallium nitride [4,5]. Ga<sub>2</sub>O<sub>3</sub> has five crystalline modifications ( $\alpha$ ,  $\beta$ ,  $\gamma$ ,  $\delta$ , and  $\varepsilon$ ), among which the monoclinic  $\beta$ -phase is most stable. Metastable Ga<sub>2</sub>O<sub>3</sub> films can be obtained by thermal annealing and can be subsequently converted into  $\beta$ -Ga<sub>2</sub>O<sub>3</sub> in a relatively convenient manner. Ga<sub>2</sub>O<sub>3</sub> is natively n-doped in the range of 10<sup>16</sup>–10<sup>18</sup> cm<sup>-3</sup> due to oxygen vacancies and can be further n-doped to free carrier densities by adding Si, Sn, or Ge [6–11].

Recrystallization through thermal annealing helps reduce oxygen-related charge traps and is generally an effective method for improving the quality of Ga<sub>2</sub>O<sub>3</sub> [12,13]. Therefore, investigating the annealing process for Ga<sub>2</sub>O<sub>3</sub> is a promising research direction. Polycrystalline Ga<sub>2</sub>O<sub>3</sub> films on glass or sapphire substrates have been converted from amorphous phase through high-temperature annealing [14–16]. Hexagonal silicon carbide (4H-SiC; bandgap of ~3.26 eV) can be used as a substrate to grow  $\beta$ -Ga<sub>2</sub>O<sub>3</sub> layers [17]. Hexagonal silicon carbide (a, b = 3.10 Å and c = 10.12 Å) has a low lattice mismatch of ~2 % with Ga<sub>2</sub>O<sub>3</sub> (a = 12.33 Å, b = 3.04 Å, and c = 5.80 Å). It also and exhibits a higher thermal conductivity (~4.5 W/cm·°C) than other WBG materials such as GaN (~1.3 W/cm·°C) and Ga<sub>2</sub>O<sub>3</sub> (0.5 W/cm·°C), making it a suitable substrate for high power applications.

In this work, heterojunction diodes were fabricated by depositing  $Ga_2O_3$  on a 4H-SiC substrate and annealing the diodes under different annealing gases. The effects of the applied annealing gas on the material properties of the resulting  $Ga_2O_3$  thin films and the electrical performance of the diodes manufactured from this material are investigated.

## 2. Materials and Methods

As a substrate for the gallium oxide film, we used a n-type 4H-SiC wafer (doping concentration:  $5 \times 10^{16}$  cm<sup>-3</sup>), with a layer of epitaxially grown 4H-SiC (n-type;  $1.0 \times 10^{19}$  cm<sup>-3</sup>), as shown in Figure 1. After cleaning the SiC wafer with SPM solution (H<sub>2</sub>SO<sub>4</sub>:H<sub>2</sub>O<sub>2</sub> = 4:1), we stripped the native silicon dioxide (SiO<sub>2</sub>) layer using a buffered oxide etch (BOE). A 200-nm-thick nickel film cathode was formed on the bottom side of the SiC wafer by E-beam evaporation. After Ni deposition, the samples were annealed at 950 °C in N<sub>2</sub> for 10 min by rapid thermal annealing (RTA) for forming ohmic contacts. Gallium oxide thin films were then deposited by radio frequency (RF) sputtering of a Ga<sub>2</sub>O<sub>3</sub> (99.99% purity) target. Before deposition, the chamber was evacuated to  $2.0 \times 10^{-6}$  Torr. The films were grown on the epitaxial 4H-SiC layer under 35 mTorr at a pure argon mass flow rate of 4.6 sccm. The RF power was 140 W, and the films were deposited on room temperature. The thickness of the deposited films ranged from 100 to 250 nm. The SiC wafers, with the deposited Ga<sub>2</sub>O<sub>3</sub> films, were annealed at 800 °C for 40 min under different atmospheres (pure oxygen and nitrogen gas). An electrode was formed by deposition of 120 nm of nickel on the Ga<sub>2</sub>O<sub>3</sub> layer.



Figure 1. Structure of Ga<sub>2</sub>O<sub>3</sub>/4H-SiC heterojunction diode and fabrication flow.

## 3. Results

## 3.1. Material Properties

To compare the influence of different annealing atmospheres on the crystallinity of Ga<sub>2</sub>O<sub>3</sub> deposited on the 4H-SiC substrates, X-ray diffraction (XRD)  $\theta$ –2 $\theta$  scans were performed on the as-grown, O<sub>2</sub> and N<sub>2</sub>–annealed samples. As shown in Figure 2, all the sample sets show reflections corresponding to polycrystalline Ga<sub>2</sub>O<sub>3</sub> with a monoclinic structure from Rietveld refinement by using General Structure Analysis System (GSAS) [18,19]. All the manufactured samples give  $\beta$ -Ga<sub>2</sub>O<sub>3</sub> diffraction peaks corresponding to (–201), (–401), and (002) faces. The crystal structures remained stable. In fact, the peak intensities were further enhanced after annealing. In particular, the peak intensities corresponding to the (–201) and (–401) faces significantly increased after N<sub>2</sub> annealing. As explained in the literature, Ga and O atoms migrate under high-temperature annealing and thus help improve the crystallinity of Ga<sub>2</sub>O<sub>3</sub>. Furthermore, dangling bonds related to oxygen defects at grain boundaries can be passivated by N<sub>2</sub> annealing by incorporating nitrogen atoms at gallium or oxygen lattice sites [20,21]. Consequently, the diffraction peak intensities of the N<sub>2</sub>-annealed samples are higher than those of the other samples, as nitrogen appears to improve the crystal quality of the Ga<sub>2</sub>O<sub>3</sub> [22,23].



Figure 2. XRD spectra of samples and refinement results of Ga<sub>2</sub>O<sub>3</sub> annealed under different atmosphere.

Figure 3a shows the optical transmittance spectra of the samples for wavelengths between 200 and 400 nm. All the samples exhibit a high transmittance (over ~80 %) at wavelengths longer than 300 nm. The oxygen concentration in the Ga<sub>2</sub>O<sub>3</sub> crystals will affect the charge states, which in turn will influence such electrical parameters as bandgap and, consequently, the transmittance [24,25]. The optical bandgap is extracted from the linear part of the graph, shown in Figure 3b, for  $(\alpha h\nu)^2 = 0$ , where h $\nu$  is the photon energy, and  $\alpha$  is the coefficient of absorption.  $\alpha = \ln(100/T)/d$ , where T and d is the transmittance and thickness (120 nm) of the Ga<sub>2</sub>O<sub>3</sub> films, respectively. For the as-grown samples, the bandgap of the Ga<sub>2</sub>O<sub>3</sub> film is found to be ~5.01 eV. The bandgaps of the samples annealed under O<sub>2</sub> and N<sub>2</sub> atmosphere are ~4.91 and ~4.89 eV, respectively. The bandgap of the N<sub>2</sub>-annealed sample is close to the typically reported bandgap value of ~4.9 eV for  $\beta$ -phase Ga<sub>2</sub>O<sub>3</sub> [26].



**Figure 3.** (a) Transmittance spectra and (b)  $(\alpha h\nu)^2$ -h $\nu$  plots of Ga<sub>2</sub>O<sub>3</sub> film samples annealed under different atmospheres.

Figure 4 shows the XPS spectra of the O 1s peaks of the three different sample sets. The peaks were calibrated using C 1s at 284.6 eV, in which the O 1s peaks were fitted using two Gaussian peaks, corresponding to  $Ga_2O_3$  and  $GaO_x$  phases, respectively. After annealing in  $O_2$  and  $N_2$  atmosphere, the peak intensity of the  $GaO_x$  phase decreases, whereas that of the  $Ga_2O_3$  phase increases. The  $GaO_x$ 

peak is reported to have a connection with oxygen vacancies [26]. The magnitude of the peak intensity corresponding to the GaO<sub>x</sub> phase was reduced, from 37.5% for the as-grown sample to 20.3% and 13.6% for the O<sub>2</sub> and N<sub>2</sub>-annealed samples, respectively. This is considered to indicate a decrease in the number of defects, such as oxygen vacancies and oxygen sites. The atomic ratios of O to Ga in the samples are 1.40, 1.43 and 1.42 for as-grown, O<sub>2</sub> and N<sub>2</sub> annealed Ga<sub>2</sub>O<sub>3</sub>. These different stoichiometric ratios indicate that an increased concentration of O<sub>2</sub> in the annealing gas can somewhat raise the number of oxygen atoms in the films.



Figure 4. O 1s XPS spectra of the Ga<sub>2</sub>O<sub>3</sub> films annealed under different atmospheres.

## 3.2. Electrical Properties

Figure 5 shows the graph of  $1/C^2$  as a function of the reverse voltage bias applied to the  $Ga_2O_3/4H$ -SiC diodes. The built-in voltage ( $V_{bi}$ ) and doping concentration can be extracted from the extrapolated graph of  $1/C^2$  versus the voltage.  $V_{bi}$  is calculated from the V-axis intercept of the fitted graph. The doping concentration is derived from the slope of  $1/C^2$ –V using Equation (1).

$$\frac{1}{C^2} = \frac{2}{qA^2} \frac{\varepsilon_{Ga_2O_3} N_{Ga_2O_3} + \varepsilon_{SiC} N_{SiC}}{N_{Ga_2O_3} N_{SiC} \varepsilon_{Ga_2O_3} \varepsilon_{SiC}} (V_{bi} - V)$$
(1)

The extracted values of V<sub>bi</sub> and doping concentration of the Ga<sub>2</sub>O<sub>3</sub> thin films are 0.47, 0.86, and 1.01 V and  $9.59 \times 10^{15}$ ,  $1.62 \times 10^{16}$ , and  $2.01 \times 10^{16}$  cm<sup>-3</sup> for the as-grown, O<sub>2</sub>-annealed, and N<sub>2</sub>-annealed samples, respectively. The V<sub>bi</sub> and doping concentration values increased after annealing, because of the decrease in oxygen-related traps. The increase in the built-in voltage can be attributed to the changes in the dopant concentration and the concentration of interface states of Ga<sub>2</sub>O<sub>3</sub>.



Figure 5. 1/C<sup>2</sup>–reverse voltage plots for different diodes measured at room temperature.

Figure 6 shows the typical I–V characteristics of the fabricated Ga<sub>2</sub>O<sub>3</sub>/4H-SiC n-n diodes both in the logarithmic and linear scales. As shown in the figure, the as-grown diode has a high leakage current (~ $1.60 \times 10^{-5}$  A) and a low rectifying ratio (~ $3.0 \times 10^{3}$ ) measured at forward (3 V) and reverse biases (–3 V). The rectifying behavior of the O<sub>2</sub> and N<sub>2</sub>-annealed diodes is improved. The different samples exhibit a similar leakage current value of approximately  $8.1 \times 10^{-11}$  A. The N<sub>2</sub>-annealed diode exhibits a higher on-current when a forward voltage is applied, with a rectifying ratio of ~ $5.0 \times 10^{7}$ , which may be related to the reduced oxygen trap concentrations after annealing. The threshold voltages of the diodes are ~1.55, ~1.47, and ~1.27 V for the as-grown, O<sub>2</sub> and N<sub>2</sub>-annealed samples, respectively. The ideality factor at room temperature can be extracted from Equation (2).

$$\mathbf{I} = I_O \left[ e^{\frac{V}{\eta K_B T}} - 1 \right] \tag{2}$$

Here, *I* and *V* are the forward current and voltage, respectively,  $I_0$  is the saturation current,  $k_B$  is Boltzmann's constant, *T* is the absolute temperature, and  $\eta$  is the ideality factor. The ideality factor is significantly reduced after the annealing process; the ideality factor of the N<sub>2</sub>-annealed diode is 2.8, which is half that of the O<sub>2</sub>-annealed diode. The lower ideality factor and the higher built-in voltage of the annealed diodes are attributed to the improved crystallinity and interface properties.



Figure 6. I–V characteristics of diodes at room temperature: (a) log-scale and (b) linear curves.

The thermal activation energy ( $E_A$ ) is obtained from the ln ( $I_O$ )–1/kT plot shown in Figure 7. The graph was plotted in the temperature range of 298–523 K with a temperature step of 25 K, where  $I_O$  is the reverse saturation current at -3 V, and T and k are the absolute temperature and Boltzmann's constant, respectively. The extracted activation energy from the experimental measurements are related to trap states at the metal–Ga<sub>2</sub>O<sub>3</sub> interfaces and the barrier heights. Low activation energy values suggest a high concentration of the trap states at the interface, which results in increased trap-assisted tunneling or thermionic emission probabilities across the barrier. As shown in Figure 6, the extracted activation energy of the devices increases after annealing. In particular, the activation energy of the N<sub>2</sub>-annealed sample (0.504 eV) is twice that of the as-grown sample. The improved rectifying ratio of the N<sub>2</sub>-annealed diode is also attributed to the increased activation energy.



Figure 7.  $ln(I_O)-1/kT$  curve for activation energy values derived from the increasing temperature.

# 4. Conclusions

We fabricated polycrystalline  $\beta$ -Ga<sub>2</sub>O<sub>3</sub>/4H-SiC heterojunction diodes annealed under different gas atmospheres (O<sub>2</sub> and N<sub>2</sub>). The material and electrical properties of the diodes were investigated to understand the effects of the different annealing gases on the device characteristics. X-ray diffraction peaks corresponding to the different faces of  $\beta$ -Ga<sub>2</sub>O<sub>3</sub> {(-201), (-402), and (002)} were observed to significantly increase, while the bandgap somewhat decreased to ~4.9 eV after annealing. The post-annealing decrease in the GaO<sub>x</sub> peak intensity indicates a decrease in the number of oxygen vacancies. With regard to the electrical properties, the leakage current decreased nearly 1000 times after annealing. To summarize, the N<sub>2</sub>-annealed sample exhibited higher rectifying ratio and built in voltage, decreased threshold voltage, lower ideality factor, and higher activation energy than the as-grown and O<sub>2</sub>-annealed samples. Therefore, we conclude that the performance of N<sub>2</sub>-annealed diodes at high temperatures is more stable due to higher activation energy compare with built-in voltage due to a lower concentration of trap states.

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Conflicts of Interest: The authors declare no conflicts of interest.

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