



Article Comparison of Sb₂O₃ and Sb₂O₃/SiO₂ Double Stacked pH Sensing Membrane Applied in Electrolyte-Insulator-Semiconductor Structure

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Abstract: In this study, electrolyte-insulator-semiconductor (EIS) capacitors with Sb_2O_3/SiO_2 double stacked sensing membranes were fabricated with pH sensing capability. The results indicate that Sb_2O_3/SiO_2 double stacked membranes with appropriate annealing had better material quality and sensing performance than Sb_2O_3 membranes did. To investigate the influence of double stack and annealing, multiple material characterizations and sensing measurements on membranes including of X-ray diffraction (XRD), X-ray photoelectron spectroscopy (XPS), and scanning electron microscopy (SEM) were conducted. These analyses indicate that double stack could enhance crystallization and grainization, which reinforced the surface sites on the membrane. Therefore, the sensing capability could be enhanced, Sb_2O_3/SiO_2 -based with appropriate annealing show promises for future industrial ion sensing devices.

Keywords: Sb₂O₃/SiO₂ double stack; pH sensing; silicate; crystallization; reiability

1. Introduction

In the 1980s, an enzyme sensor based on an electrolytic insulator semiconductor [1,2] (EIS) capacitive structure was demonstrated, which had a very simple structure. These EIS sensors consist of a dielectric layer [3] and are deposited on a silicon substrate [4]. The EIS sensor is immersed in a sample solution to form a capacitance structure and the capacitance of the structure changes [5] depending on the pH value of the contact liquid [6]. As the pH value changes, the flat band capacitance [7] of the EIS chip moves along the voltage axis [8]. Through the change of capacitance [9], the corresponding voltage change [10] can be measured according to the pH value (voltage-capacitance method). In recent years, several kinds of high-k dielectrics [11] have been proposed as sensing membranes such as WO₃ [12], Y₂O₃ [13], Pr₂O3 [14], and HfO₂ [15] to substitute SiO₂ [16] as a sensing membrane due to its low cost, compatibly with silicon, and compact size [17]. However, the dangling bonds and traps in high-k materials may cause great trouble in the future, and these peak applications in sensing devices [18]. SiO₂ as the sensing membrane



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Copyright: © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). still has some advantages such as better crystallization and lower defect density [19]. Therefore, it is possible to have dual advantages by combing SiO₂ and other dielectrics. In this work, double layer Sb₂O₃/SiO₂ [20] sensing films were fabricated. Post-RTA in different temperatures (400 °C, 500 °C, 600 °C) was applied in EIS structure [21]. The sensor performance including sensitivity [22], hysteresis [23], and drift, rate [24], were measured to find the optimal annealing condition [25]. To examine the improvements of material properties, X-ray diffraction (XRD), X-ray photoelectron spectroscopy (XPS), and atomic force microscope (AFM) [26,27] were used to examine the crystalline structure, chemical bindings, and surface roughness. The results indicate that double stack and annealing could enhance crystallization and suppress silicate-related defects [28,29].

2. Experimental

The EIS structures were fabricated on 4-in n-type (100) Si wafers, which have a resistivity of 5–10 Ω -cm. The standard Radio Corporation of America (RCA) cleaning process was performed on the chips. The chip was first immersed in ethanol for 5 min and soaked in DI water for 5 min. Followed by immersion in acetone for 5 min, cleansed in DI water for 5 min, and then isopropyl alcohol was used to remove the organic contaminates. Then, the samples were dipped into 1% hydrofluoric acid to remove native oxide from the surface. A SiO₂ film of 50 nm in thickness was grown on the wafer by dry thermal oxidation. Then, a 50-nm Sb₂O₃ was deposited by radio frequency (RF) reactive sputtering on an n-type silicon wafer and a SiO₂ deposited wafer, respectively. During the RF sputtering, the Sb₂O₃ target was used in the ambient of Ar: O₂ at 20:5. The RF power and pressure were 100 Watt and 20mTorr, respectively. Next, the samples were annealed at different temperatures (400 °C, 500 °C, and 600 °C) by rapid thermal annealing (RTA) in O_2 ambient for 30 s. The back-side contact of the Si wafer was deposited by Al film with 300 nm-thick. Then, the sensing area was defined by a standard photolithography process using a photosensitive epoxy, SU8-2005 of Micro-Chem Inc. (Adel, GA, USA). Finally, the samples were fabricated on the copper lines of the printed circuit board (PCB) by silver gel. As for the sensing dielectric dimensions, the SiO² film of 50 nm in thickness was grown on the wafer by thermal dry oxidation. Then, a 50-nm Sb₂O₃ was deposited by radio frequency (rf) reactive sputtering on the SiO₂ film. The sensing area is 0.01 cm^2 . The oxide thicknesses were determined by high-frequency (100 kHz) capacitance-voltage (C-V) measurements. The expected error range of the oxide thicknesses for values is about 5–10%. An epoxy package was used to separate the EIS structure and the copper line. The detailed Sb_2O_3/SiO_2 EIS structure is illustrated in Figure 1.



Figure 1. Cont.



Figure 1. (a) The Sb₂O₃/SiO₂ EIS structure. (b) The Sb₂O₃ sensing membrane applied to the EIS structure with RTA in O₂ ambient.

Figure 2a,b show the XRD patterns of the Sb₂O₃ layer and Sb₂O₃/SiO₂ layer with various rapid thermal annealing conditions in O₂ treatment. Obviously, in Figure 2a,b three diffraction peaks (222), (400) and (440) can be observed at 27.67°, 32.08°, and 45.96° can be observed for the two types of samples, respectively. When the temperature increased to 500 °C, the peak (222), (400) and (440) intensity increased. Due to enhancement of the crystalline structure during annealing, the peak intensity increased. The two types of samples annealed at a temperature of 500 °C exhibited the strongest peak of (222) and (440) among all of the samples. Moreover, compared with the single-layer (Sb₂O₃), the double-layer structure had a narrow full width at half maximum (FWHM), indicating the grain was larger. In addition, the peaks of the double stacked samples were stronger indicative of strong crystallization. The results reveal that the double-layer (Sb₂O₃/SiO₂) had better crystallization and lower defect density.

To investigate the chemical-binding states in the Sb₂O₃/SiO₂ and Sb₂O₃ sensing membranes, XPS was used. The O 1s spectra for the as-deposited Sb₂O₃ and Sb₂O₃ annealed films are shown in Figure 3a with their appropriate three-peak curve-fitting lines. In the three sets of spectra, the O 1s signal comprised three peaks at 532.9, 531.2 and 530.6 eV, which are SiO₂, Sb-silicate, and Sb₂O₃ respectively. Compared to the single-layer (Sb₂O₃), the intensity of the double-layer (Sb₂O₃/SiO₂) film had a stronger Sb₂O₃ peak and weaker silicate peak, especially for the sample annealed at 500 °C. This was due to the fact that the SiO₂ layer can block Sb atoms from diffusion into the silicon to form silicate. After post-RTA annealing treatment at 500 °C in O₂, the O 1s peak corresponding to Sb–silicate became weaker for the two types of samples indicative of improvement of crystallization and low defect density.



Figure 2. XRD patterns of the (a) Sb_2O_3 and (b) Sb_2O_3/SiO_2 films annealed at various temperatures in O_2 ambient for 30 s.



Figure 3. The O 1s XPS results of (**a**) Sb_2O_3 film, (**b**) Sb_2O_3/SiO_2 films annealed at various temperatures in O_2 ambient for 30 s.

Furthermore, AFM was used to analyze the surface roughness. shown in Figure 4. The root means square (RMS) values of the Sb₂O₃ films and for the as-dep sample and the samples with RTA annealing from 400 °C to 600 °C were 0.250 nm, 0.267 nm, 0.292 nm, and 0.216 nm while the root means square (RMS) values of the Sb₂O₃/SiO₂ for the as-dep sample and the samples with RTA annealing from 400 °C to 600 °C to 600 °C were 0.261 nm, 0.276 nm, 0.311 nm, and 0.242 nm. AFM was used to analyze the grainization effect of films. Consistent with XRD analysis, the double-layer structure has a narrower full width at half maximum, larger grains can make the surface roughness higher. In the case of 500 °C, there was the optimal lattice structure. As the annealing temperature approached the melting point, the crystal grains gradually disappear and the roughness began to decrease. When the temperature reached 600 °C, the roughness value decreased. This may be due to the fact there were many defects in the film and the lattice structure was destroyed.

To calculate the sensitivity and linearity of the Sb₂O₃ and Sb₂O₃/SiO₂, C-V curves of the samples with various annealing conditions were graphed. The sensitivity values of the as-dep Sb₂O₃ film and the Sb₂O₃ films annealed at 400 °C, 500 °C and 600 °C were 42.25 mV/pH, 45.24 mV/pH, 51.96 mV/pH, and 34.44 mV/pH, respectively. The linearly values of the above four samples were 96.26%, 99.24%, 98.10%, and 82.69%, respectively. The sensitivity values of the as-dep Sb₂O₃/SiO₂, film and the Sb₂O₃/SiO₂, films annealed at 400 °C, 500 °C and 600 °C were 49.42 mV/pH, 49.77 mV/pH, 54.01 mV/pH, and



(c)

38.80 mV/pH, respectively. The linearly values of the above four samples were 99.41%, 98.89%, 99.03%, and 95.42%, respectively.

Figure 4. Cont.







Figure 4. Cont.







(h)

Figure 4. AFM images of Sb₂O₃ film film (a) As-dep (b) 400 °C (c) 500 °C (d) 600 °C surface after RF sputter in Ar:O₂ = 20:5 annealing at various temperatures in O₂ ambient for 30 s. AFM images of Sb₂O₃/SiO₂ film (e) As-dep (f) 400 °C (g) 500 °C (h) 600 °C surface after RF sputter in Ar:O₂ = 20:5 annealing at various temperatures in O₂ ambient for 30 s.

Figure 5a–d show the C-V curves of the as-dep Sb₂O₃ film, the Sb₂O₃ films annealed at 500 °C the as-dep Sb₂O₃/SiO₂, film and the Sb₂O₃/SiO₂, film annealed at 500 °C. Consistent with material characterizations, the Sb₂O₃/SiO₂ sample annealed in the temperature of 500 °C possessed the highest sensitivity since the annealing at an appropriate temperature could compensate oxygen vacancies to improve the sensing capability. Moreover, with the

addition of the SiO_2 layer, the trapping charges or dangling bonds could be reduced since the thermal grown SiO_2 layer was near-perfect dielectric with fewer defects.

To investigate the hysteresis effects of the membrane, the tested samples were immersed in buffer solutions with different pH values in an alternate cycle (pH 7, pH 4, pH 7, pH 10, and pH 7). The submerging time was five minutes for each solution. We subjected the above samples to a pH loop of $7\rightarrow 4\rightarrow 7\rightarrow 10\rightarrow 7$ over 30 min. The hysteresis voltage was defined as the gate voltage difference between the initial and the terminal voltages measured in the above pH loop. The interior sites of defects could react with the ions existing in the tested solution and thus generate hysteresis response.



Figure 5. Cont.



Figure 5. The C-V curves and the extracted sensing data of (**a**) the as-dep Sb₂O₃ film, (**b**) the Sb₂O₃ films annealed at 500 °C, (**c**) the as-dep Sb₂O₃/SiO₂, and (**d**) the Sb₂O₃/SiO₂, film annealed at 500 °C.

Figure 6a shows the hysteresis voltage of the Sb₂O₃ for the as-dep sample and the sample annealed at temperatures from 400 °C, 500 °C, and 600 °C were 47.61 mV, 25.58 mV, 13.82 mV, and 61.98 mV, respectively. Figure 6b shows the hysteresis voltage of the Sb₂O₃/SiO₂ samples for the as-dep sample and the sample annealed at temperatures from 400 °C, 500 °C, and 600 °C were 31.91 mV, 24.42 mV, 12.35 mV, and 36.12 mV, respectively. The Sb₂O₃/SiO₂ sample annealed at a temperature of 500 °C had the lowest hysteresis deviation. The SiO₂ layer could reduce charge loss and increase the reliability of



the interface. Annealing in O_2 ambient could repair the dangling bonding connection and fill the traps in the film.

Figure 6. (a) Hysteresis of Sb₂O₃ sensing membrane with various RTA temperatures in O₂ ambient during the pH loop of $7\rightarrow 4\rightarrow 7\rightarrow 10\rightarrow 7$ over a period of 30 min. (b) Hysteresis of Sb₂O₃/SiO₂ sensing membrane with various RTA temperatures in O₂ ambient during the pH loop of $7\rightarrow 4\rightarrow 7\rightarrow 10\rightarrow 7$ over 30 min.

To investigate the drift rate of the membrane for long-time reliability, the tested samples were immersed in pH7 buffer solutions and the submerging time was twelve hours. We can use the model of gate voltage drift of pH-ISFET to describe a hopping and/or trap-limited transport mechanism. 'Drift coefficient' is a parameter that describes the long-term stability of a pH sensor. The curves of drift effect of the Sb₂O₃ and Sb₂O₃/SiO₂ sensing film were measured in pH 7 buffer solution for 12 h as shown in Figure 7a,b respectively. Figure 6a shows the drift rate values of the as-dep Sb₂O₃ sample and the Sb₂O₃ samples annealed at 400 °C, 500 °C, and 600 °C were 35.43 mV/h, 8.11 mV/h, 7.25 mV/h, and 12.71 mV/h, respectively. Figure 6b shows the drift rate of the as-dep Sb₂O₃/SiO₂ sample and the Sb₂O₃/SiO₂ sample annealed at 400 °C, 500 °C and 600 °C were 12.56 mV/h, 7.15 mV/h, 6.48 mV/h, and 10.31 mV/h, respectively. The Sb₂O₃/SiO₂ sample annealed in O₂ treatment at a temperature of 500 °C had the lowest hysteresis deviation.



Figure 7. (a) Drift voltage of Sb_2O_3 sensing membrane annealed with various RTA temperatures in O_2 ambient, then dipped in pH 7 buffer solution for 12 h. (b) Drift voltage of Sb_2O_3/SiO_2 sensing membrane annealed with various RTA temperatures in O_2 ambient, then dipped in pH 7 buffer solution for 12 h.

3. Conclusions

In this study, double stacked Sb₂O₃/SiO₂ membranes in EIS structures were fabricated. The double stacked membrane annealed at 500 °C had a higher sensitivity, higher linearity, lower hysteresis voltage, and lower drift rate. Multiple material characterizations indicate that double stack structures and annealing could enhance the crystallization and reduce the defects. Sb₂O₃/SiO₂-based with appropriate annealing show promises for future biomedical sensing devices.

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