

Evaluation of Nanostructured NiS₂ Thin Films from a Single-Source Precursor for Flexible Memristive Devices

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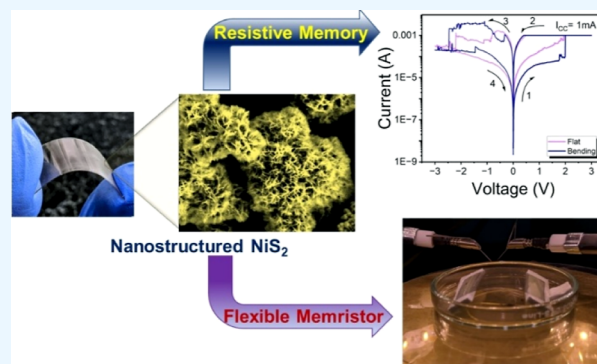


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ABSTRACT: Herein, we report the first demonstration of a single-step, *in situ* growth of NiS₂ nanostructures from a single-source precursor onto a flexible substrate as a versatile platform for an effective nonvolatile memristor. The low temperature, solution-processed deposition of NiS₂ thin films exhibits a wide band gap range, spherical-flower-like morphology with high surface area and porosity, and negligible surface roughness. Moreover, the fabricated Au/NiS₂/ITO/PET memristor device reveals reproducible bipolar resistive switching (RS) at low operational voltages under both flat and bending conditions. The flexible device shows stable RS behavior for multiple cycles with a good memory window ($\sim 10^2$) and data retention of up to 10^4 s. The switching of a device between a high-resistance state and a low-resistance state is attributed to the filamentary conduction based on sulfur ion migration and sulfur vacancies and plays a key role in the outstanding memristive performance of the device. Consequently, this work provides a simple, scalable, solution-processed route to fabricate a flexible device with potential applications in next-generation neuromorphic computing and wearable electronics.



1. INTRODUCTION

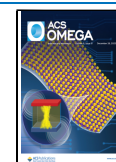
Flexible electronic devices with robust bending stability have attracted much attention due to their applications in bionic sensors,^{1,2} memory,³ and human–machine interfaces.⁴ Moreover, the advances in sensor technology, intelligent algorithms, and integrated circuits have propelled human–computer interaction to the forefront of innovation.^{5,6} Consequently, there is a need for the development of flexible electronic systems with low energy consumption and potential scope in the areas of computation. In conventional computing systems, the major cause for energy consumption arises from the von Neumann architecture where the computing units are separated from memory units leading to huge energy loss while shuffling data.⁷ The new computing paradigm based on memristor with in-memory computing is an alternative to the von Neumann computing system and a prominent candidate for the next generation of memory and neuromorphic computing applications.^{8,9} The resistive switching (RS) phenomena of the memristive device with metal–insulator–metal (MIM) structure rely on the change in the resistance of the active layer material sandwiched between the two metal electrodes.^{10,11} Under the influence of an applied electric field, the resistance of the active layer material can alternate between the high-resistance state (HRS) and the low-resistance state (LRS) to store data.¹² Unfortunately, the majority of memristors are built on conventional silicon substrates which are not suitable for flexible electronic applications. As a result, it is crucial to investigate novel nanomaterials that combine

state-of-the-art flexible memristors with high performance, nonvolatile RS behavior along with scalability and industrially acceptable processing.

The low-dimensional (layered) transition metal dichalcogenides (TMDCs) are attractive candidates for future electronics due to their tunable band gap, transparency, flexibility, and ability to downscale to atomic level in material size and thickness.¹³ They are particularly interesting as switching materials for memristor devices and future low-power neuromorphic computing. Compared with commonly investigated metal oxides, metal chalcogenides (S, Se, and Te) feature a shallow energy landscape due to softer chemical bonds (i.e., lower cohesive energy) and the physical and chemical origins of electronic transitions in chalcogenide-based materials can be more energy efficient.¹⁴ In addition, TMDCs offer easily accessible surfaces that can be used to control electronic transitions by external inputs.¹⁵

Moreover, the large-scale deposition of nanostructured TMDCs on flexible substrates has never been straightforward due to limitations in current deposition methods such as

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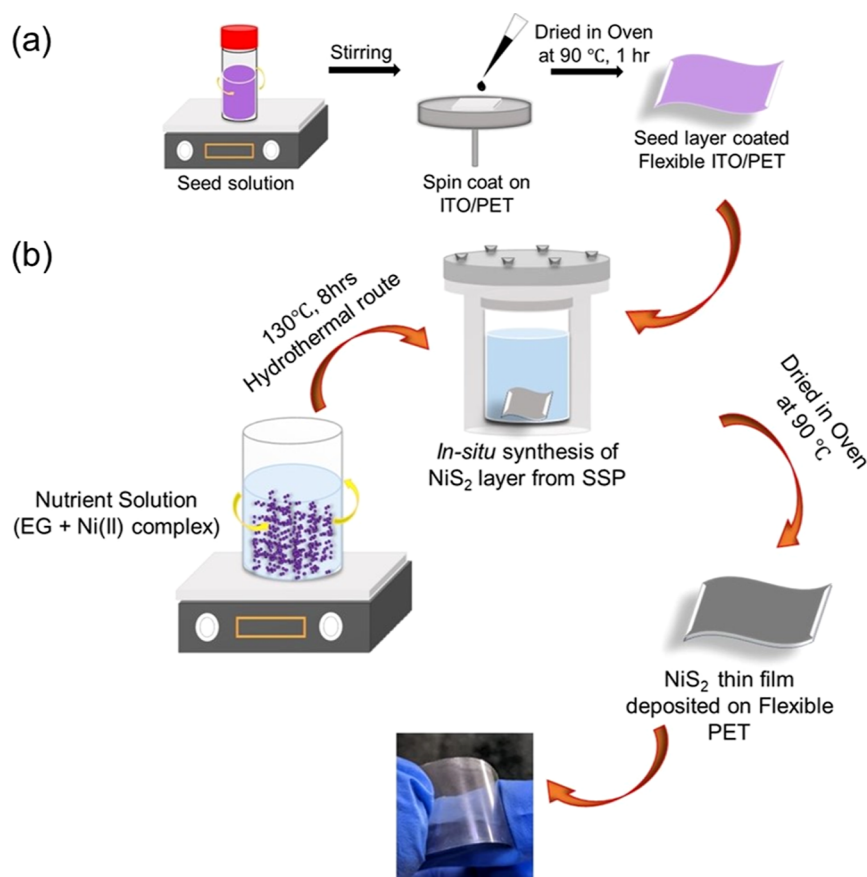


Figure 1. Graphical illustration of the *in situ* growth of NiS₂ layer using SSP on flexible ITO/PET substrate. (a) Seed layer deposition by spin coat technique and (b) growth of NiS₂ using the solvothermal route.

exfoliation and chemical vapor deposition (CVD). The promising performance of flexible memristors has been demonstrated on a few layers of TMDCs using an assembly of small flakes exfoliated from crystals and transferred onto target substrates,^{16,17} a process that is not compatible with large-scale manufacturing. CVD¹⁸ and physical epitaxy¹⁹ show promising scalability; however, their high deposition temperatures are not compatible with the thermal budget of most of the potential flexible substrates. Although a film transfer process could be adapted to integrate those films onto flexible substrates, the size is limited to a centimeter scale.²⁰ Solution processable methods have a number of key distinguishing features. It enables low-temperature processing, easy stoichiometric modulations, eco-friendly, high throughput production, and good compatibility with roll-to-roll manufacturing and printing techniques.²¹ Specifically, the low thermal solution processable technique avoids the deformation of polymer substrates like PET and polyimides. This makes solution-processing techniques key to the next generation of flexible and wearable electronics. Many of the potential flexible electronics are limited by the lack of availability of mass manufacturing of low-dimensional chalcogenides. Among these methods, the *in situ* solvothermal approach serves as the better alternative for the synthesis of metal sulfide nanostructures from single-source precursors (SSP), which have the potential advantages of relatively low cost, high purity, and controlled morphology.²² Using single-source precursors has its own advantages over those of its dual-source counterparts. The outstanding properties of the precursors such as ease of handling and

compatibility with solvents enable binder-free deposition without using toxic stabilizers and reducing agents. Furthermore, the earth-abundant nickel sulfide has attracted much interest among the family of metal sulfides due to its various stoichiometries²³ (NiS, Ni₃S₂, Ni₃S₄, Ni₆S₅), high electron mobility and unique metal–insulator properties.²⁴ Consequently, it is widely studied as an electrode material for energy storage devices such as supercapacitors,²⁵ and also a potential competitor for silicon in thin film solar cells.²⁶ However, the role of nanostructured nickel disulfide (NiS₂) as the RS material for flexible resistive random access memory (ReRAM) applications remains unexplored.

In this work, we demonstrate the novel, single-step, *in situ* growth of NiS₂ nanostructures on a flexible substrate from the single-source precursor [Ni{S₂P(OPr)₂}₂] at low temperatures using a solution-processable, solvothermal route. The as-fabricated NiS₂ nanostructure-based flexible device acts as an excellent RS memory. In particular, the developed device exhibits bipolar RS with a stable memory window under the applied direct current (DC) voltage sweep including better-switching repeatability and mechanical stability. This work opens up new avenues for simple, scalable, and cost-effective devices for flexible electronics.

2. MATERIALS AND METHODS

2.1. Materials. Nickel chloride (NiCl₂·6H₂O) and phosphorus pentasulfide (P₂S₅) were purchased from Sigma-Aldrich. Toluene (99.5%, bp 110.6 °C), propanol (Pr-99%, bp 97 °C), benzene, and ethylene glycol were procured from S D

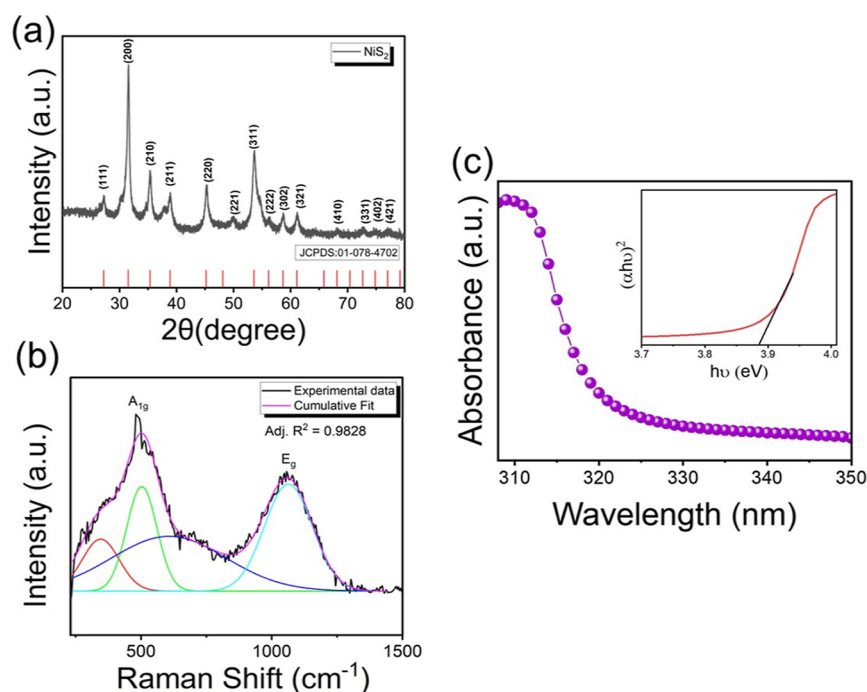


Figure 2. (a) XRD and (b) Raman spectra of NiS₂. (c) Absorption spectra with a band gap analysis of NiS₂ on a flexible substrate (ITO/PET).

Fine-chem Limited. All chemicals were of analytical grade. The solvents (toluene and benzene) were dried and distilled by the standard methods before use. The reactions were performed in Schlenk and vacuum lines, and precautions were taken to avoid moisture during experimental manipulations. ITO/PET (indium tin oxide layer coated on polyethylene terephthalate) was purchased from Sigma-Aldrich.

2.2. Synthesis of Single-Source Precursor. The single-source precursor [Ni{S₂P(OPr)₂}]₂ was synthesized from a metathesis reaction of ammonium salts of *O,O'*-dipropyl dithiophosphates and NiCl₂·6H₂O in a 2:1 molar ratio.²⁷ The purple solid was further characterized by IR, ¹H, and ³¹P {¹H} NMR spectroscopy. Yield: (0.96 g) 88.88%; IR (KBr): 983b, 856s, 752s, 642s, 547s cm⁻¹; {¹H} NMR (CDCl₃, 298k, δ ppm): 4.19 (dd, [4H], *o*-CH₂), 1.78 (m, [4H], -CH₂CH₃), 1.00 (t, [6H], -CH₃); ³¹P (CDCl₃, 298k, δ ppm): 92.95 (s).

2.3. Direct Growth of NiS₂ Thin Films on Flexible Substrate Using SSP. *In situ* growth of nickel disulfide on the flexible ITO/PET substrate was performed under the low temperature, solution-processable, solvothermal route as illustrated in Figure 1. Initially, the seed solution was prepared by dissolving [Ni{S₂P(OPr)₂}]₂ complex (50 mg) in 1 mL of benzene, and the solution was spin-coated at 3000 rpm onto the cleaned ITO/PET substrate. Further, the seed layer-coated ITO/PET substrate was dried in an oven at 90 °C for 1 h (Figure 1a). Next, the nutrient solution was prepared by dispersing 0.161 g (0.7 mmol) of [Ni{S₂P(OPr)₂}]₂ in 20 mL ethylene glycol, and the mixture was stirred continuously for 1 h at R.T. After stirring, the suspension was transferred to a 25 mL Teflon liner and the seed layer-coated ITO/PET was placed upside down inside the solution as shown in Figure 1b. The autoclave reactor was packed tightly and moved to the furnace, and the temperature was maintained at 130 °C for 8 h. After the reaction, the autoclave was allowed to cool to room temperature. Further, the deposited film was rinsed twice with deionized water. Finally, the obtained film was dried in a vacuum oven at 80 °C for 2 h.

2.4. Fabrication and Measurements of Au/NiS₂/ITO/PET Flexible Memristor. The device was fabricated using the solvothermal grown NiS₂ as the active layer and the flexible ITO-coated PET (2 × 2 cm²) acts as the bottom electrode. The thickness of the active layer was measured to be ~8.01 μm. Finally, the top circular Au electrodes (thickness ~100 nm and 0.2, 0.5- and 1 mm radius) were patterned on the NiS₂/ITO layer by the shadow mask-assisted thermal evaporation technique. Overall, the Au/NiS₂/ITO/PET device was fabricated by a simple and cost-effective route. The electrical measurements of the fabricated flexible device were measured using a B1500A Semiconductor Device Parameter Analyzer with PE4 Probe System.

2.5. Material Characterization. The infrared spectra were recorded using an Alpha Bruker FTIR spectrometer in the range of 400–1200 cm⁻¹. A Bruker Avance NEO NMR spectrophotometer (500 MHz) was used for the (¹H and ³¹P) NMR measurement in CDCl₃. RIGAKU Smartlab instrument (λ = 1.54) with a κα filter was used at room temperature to confirm the crystalline phases of samples. The Raman spectra were recorded using a Raman spectrometer (Bruker Multi-RAM) in the range of 120–1700 cm⁻¹ at the excitation wavelength of 514 nm. The absorbance and transmittance spectra were recorded from the 200–1000 nm range using UV–vis spectroscopy (Cary 5000, Varian, USA). The surface morphology and topology of the as-synthesized NiS₂ nanostructures were studied using a scanning electron microscope (SEM) (JEOL-JSM 6360A) and the atomic force microscopy (Asylum RESEARCH AFM MFP-3D) in tapping mode/AC mode and tapping frequency 300 kHz, respectively.

3. RESULTS AND DISCUSSION

3.1. Precursor Characterization. The purity and composition of the complex was confirmed from the IR, ¹H, and ³¹P{¹H} NMR spectra as shown in Figures S1–S3, respectively. The five important vibrational bands are observed in the IR spectra of the [Ni{S₂P(OPr)₂}]₂ complex (Figure

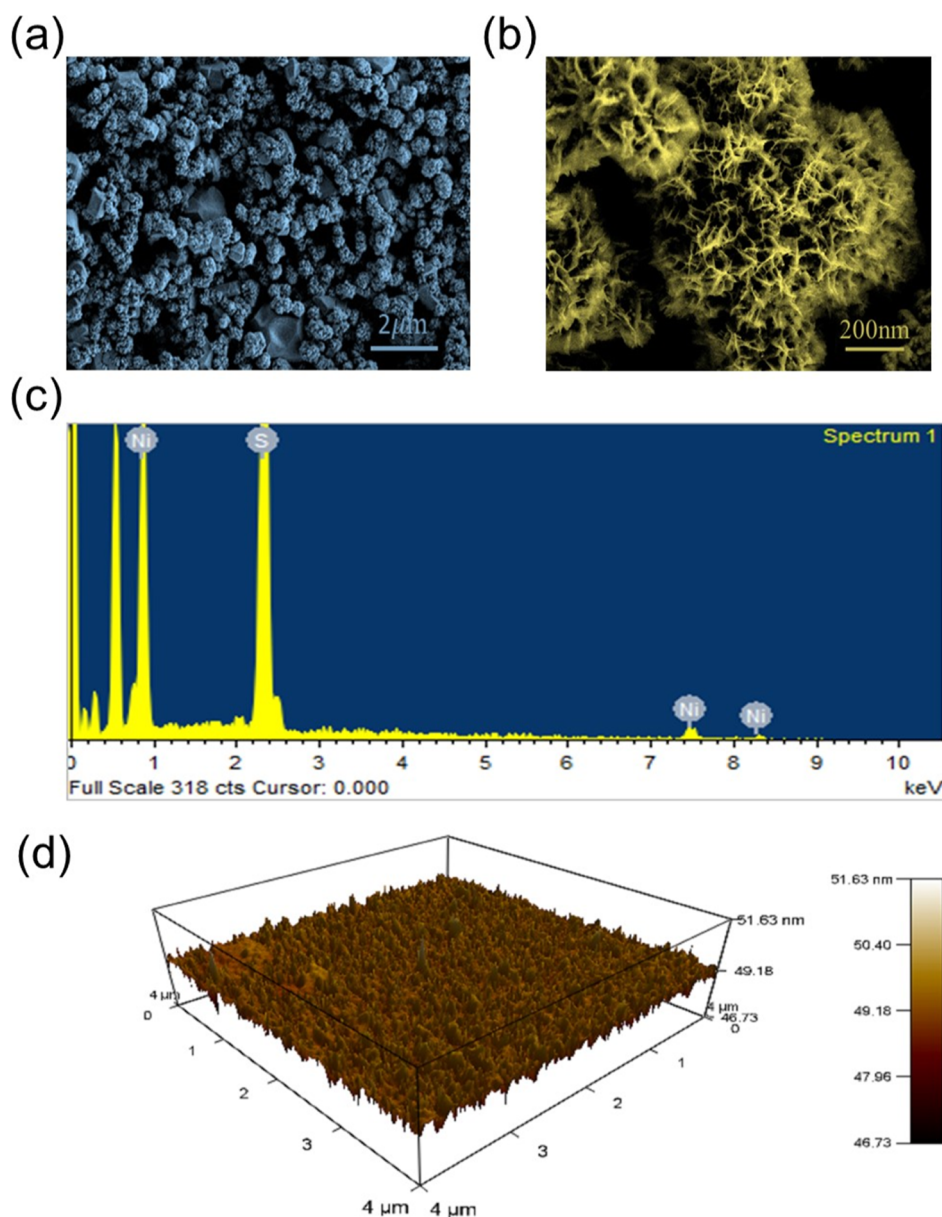


Figure 3. SEM images of NiS₂ nanoflowers with different magnifications such as (a) 2 μm, (b) 200 nm, and (c) EDS spectrum. (d) AFM image of the NiS₂/ITO thin film on a flexible substrate (PET).

S1). Two strong intensity bands observed at 983 and 856 cm⁻¹ correspond to the $\nu(\text{P})\text{-O-C}$ and $\nu \text{P-O-(C)}$ stretching vibrations. While the band at 752 and 642 cm⁻¹ corresponds to $\nu(\text{P-O})$ and $\nu(\text{P=S})$ vibrations and shows a slight shift toward lower wavenumber in comparison with [NH₄]-[(OC₃H₇)₂S₂P]. The Ni-S bond is observed in the far IR region at 547 cm⁻¹ which confirms the coordination of metal with *O,O'*-dipropyl dithiophosphates. The ¹H} (Figure S2) and ³¹P{¹H} (Figure S3) NMR spectra for the [Ni{S₂P-(OPr)₂}₂] complex were recorded in CDCl₃ at room temperature. The ¹H} spectrum is compared to the spectrum of ammonium salts of dithiophosphoric acids [NH₄]-[(OC₃H₇)₂S₂P]. In the complex, the -CH₃ protons observed as multiplets, resonating in the region of 1.00–4.19 ppm, show the coordination shift of 0.6 ppm associated with those of corresponding ammonium salts. The ³¹P{¹H} NMR spectra of the complex display a sharp singlet peak at 92.95 ppm, indicating the presence of a pure single form of phosphorus

and coordination of the dithiophosphate group with the nickel center in the molecule. The ³¹P{¹H} NMR chemical shift for this complex is 18 ppm upfield from that of the ammonium salts of dithiophosphoric acids.

3.2. Solution-Processed *In Situ* Growth of Nanostructured NiS₂ Thin Films. The NiS₂ thin film was deposited by a simple, solution-processable solvothermal route using single-source precursor [Ni{S₂P(OPr)₂}₂]. The commonly used dual source techniques with high deposition temperatures are not compatible with the thermal budget of most of the flexible substrates including PET. Therefore, the *in situ* solvothermal route based on SSPs serves as an alternative low-temperature solution process method. The amazing versatility of dithiophosphate complexes as SSPs stems from their ease of handling, high shelf life, and relative stability of metal complexes. Moreover, [Ni{S₂P(OPr)₂}₂] contains a preformed Ni-S bond which decomposes even at low temperatures in the solution phase,²⁸ and the ethylene glycol²⁹

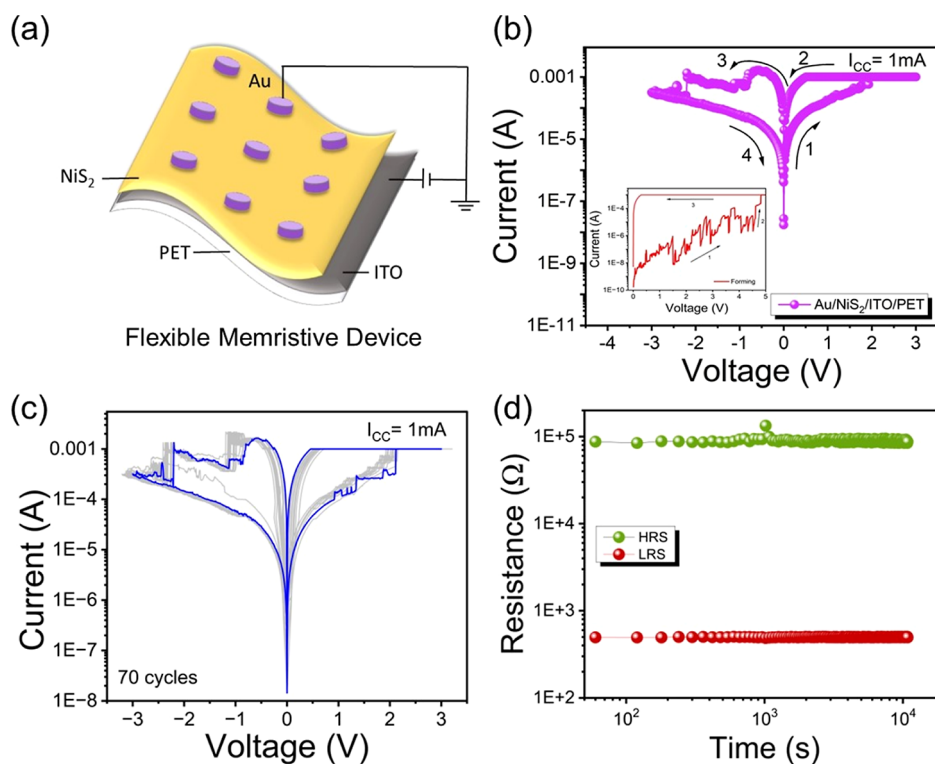


Figure 4. (a) Schematic model illustrating the flexible Au/NiS₂/ITO/PET device structure. (b) Bipolar I - V characteristics (inset electroforming process) of the Au/NiS₂/ITO/PET device. (c) Multiple I - V cycles. (d) Data retention characteristics of the flexible Au/NiS₂/ITO/PET memristor device.

as solvent controls the particle size and homogeneity of nanostructures resulting in the formation of low dimensional nickel disulfides. Thus, the novel solution-processed method enables a high-quality (pure, uniform, well-adhered) NiS₂ thin film to be deposited on a flexible substrate at a low temperature.

3.3. Structural and Morphological Studies of Nanostructured NiS₂ Thin Film.

3.3.1. X-ray Diffraction Studies. The X-ray diffraction (XRD) spectra of the as-synthesized NiS₂ thin films were recorded to study the crystallinity and phase structure. The sharp diffraction peaks observed in the XRD pattern (Figure 2a) indicate the crystalline nature of the as-synthesized NiS₂. All the diffraction peaks containing (111), (200), (210), (211), (220), (221), (311), (222), (302), (321), (410), (331), (402), and (421) faces in this pattern well matched with the JCPDS reference no. 01-078-4702 with lattice constants of $a = b = c = 5.66$ Å and can be indexed to the pure cubic structure of the NiS₂ phase. No additional peaks of any other form of nickel sulfide were observed indicating the purity of NiS₂ from single molecular precursors.³⁰ The average particle size was calculated using the Debye–Scherrer formula and is found to be around 54.71 nm.

3.3.2. Raman Spectral Studies. Raman spectra of the as-synthesized NiS₂ layer were studied to analyze the crystalline structure of the sample. Figure 2b shows the deconvoluted Raman spectra of the NiS₂ layer fitted using the Lorentzian function. The band at 270, 480, 513, and 1051 cm⁻¹ corresponds to the A_{1g} and E_g active modes of vibration of NiS₂.^{31,32} As the Raman modes of vibration strongly depend on the crystal lattice vibrations, some of the vibration modes of bulk NiS₂ are not clearly observed due to the reduction in size of NiS₂ nanostructures.³³ Thus, the Raman peaks confirm the formation of single-phase NiS₂.

3.3.3. UV–Vis Spectroscopic Studies. Using UV–vis spectroscopy, the optical characteristics of the as-synthesized NiS₂ thin layer were investigated, and the band gap was determined using the Tauc equation:³⁴ $(ah\nu)^n = B(h\nu - E_g)$, where E_g is the optical band gap, h is the Planck's constant, ν is the frequency of incident photon, α is absorption coefficient, B is a material constant, and n is 2 for direct transitions and 1/2 for indirect transitions. The optical absorption spectrum and Tauc plot for the band gap calculation (inset) are shown in Figure 2c. In comparison to bulk nickel sulfide, the as-synthesized NiS₂ layer absorbs maximum light in the UV region (200–350 nm). The observed blue shift can be attributed to the decrease in particle size and thus confirms the formation of nanosized nickel sulfide particles. This in turn increases the band gap of synthesized nanostructures.³⁵ The band gap of as-deposited NiS₂ thin layer was calculated as 3.87 eV. The linear relation is observed for $n = 2$ and, thus, the NiS₂ can be considered as a direct band gap semiconductor material. The transmittance spectrum of the NiS₂ layer on the flexible ITO/PET substrate was recorded in the range of 200–1000 cm⁻¹ and was found to be more than 50% as shown in Figure S4. Therefore, the spherical flower-like morphology with high surface area and porosity, outstanding electrical characteristics such as the wide band gap range, and high dielectric strength of the as-synthesized NiS₂ thin films make it suitable for RS.

3.3.4. SEM Studies. The SEM images are acquired to better understand the morphology of the sample. Figure 3a,b shows the in situ growth of NiS₂ on a flexible substrate using $[\text{Ni}\{\text{S}_2\text{P}(\text{OPr})_2\}_2]$ complex at low temperature, which confirms the spherical flower-like morphology and uniformity over the large surface area. In addition, the high magnification SEM image (Figure 3b) shows the high surface area and porous structure of the NiS₂ nanoflowers in the 50–100 nm

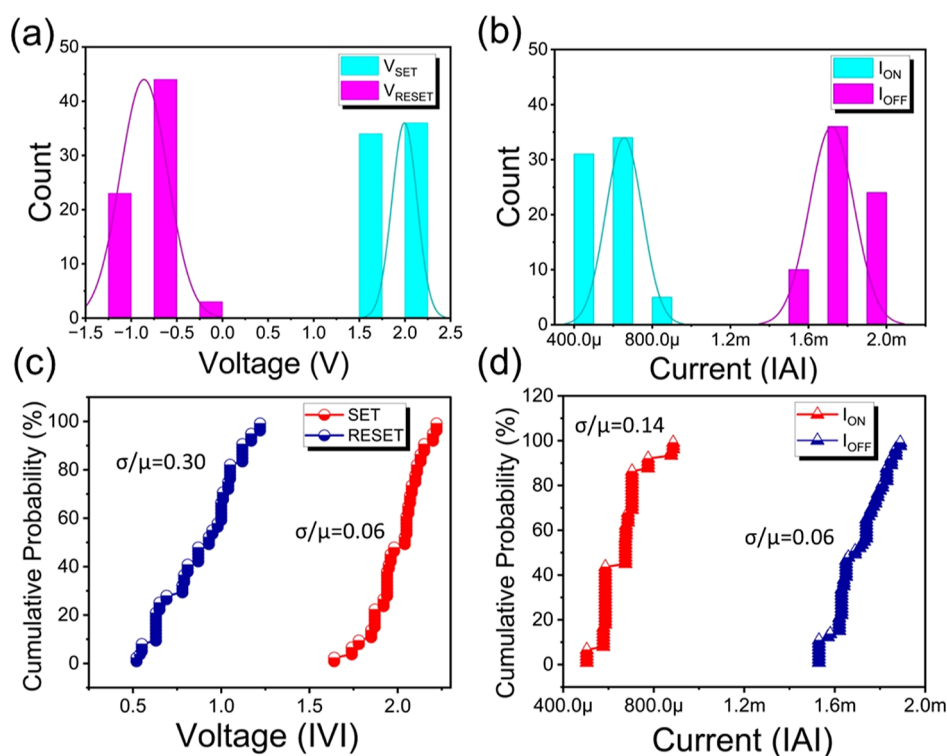


Figure 5. Statistical evaluation of the switching voltage and current of the flexible Au/NiS₂/ITO/PET memristor device. Histogram plots of (a) V_{SET} and V_{RESET} and (b) OFF and ON current distribution. Cumulative probability of (c) V_{SET} and V_{RESET} and (d) I (OFF) and I (ON).

size range. Furthermore, the EDS analysis of the sample was carried out to confirm the formation of pure NiS₂ from SSP. Figure 3c shows the EDS spectra of the NiS₂ nanostructures and the resulting peaks suggest the presence of nickel and sulfur with the atomic percentage of a 1:2 ratio which confirms the formation of NiS₂ via the hydrothermal route. Moreover, surface defects and roughness are the critical factors influencing the leakage current or unintended conduction pathways. Therefore, the as-deposited film texture was examined using an AFM. Figure 3d shows the AFM image of the NiS₂ thin film on the flexible substrate (PET). The demonstrated structure revealed a dense morphology and appeared as a uniform surface, which is consistent with the SEM data. The measured roughness is in the range of 49–53 nm for the selected area (4 × 4 μm).

3.4. Device Characterization. **3.4.1. Nanostructured NiS₂-Based Nonvolatile Flexible Memory Device.** The flexible device was fabricated in the form of a MIM structure with porous NiS₂ nanoflowers sandwiched between the Au top electrode and ITO-coated PET as the bottom electrode (Figure 4a). The electrical behavior and RS properties of the porous NiS₂ were studied from the current–voltage (I – V) characteristics. The semilogarithmic I – V plot of the Au/NiS₂/ITO/PET device shown in Figure 4b exhibited asymmetric, bipolar RS behavior, and the arrows indicate the switching direction. The device is initially subjected to a forming voltage of 5 V to cause a mild breakdown that can be regarded as electroforming. The I – V curves were then measured by sweeping the DC voltage from 0 V → +3 V → 0 V → –3 V → 0 V with the compliance current of 1 mA in the positive voltage region and 50 mA in the negative voltage region. The compliance current limits the operating current and consequently prevents the breakdown of the device.³⁶ On application of positive DC sweep from 0 to +3 V, the device

gradually switches from high to low resistance state (SET) at $V_{\text{SET}} \sim +2$ V and RESET's back from low to high resistance state at $V_{\text{RESET}} = -1.4$ V during the reversal of polarity. This change in state is due to the formation and rupture of the filament within the switching layer. Further, to test the repeatability of the device, the I – V curves were recorded for multiple switching cycles, and the cycle-to-cycle performance of the as-fabricated device was investigated. The device exhibited reproducible and uniform switching behavior over 70 consecutive cycles (Figure 4c). Figure S5 shows the DC endurance of the fabricated device, where the device maintains a memory window (HRS/LRS) of ~ 100 . The data retention performance of the Au/NiS₂/ITO/PET device was studied by measuring the resistance states repeatedly (R_{ON} and R_{OFF}) for a longer duration of time at a read voltage of 0.4 V. Appreciably, the NiS₂-based device can retain data over 1×10^4 s without any interruption (Figure 4d).

Further, to evaluate the reliability and uniformity of our memristive device, the memory performance properties, including the histogram distribution (Figure 5a,b) and cumulative probability plots (Figure 5c,d), were studied for 70 consecutive switching cycles. Both curves depict the excellent uniformity of the switching performance with differentiating SET–RESET voltages and ON–OFF currents. To provide a quantitative comparison of all switching parameters, the coefficient of variation (σ/μ , σ is standard deviation, and μ is the mean value) was determined and is observed to be 0.06 and 0.30 for V_{SET} and V_{RESET} , respectively. Moreover, the coefficient of variation for LRS and HRS was also determined and is measured to be 0.14 and 0.06, respectively. These results reveal the narrow distribution of the operational voltage and current and demonstrate the excellent cycle-to-cycle uniformity of the device.

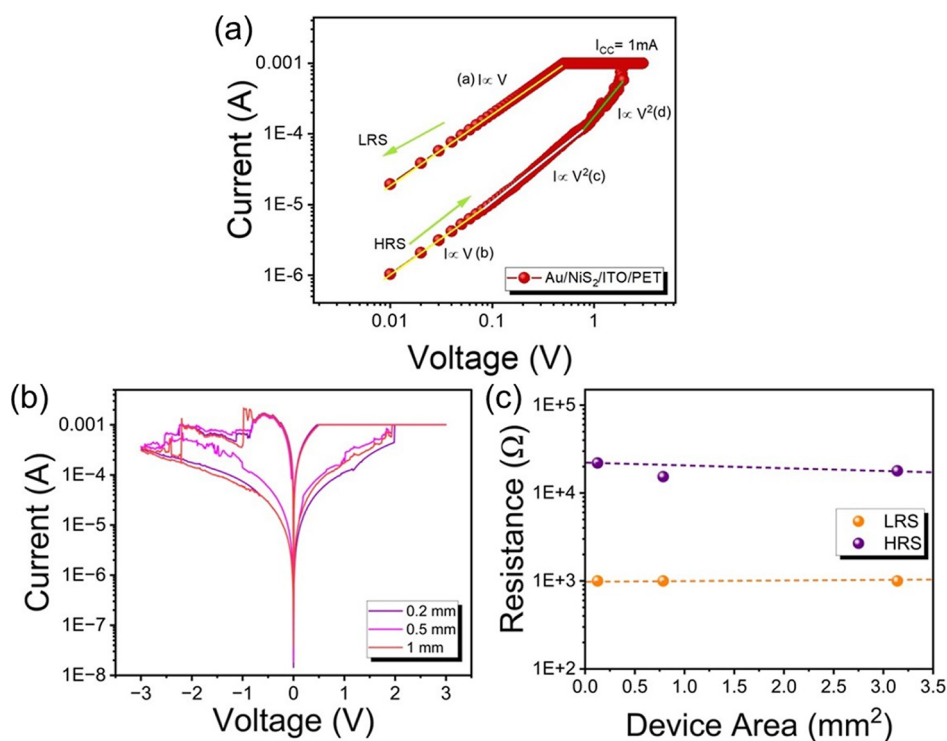


Figure 6. (a) Double logarithmic I – V plot showing charge transport fitting models. (b) Bipolar I – V switching characteristics with varied sizes of top electrode (radius—0.2, 0.5, 1 mm) and (c) HRS and LRS of the flexible Au/NiS₂/ITO/PET memristor as the function of device area.

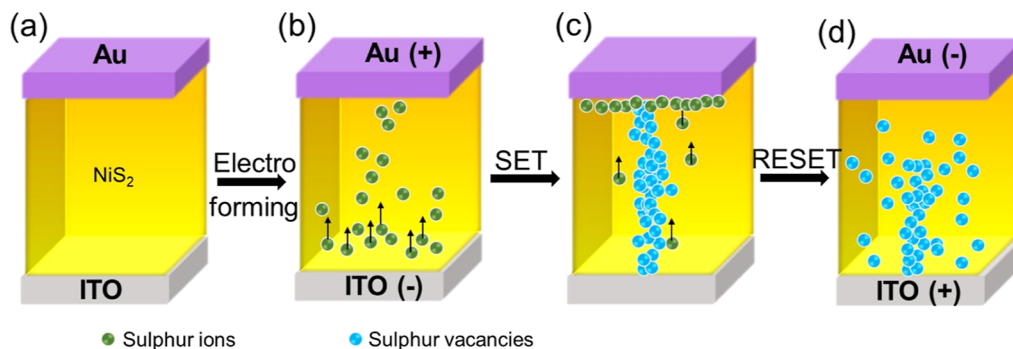


Figure 7. Schematic illustration of switching mechanism of flexible Au/NiS₂/ITO/PET memory device. (a) Pristine state, (b) electroforming process, (c) SET process, and (d) RESET process.

To investigate the conduction mechanism in the Au/NiS₂/ITO/PET memristor, the I – V plot was converted to a double-logarithmic scale, as shown in Figure 6a (elaborated fitting graphs for different regions are mentioned in Figure S6). The conduction mechanism is studied by considering four regions. In the LRS (0–0.5 V), the fitting results indicate the dominance of the Ohmic conduction mechanism where the current is linearly proportional to the applied voltage (region a) and suggest the formation of conductive filament within the switching layer during the SET process. In the case of HRS, the Ohmic conduction is found to be dominated at a low voltage region ($I \propto V$) (region b). This can be attributed to the thermally generated charge carriers taking part in the conduction at a low-applied field.³⁷ At the mid- and high-voltage regions, the current shows voltage square dependence, hence following Child's square law ($I \propto V^2$) (regions c and d), thus indicating the governance of the space-charge-limited-current (SCLC) conduction model, which is widely observed in most of the low-dimensional materials.³⁸

To further elucidate the switching mechanism, we have conducted the top electrode-area-dependent study of our flexible memristor. Figure 6b illustrates the bipolar I – V characteristics of the dot patterned devices with varying areas (radius 0.2, 0.5- and 1 mm). It is worth noting that the devices exhibit similar kinds of I – V curves for each top electrode. Moreover, Figure 6c presents a plot of HRS and LRS with respect to the device area. It is evident that the LRS is independent of the device area and HRS decreases with respect to the increase in area. The remarkable area independence within LRS confirms the filamentary-based switching behavior of the typical ReRAM.³⁹

3.4.2. Conduction Mechanism. The RS mechanism of Au/NiS₂/ITO/PET is consistent with the above-mentioned fitting models and the electrical properties of the device. Figure 7a shows the pristine state of the fabricated Au/NiS₂/ITO/PET device. It has been reported that there are numerous sulfur vacancies (V_s) in TMDCs.^{40,41} Initially, the high positive biasing greater than the V_{SET} is applied to the device, known as

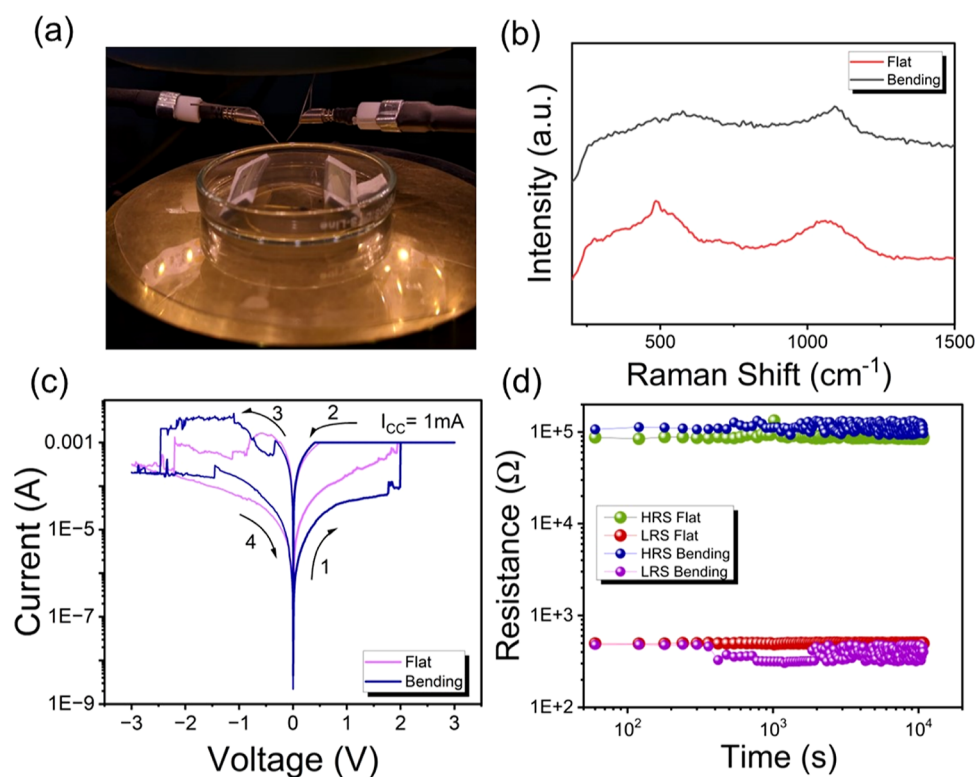


Figure 8. (a) Photograph of flexible Au/NiS₂/ITO/PET for bending test ($R = 20$ mm). (b) Raman spectra of a flexible device during flat and bending tests. (c) Semilog I - V characteristics and (d) retention performance of the flexible device prior to bending and during bending.

Table 1. TMDC-Based Memristor Devices for Nonvolatile Memory Application

device configuration	fabrication route	switching voltage (V)	retention (s)	flexible	refs
Au/NiS ₂ /ITO/PET	single-step hydrothermal	±2	10 ⁴	yes	this work
Al/MoS ₂ -GO/Al	spin coat	+0.3 and -1	10 ⁴	-	47
Al/MoSe ₂ @PMMA/ITO	hydrothermal and spin coat	+1 and -3	10 ³	-	48
Al/WS ₂ /Pt/Ti/SiO ₂ /Si	CVD	±2	10 ³	-	49
Ag/MoS ₂ /Ag	CVD	+0.8 and -0.6	10 ⁵	-	41
Ag/WO _{3-x} /WSe ₂ /graphene	mechanical exfoliation	±3	10 ³	-	50
Ti/Ni/MoTe ₂ /Ti/Au	mechanical exfoliation	±3	10 ²	-	51
Pt/WSe ₂ /Pt	wet transfer method	±2	10 ⁴	-	52
Ti/MoS ₂ /Pt	vacuum filtration and transfer	±0.2	10 ⁴	-	44
Ag/MoS ₂ /Au	exfoliation	±1	10 ⁴	yes	42

the forming voltage which causes the dielectric breakdown in the NiS₂ thin films. During this forming process with positive biasing of the top electrode, the sulfur atoms ionize to S²⁻ ions at the bottom electrode (Figure 7b). The sulfur ions behave as movable anionic species⁴² similar to oxygen ion species.⁴³ Further, when a positive voltage (+3 V) is applied to the top electrode, the S²⁻ ions transfer and accumulate at the top electrode and react with Au to form a conductive layer at the Au/NiS₂ interface. The escaping sulfur ions leave a large number of sulfur vacancies (V_s) behind, eventually forming a conduction filament (CF) connecting the top and bottom electrodes and causing filament formation (Figure 7c).⁴⁴ The filament formation switches the device to the LRS, i.e., the SET process. Further, reversing the polarity (-3 V) causes the rupturing of filament and the device switches to an HRS, i.e., RESET process (Figure 7d). The device switches between the ON and OFF states.

3.4.3. Bending Test. To confirm the mechanical feasibility of our device for flexible nonvolatile memory applications and

wearable electronics, the device was subjected to a bending radius (R) of 20 mm. The switching performance and Raman stretching modes during flat and bending conditions are shown in Figure 8a,b, respectively. The applied strain was calculated using the below equation⁴⁵

$$\text{strain}(\%) = \frac{(\text{total thickness of device})}{(2 \times \text{radius of curvature})} \times 100 \quad (1)$$

where the total thickness of the device is 208.4 μm, consisting of the top electrode (100 nm), NiS₂ layer thickness (8.01 μm), ITO layer (200 nm), and substrate (200 μm), and radius of curvature is 20 mm. The applied strain to the substrate was 0.52%. During bending, the broadening of bands at lower frequencies (i.e., 480 and 513 cm⁻¹) takes place and the vibration frequency decreases which can be attributed to the increase in bond length. Thus, the bending condition causes a slight change in A_{1g} stretching modes leading to broadening and shifting of peak toward the lower frequency values.⁴⁶ The device shows bipolar RS during the bending test with reliable

SET and RESET processes at a low operating voltage (Figure 8c). The similar compliance currents of 1 and 50 mA (positive and negative voltage region, respectively) were set to avoid the breakdown of the device. Interestingly, the device exhibited reliable performance under bending conditions with a shift in the ON current toward a higher level. This can be attributed to the polysulfide filament formation maintaining the flexibility within the thin NiS₂ layer.³⁷ Moreover, the effect is reversible, and the ON current returns to its original position after bending (Figure S7). The retention test of the flexible device was studied to evaluate the stability and reliability of the device under strain. The HRS and LRS were consistent over 10⁴ s without degradation under bending conditions indicating the nonvolatility and better performance of the fabricated flexible device (Figure 8d).

Table 1 shows the comparative study of the RS performance of our flexible Au/NiS₂/ITO/PET device with other reported metal-sulfide-based memory devices. It has been observed that the study is limited to the usage of MoS₂, MoSe₂, WS₂, and WSe₂ as the switching layer on the rigid substrates. Notably, our device demonstrates single-step and scalable device fabrication along with competitive switching performance onto the flexible substrate. Moreover, the easy stoichiometric modulations of the novel precursors enable eco-friendly, low-temperature processing and high throughput production, making it compatible with the thermal budget of the flexible substrates. This prevents the deformation of polymer substrates and paves the way for future large-scale manufacturing of metal-chalcogenide-based thin films, beneficial for the development of flexible and wearable electronics.

4. CONCLUSIONS

In summary, we have successfully demonstrated the simple, solution-processable solvothermal route for *in situ* growth of the nanostructured NiS₂ thin film on a flexible substrate via SSP. This novel low-temperature route negates the key thermal budget requirement for most of the flexible substrates, providing a low-cost, scalable, industrially compatible processing. The as-deposited NiS₂ nanoflowers (~50–100 nm) with dense morphology, high surface area, and negligible surface roughness serve as a suitable platform for RS memory onto a flexible substrate. The fabricated Au/NiS₂/ITO/PET device revealed reliable and reproducible bipolar RS behavior at low operating voltage. Furthermore, the device exhibits data retention up to 10⁴ s and a uniform cycle-to-cycle performance over 70 continuous cycles. The detailed study of the conduction mechanism reveals ohmic and SCLC-based device conduction. The switching mechanism is attributed to the formation and rupture of filament based on the migration of sulfur ions and vacancies within the porous structure of the active layer. Moreover, the Au/NiS₂/ITO/PET device successfully demonstrated reliable RS under the applied strain of 0.52% with good mechanical stability and retention properties with well-resolved switching states and a memory window (~100). The cost-effective and scalable nickel disulfide-based flexible device could be a potential candidate for future wearable electronics and next-generation neuro-morphic computing applications.

■ ASSOCIATED CONTENT

SI Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/10.1021/acsomega.3c06331>.

Spectroscopic details (FTIR, ¹H) and ³¹P{¹H} NMR) of [Ni{S₂P(OPr)₂}]₂ precursor. Additional details of flexible Au/NiS₂/ITO/PET device: transmittance spectra, DC endurance studies, charge transport fitting models over different voltage ranges, and bending reversibility test (PDF)

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Notes

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■ ABBREVIATIONS

SSP, single-source precursor; PET, polyethylene terephthalate; ITO, indium tin oxide; 2D, two dimensional; RS, resistive switching; HRS, high resistance state; LRS, low resistance state; TE, top electrode; BE, bottom electrode; DC, direct current; SCLC, space-charge-limited-current; CF, conduction filament

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